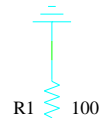


U1

BANK0
XC6SLX150-3FGG676

IO_L1P_HSWAPEN_0	A3	IOR_LVDS33_11_P	
IO_L1N_VREF_0	A2	IOR_LVDS33_11_N	
IO_L2P_0	B4	IOR_LVDS33_27_P	
IO_L2N_0	A4	IOR_LVDS33_27_N	
IO_L3P_0	E6	IOR_LVDS33_117_P	
IO_L3N_0	D5	IOR_LVDS33_117_N	
IO_L4P_0	C5	IOR_LVDS33_43_P	
IO_L4N_0	A5	IOR_LVDS33_43_N	
IO_L5P_0	G8	IOR_LVDS33_133_P	
IO_L5N_0	F7	IOR_LVDS33_133_N	
IO_L6P_0	B6	IOR_LVDS33_10_P	
IO_L6N_0	A6	IOR_LVDS33_10_N	
IO_L7P_0	G9	IOR_EXT_LVDS33_2_P	NC
IO_L7N_0	F8	IOR_EXT_LVDS33_2_N	NC
IO_L8P_0	D6	IOR_LVDS33_91_P	
IO_L8N_VREF_0	C6	IOR_LVDS33_91_N	
IO_L9P_0	K12	IOR_EXT_LVDS33_6_P	NC
IO_L9N_0	J11	IOR_EXT_LVDS33_6_N	NC
IO_L11P_0	H10	IOR_EXT_LVDS33_4_P	NC
IO_L11N_0	H9	IOR_EXT_LVDS33_4_N	NC
IO_L12P_0	C7	IOR_LVDS33_26_P	
IO_L12N_0	A7	IOR_LVDS33_26_N	
IO_L13P_0	E8	IOR_LVDS33_58_P	
IO_L13N_0	D7	IOR_LVDS33_58_N	
IO_L14P_0	D8	IOR_LVDS33_74_P	
IO_L14N_0	C8	IOR_LVDS33_74_N	
IO_L15P_0	F9	IOR_LVDS33_100_P	
IO_L15N_0	E9	IOR_LVDS33_100_N	
IO_L16P_0	B8	IOR_LVDS33_42_P	
IO_L16N_0	A8	IOR_LVDS33_42_N	
IO_L17P_0	C9	IOR_LVDS33_9_P	
IO_L17N_0	A9	IOR_LVDS33_9_N	
IO_L18P_0	E10	IOR_LVDS33_90_P	
IO_L18N_0	F10	IOR_LVDS33_90_N	
IO_L19P_0	D10	IOR_LVDS33_57_P	
IO_L19N_0	C10	IOR_LVDS33_57_N	
IO_L21P_0	J12	IOR_EXT_LVDS33_5_P	NC
IO_L21N_0	H13	IOR_EXT_LVDS33_5_N	NC
IO_L22P_0	B10	IOR_LVDS33_25_P	
IO_L22N_0	A10	IOR_LVDS33_25_N	
IO_L23P_0	D11	IOR_LVDS33_89_P	
IO_L23N_0	F11	IOR_LVDS33_89_N	
IO_L24P_0	C11	IOR_LVDS33_41_P	
IO_L24N_0	A11	IOR_LVDS33_41_N	
IO_L25P_0	H12	IOR_EXT_LVDS33_3_P	NC
IO_L25N_0	G11	IOR_EXT_LVDS33_3_N	NC
IO_L26P_0	B12	IOR_LVDS33_8_P	
IO_L26N_0	A12	IOR_LVDS33_8_N	
IO_L30P_0	F12	IOR_EXT_LVDS33_0_P	NC
IO_L30N_0	E12	IOR_EXT_LVDS33_0_N	NC
IO_L31P_0	D12	IOR_LVDS33_72_P	
IO_L31N_0	C12	IOR_LVDS33_72_N	
IO_L34P_GCLK19_0	C13	IOR_LVDS33_24_P	
IO_L34N_GCLK18_0	A13	IOR_LVDS33_24_N	
IO_L35P_GCLK17_0	B14	IOR_LVDS33_40_P	
IO_L35N_GCLK16_0	A14	IOR_LVDS33_40_N	
IO_L36P_GCLK15_0	D14	IOR_LVDS33_55_P	
IO_L36N_GCLK14_0	C14	IOR_LVDS33_55_N	
IO_L37P_GCLK13_0	C15	IOR_LVDS33_7_P	
IO_L37N_GCLK12_0	A15	IOR_LVDS33_7_N	



U1

BANK0
XC6SLX150-3FGG676

IO_L32P_0	G13	IOR_EXT_LVDS33_1_P	NC
IO_L32N_0	F14	IOR_EXT_LVDS33_1_N	NC
IO_L33P_0	F13	IOR_LVDS33_132_P	
IO_L33N_0	D13	IOR_LVDS33_132_N	
IO_L38P_0	B16	IOR_LVDS33_23_P	
IO_L38N_VREF_0	A16	IOR_LVDS33_23_N	
IO_L43P_0	J14	IOR_LVDS33_99_P	
IO_L43N_0	G14	IOR_LVDS33_99_N	
IO_L44P_0	E14	IOR_LVDS33_87_P	
IO_L44N_0	D15	IOR_LVDS33_87_N	
IO_L45P_0	J13	IOR_EXT_LVDS33_7_P	NC
IO_L45N_0	K14	IOR_EXT_LVDS33_7_N	NC
IO_L46P_0	D16	IOR_LVDS33_54_P	
IO_L46N_0	C16	IOR_LVDS33_54_N	
IO_L47P_0	G16	IOR_LVDS33_70_P	
IO_L47N_0	F15	IOR_LVDS33_70_N	
IO_L48P_0	F17	IOR_LVDS33_71_P	
IO_L48N_0	E17	IOR_LVDS33_71_N	
IO_L49P_0	F16	IOR_LVDS33_88_P	
IO_L49N_0	E16	IOR_LVDS33_88_N	
IO_L50P_0	C17	IOR_LVDS33_39_P	
IO_L50N_0	A17	IOR_LVDS33_39_N	
IO_L51P_0	J15	IOR_LVDS33_56_P	
IO_L51N_0	H15	IOR_LVDS33_56_N	
IO_L52P_0	D18	IOR_LVDS33_86_P	
IO_L52N_0	C18	IOR_LVDS33_86_N	
IO_L53P_0	K15	IOR_EXT_LVDS33_8_P	NC
IO_L53N_0	J16	IOR_EXT_LVDS33_8_N	NC
IO_L54P_0	E19	IOR_LVDS33_69_P	
IO_L54N_0	D19	IOR_LVDS33_69_N	
IO_L55P_0	H16	IOR_LVDS33_115_P	
IO_L55N_0	G17	IOR_LVDS33_115_N	
IO_L56P_0	D21	IOR_LVDS33_68_P	
IO_L56N_0	C20	IOR_LVDS33_68_N	
IO_L57P_0	F18	IOR_LVDS33_131_P	
IO_L57N_0	E18	IOR_LVDS33_131_N	
IO_L58P_0	E20	IOR_LVDS33_52_P	
IO_L58N_0	D20	IOR_LVDS33_52_N	
IO_L59P_0	J17	IOR_LVDS33_138_P	
IO_L59N_0	H17	IOR_LVDS33_138_N	
IO_L62P_0	B18	IOR_LVDS33_6_P	
IO_L62N_VREF_0	A18	IOR_LVDS33_6_N	
IO_L63P_SCP7_0	C19	IOR_LVDS33_22_P	
IO_L63N_SCP6_0	A19	IOR_LVDS33_22_N	
IO_L64P_SCP5_0	B20	IOR_LVDS33_38_P	
IO_L64N_SCP4_0	A20	IOR_LVDS33_38_N	
IO_L65P_SCP3_0	C21	IOR_LVDS33_5_P	
IO_L65N_SCP2_0	A21	IOR_LVDS33_5_N	
IO_L66P_SCP1_0	B22	IOR_LVDS33_21_P	
IO_L66N_SCP0_0	A22	IOR_LVDS33_21_N	

Signals with net name of IOR_EXT** are only available for SLX150

TITLE: Adapter	
Bank0 of U1, VCCO: 3.3V	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 2 of 28	Drawn By Shiming Yang

U1

BANK1
XC6SLX150-3FGG676

IO_L28P_1	L19	IO25_1_P	
IO_L28N_VREF_1	K19	IO25_1_N	
IO_L29P_A23_M1A13_1	L23	FMC_HPC_HA00_CC_P	
IO_L29N_A22_M1A14_1	L24	FMC_HPC_HA00_CC_N	
IO_L30P_A21_M1RESET_1	P20	IOR_LVDS25_4_P	
IO_L30N_A20_M1A11_1	N21	IOR_LVDS25_4_N	
IO_L31P_A19_M1CKE_1	M23	FMC_HPC_HA05_P	
IO_L31N_A18_M1A12_1	N24	FMC_HPC_HA05_N	
IO_L32P_A17_M1A8_1	L17	IO25_0_P	
IO_L32N_A16_M1A9_1	K18	IO25_0_N	
IO_L33P_A15_M1A10_1	P24	IOR_LVDS25_22_NC_P	NC
IO_L33N_A14_M1A4_1	P26	IOR_LVDS25_22_NC_N	NC
IO_L34P_A13_M1WE_1	M19	FMC_HPC_HA09_P	
IO_L34N_A12_M1BA2_1	L18	FMC_HPC_HA09_N	
IO_L35P_A11_M1A7_1	R25	FMC_HPC_HA01_CC_P	
IO_L35N_A10_M1A2_1	R26	FMC_HPC_HA01_CC_N	
IO_L36P_A9_M1BA0_1	M18	IO25_2_P	
IO_L36N_A8_M1BA1_1	N19	IO25_2_N	
IO_L37P_A7_M1A0_1	N22	FMC_HPC_HA13_P	
IO_L37N_A6_M1A1_1	N23	FMC_HPC_HA13_N	
IO_L38P_A5_M1CLK_1	N17	FMC_HPC_LA11_P	
IO_L38N_A4_M1CLKN_1	N18	FMC_HPC_LA11_N	
IO_L39P_M1A3_1	R23	FMC_HPC_HA04_P	
IO_L39N_M1ODT_1	R24	FMC_HPC_HA04_N	
IO_L40P_GCLK11_M1A5_1	N20	FMC_HPC_LA06_P	
IO_L40N_GCLK10_M1A6_1	M21	FMC_HPC_LA06_N	
IO_L41P_GCLK9_IRDY1_M1RASN_1	P21	FMC_HPC_LA02_P	
IO_L41N_GCLK8_M1CASN_1	P22	FMC_HPC_LA02_N	
IO_L42P_GCLK7_M1UDM_1	V23	FMC_HPC_HA08_P	
IO_L42N_GCLK6_TRDY1_M1LDM_1	W24	FMC_HPC_HA08_N	
IO_L43P_GCLK5_M1DQ4_1	U25	FMC_HPC_LA16_P	
IO_L43N_GCLK4_M1DQ5_1	U26	FMC_HPC_LA16_N	
IO_L44P_A3_M1DQ6_1	W25	FMC_HPC_HA15_P	
IO_L44N_A2_M1DQ7_1	W26	FMC_HPC_HA15_N	
IO_L45P_A1_M1LDQS_1	V24	FMC_HPC_CLK0_M2C_P	
IO_L45N_A0_M1LDQSN_1	V26	FMC_HPC_CLK0_M2C_N	
IO_L46P_FCS_B_M1DQ2_1	T24	FMC_HPC_CLK1_M2C_P	
IO_L46N_FCS_B_M1DQ3_1	T26	FMC_HPC_CLK1_M2C_N	
IO_L47P_FWE_B_M1DQ0_1	Y24	FMC_HPC_LA00_CC_P	
IO_L47N_LDC_M1DQ1_1	Y26	FMC_HPC_LA00_CC_N	
IO_L48P_HDC_M1DQ8_1	AD24	FMC_HPC_HA07_P	
IO_L48N_M1DQ9_1	AD26	FMC_HPC_HA07_N	
IO_L49P_M1DQ10_1	AB24	FMC_HPC_HA02_P	
IO_L49N_M1DQ11_1	AB26	FMC_HPC_HA02_N	
IO_L50P_M1UDQS_1	AC25	FMC_HPC_LA01_CC_P	
IO_L50N_M1UDQSN_1	AC26	FMC_HPC_LA01_CC_N	
IO_L51P_M1DQ12_1	AA25	FMC_HPC_HA03_P	
IO_L51N_M1DQ13_1	AA26	FMC_HPC_HA03_N	
IO_L52P_M1DQ14_1	AE25	FMC_HPC_HA06_P	
IO_L52N_M1DQ15_1	AE26	FMC_HPC_HA06_N	

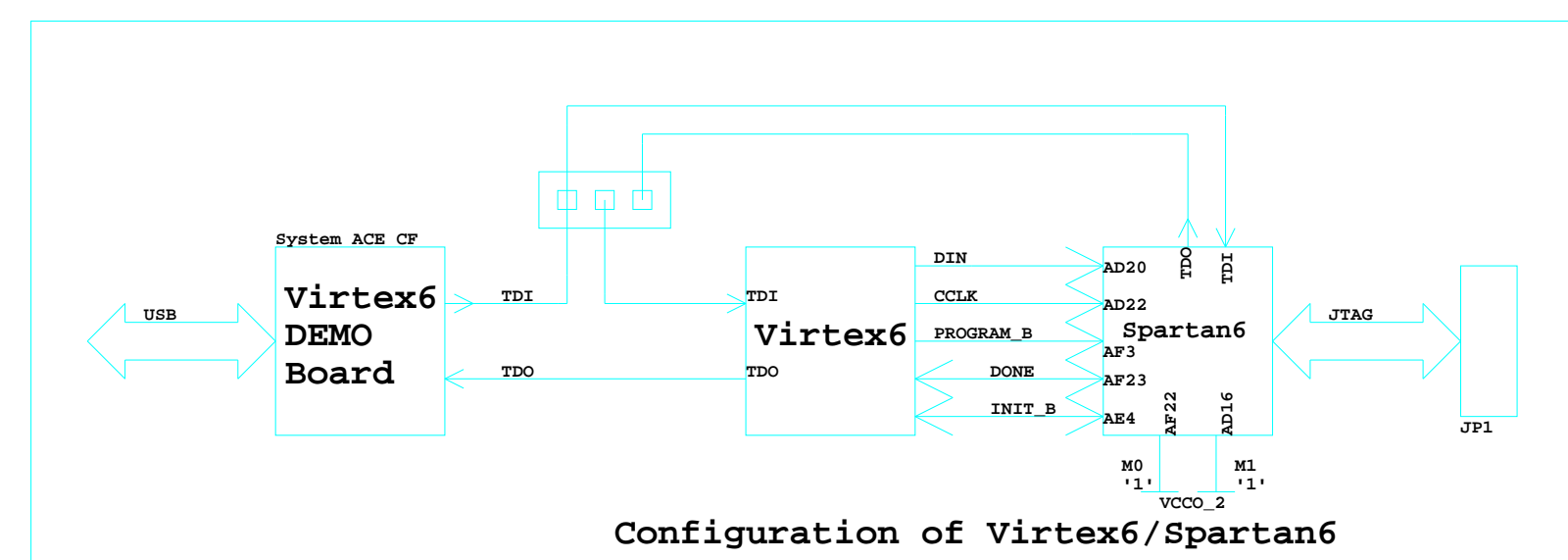
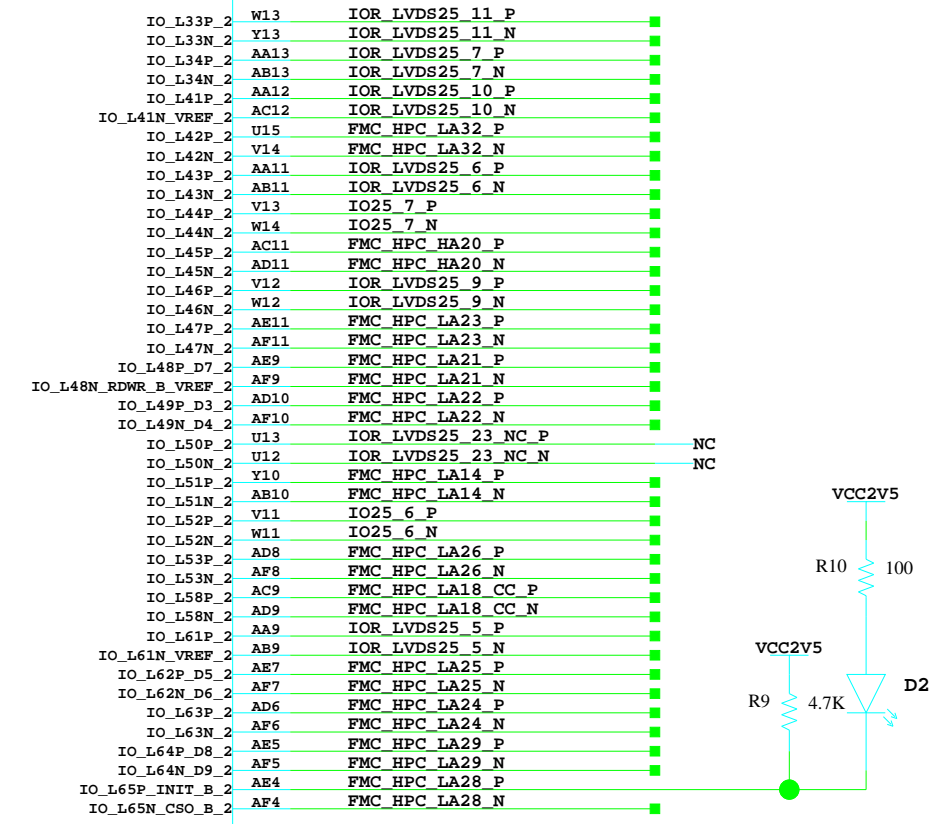
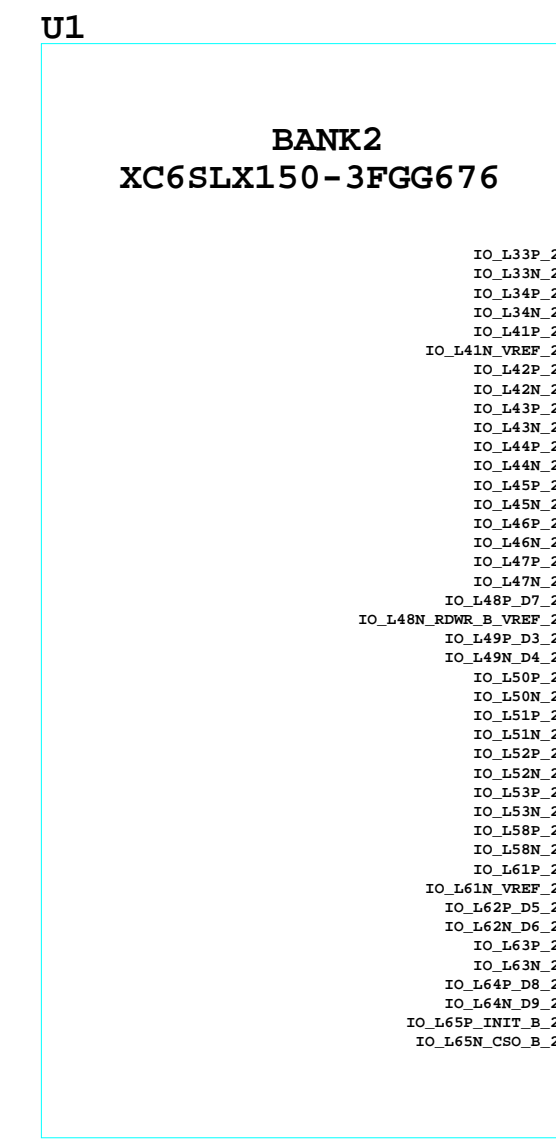
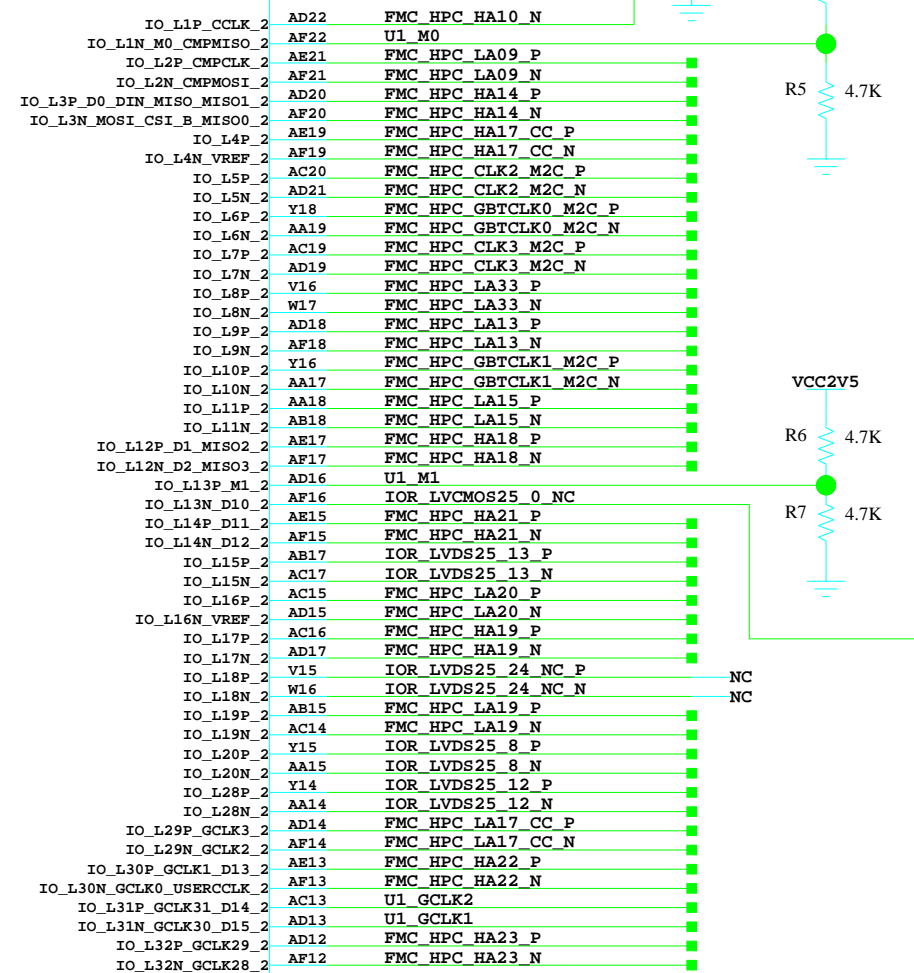
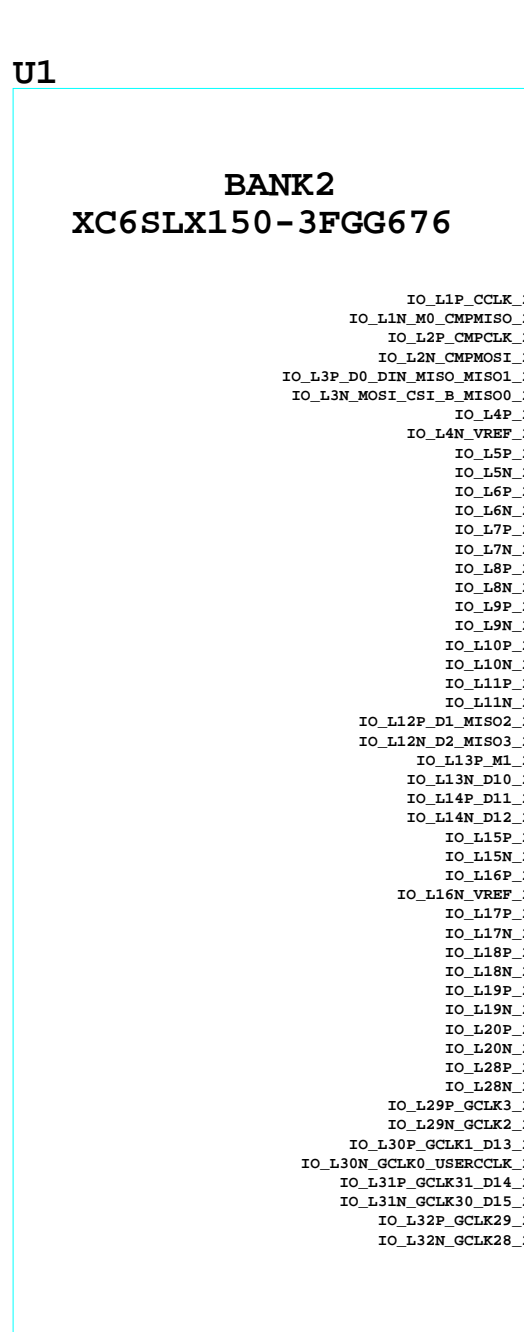
U1

BANK1
XC6SLX150-3FGG676

IO_L53P_1	T23	FMC_HPC_LA03_P	
IO_L53N_VREF_1	U24	FMC_HPC_LA03_N	
IO_L55P_1	R22	FMC_HPC_LA10_P	
IO_L55N_1	R21	FMC_HPC_LA10_N	
IO_L56P_1	P17	FMC_HPC_LA27_P	
IO_L56N_1	P18	FMC_HPC_LA27_N	
IO_L57P_1	R20	IOR_LVDS25_3_P	
IO_L57N_1	R19	IOR_LVDS25_3_N	
IO_L58P_1	R17	IO25_3_P	
IO_L58N_1	R18	IO25_3_N	
IO_L59P_1	T22	FMC_HPC_HA16_P	
IO_L59N_1	U23	FMC_HPC_HA16_N	
IO_L60P_1	T18	IOR_LVDS25_0_P	
IO_L60N_1	T19	IOR_LVDS25_0_N	
IO_L61P_1	U21	FMC_HPC_LA04_P	
IO_L61N_1	U22	FMC_HPC_LA04_N	
IO_L62P_1	U17	FMC_HPC_LA31_P	
IO_L62N_1	V17	FMC_HPC_LA31_N	
IO_L63P_1	AA23	FMC_HPC_LA08_P	
IO_L63N_1	AA24	FMC_HPC_LA08_N	
IO_L64P_1	T20	IOR_LVDS25_2_P	
IO_L64N_1	U20	IOR_LVDS25_2_N	
IO_L65P_1	AC23	FMC_HPC_HA12_P	
IO_L65N_1	AC24	FMC_HPC_HA12_N	
IO_L66P_1	V18	IO25_4_P	
IO_L66N_1	V19	IO25_4_N	
IO_L67P_1	AE24	FMC_HPC_LA05_P	
IO_L67N_1	AF25	FMC_HPC_LA05_N	
IO_L68P_1	W18	IOR_LVDS25_25_NC_P	NC
IO_L68N_1	W19	IOR_LVDS25_25_NC_N	NC
IO_L69P_1	AB21	FMC_HPC_LA30_N	
IO_L69N_VREF_1	AB22	FMC_HPC_LA30_P	
IO_L70P_1	U19	IO25_5_P	
IO_L70N_1	V20	IO25_5_N	
IO_L71P_1	V22	IOR_LVDS25_1_P	
IO_L71N_1	W22	IOR_LVDS25_1_N	
IO_L72P_1	Y20	FMC_HPC_LA12_P	
IO_L72N_1	Y21	FMC_HPC_LA12_N	
IO_L73P_1	Y22	FMC_HPC_LA07_P	
IO_L73N_1	AA22	FMC_HPC_LA07_N	
IO_L74P_AWAKE_1	AE23	FMC_HPC_HA11_P	
IO_L74N_DOUT_BUSY_1	AF24	FMC_HPC_HA11_N	

TITLE: Adapter
Bank1 of U1, VCCO: 2.5V

Date:	30-03-2011_16:34	DWG:	Adapter/Termination Board
Sheet Size:	B	REV:	1.0
Sheet	3 of 28	Drawn By	Shiming Yang



TITLE: Adapter	
Bank2 of U1, VCCO: 2.5V	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 4 of 28	Drawn By Shiming Yang

U1

BANK3
XC6SLX150-3FGG676

IO_L1P_3	AC7	IOR LVDS33 65 P
IO_L1N_VREF_3	AD7	IOR LVDS33 65 N
IO_L2P_3	AE3	IOR LVDS33 114 P
IO_L2N_3	AF2	IOR LVDS33 114 N
IO_L3P_3	AC4	IOR LVDS33 64 P
IO_L3N_3	AD4	IOR LVDS33 64 N
IO_L4P_3	AA8	IOR LVDS33 121 P
IO_L4N_3	AB8	IOR LVDS33 121 N
IO_L7P_3	AA7	IOR LVDS33 120 P
IO_L7N_3	Y6	IOR LVDS33 120 N
IO_L8P_3	AB7	IOR LVDS33 137 P
IO_L8N_3	AB6	IOR LVDS33 137 N
IO_L9P_3	Y9	IOR LVDS33 81 P
IO_L9N_3	Y8	IOR LVDS33 81 N
IO_L10P_3	AC5	IOR LVDS33 97 P
IO_L10N_3	AD5	IOR LVDS33 97 N
IO_L15P_3	W8	IOR LVDS33 143 P
IO_L15N_3	W7	IOR LVDS33 143 N
IO_L16P_3	AA5	IOR LVDS33 104 P
IO_L16N_3	AB5	IOR LVDS33 104 N
IO_L17P_3	V7	IOR LVDS33 126 P
IO_L17N_VREF_3	V6	IOR LVDS33 126 N
IO_L18P_3	AB4	IOR LVDS33 80 P
IO_L18N_3	AC3	IOR LVDS33 80 N
IO_L19P_3	W9	IOR LVDS33 63 P
IO_L19N_3	V8	IOR LVDS33 63 N
IO_L20P_3	AA4	IOR LVDS33 96 P
IO_L20N_3	AA3	IOR LVDS33 96 N
IO_L21P_3	W10	IOR LVDS33 127 P
IO_L21N_3	V10	IOR LVDS33 127 N
IO_L22P_3	W5	IOR LVDS33 110 P
IO_L22N_3	Y5	IOR LVDS33 110 N
IO_L23P_3	U8	IOR LVDS33 107 P
IO_L23N_3	U7	IOR LVDS33 107 N
IO_L24P_3	U5	IOR LVDS33 136 P
IO_L24N_3	V5	IOR LVDS33 136 N
IO_L25P_3	T10	IOR LVDS33 140 P
IO_L25N_3	U9	IOR LVDS33 140 N
IO_L27P_3	R10	IOR LVDS33 142 P
IO_L27N_3	T9	IOR LVDS33 142 N
IO_L28P_3	U4	IOR LVDS33 95 P
IO_L28N_3	U3	IOR LVDS33 95 N
IO_L29P_3	T8	IOR LVDS33 103 P
IO_L29N_3	T6	IOR LVDS33 103 N
IO_L30P_3	R5	IOR LVDS33 62 P
IO_L30N_3	T4	IOR LVDS33 62 N
IO_L31P_3	R7	IOR LVDS33 139 P
IO_L31N_VREF_3	R6	IOR LVDS33 139 N
IO_L32P_M3DQ14_3	AB3	IOR LVDS33 129 P
IO_L32N_M3DQ15_3	AB1	IOR LVDS33 129 N

U1

BANK3
XC6SLX150-3FGG676

IO_L33P_M3DQ12_3	AD3	IOR LVDS33 146 P
IO_L33N_M3DQ13_3	AD1	IOR LVDS33 146 N
IO_L34P_M3UDQS_3	AC2	IOR LVDS33 113 P
IO_L34N_M3UDQSN_3	AC1	IOR LVDS33 113 N
IO_L35P_M3DQ10_3	AE2	IOR LVDS33 130 P
IO_L35N_M3DQ11_3	AE1	IOR LVDS33 130 N
IO_L36P_M3DQ8_3	AA2	IOR LVDS33 145 P
IO_L36N_M3DQ9_3	AA1	IOR LVDS33 145 N
IO_L37P_M3DQ0_3	Y3	IOR LVDS33 112 P
IO_L37N_M3DQ1_3	Y1	IOR LVDS33 112 N
IO_L38P_M3DQ2_3	W2	IOR LVDS33 128 P
IO_L38N_M3DQ3_3	W1	IOR LVDS33 128 N
IO_L39P_M3LDQS_3	V3	IOR LVDS33 144 P
IO_L39N_M3LDQSN_3	V1	IOR LVDS33 144 N
IO_L40P_M3DQ6_3	U2	IOR LVDS33 111 P
IO_L40N_M3DQ7_3	U1	IOR LVDS33 111 N
IO_L41P_GCLK27_M3DQ4_3	T3	IOR LVDS33 16 P
IO_L41N_GCLK26_M3DQ5_3	T1	IOR LVDS33 16 N
IO_L42P_GCLK25_TRDY2_M3UDM_3	V4	IOR LVDS33 79 P
IO_L42N_GCLK24_M3LDM_3	W3	IOR LVDS33 79 N
IO_L43P_GCLK23_M3RASN_3	N8	IOR LVDS33 106 P
IO_L43N_GCLK22_IRDY2_M3CASN_3	P8	IOR LVDS33 106 N
IO_L44P_GCLK21_M3A5_3	R2	IOR LVDS33 32 P
IO_L44N_GCLK20_M3A6_3	R1	IOR LVDS33 32 N
IO_L45P_M3A3_3	P7	IOR LVDS33 119 P
IO_L45N_M3ODT_3	P6	IOR LVDS33 119 N
IO_L46P_M3CLK_3	R4	IOR LVDS33 78 P
IO_L46N_M3CLKN_3	R3	IOR LVDS33 78 N
IO_L47P_M3A0_3	N7	IOR LVDS33 125 P
IO_L47N_M3A1_3	N6	IOR LVDS33 125 N
IO_L48P_M3BA0_3	P3	IOR LVDS33 48 P
IO_L48N_M3BA1_3	P1	IOR LVDS33 48 N
IO_L49P_M3A7_3	P10	IOR LVDS33 123 P
IO_L49N_M3A2_3	R9	IOR LVDS33 123 N
IO_L50P_M3WE_3	P5	IOR LVDS33 109 P
IO_L50N_M3BA2_3	N5	IOR LVDS33 109 N
IO_L51P_M3A10_3	M10	IOR LVDS33 73 P
IO_L51N_M3A4_3	N9	IOR LVDS33 73 N
IO_L52P_M3A8_3	N4	IOR LVDS33 94 P
IO_L52N_M3A9_3	N3	IOR LVDS33 94 N
IO_L53P_M3CKE_3	M9	IOR LVDS33 135 P
IO_L53N_M3A12_3	M8	IOR LVDS33 135 N
IO_L54P_M3RESET_3	L4	IOR LVDS33 77 P
IO_L54N_M3A11_3	L3	IOR LVDS33 77 N
IO_L55P_M3A13_3	M6	IOR LVDS33 61 P
IO_L55N_M3A14_3	M4	IOR LVDS33 61 N
IO_L57P_3	L7	IOR LVDS33 141 P
IO_L57N_VREF_3	L6	IOR LVDS33 141 N

TITLE: Adapter
Bank3 of U1, VCCO: 3.3V

Date:	30-03-2011_16:34	DWG:	Adapter/Termination Board
Sheet Size:	B	REV:	1.0
Sheet	5 of 28	Drawn By	Shiming Yang

U1

BANK4
XC6SLX150-3FGG676

IO_L58P_4	K8	IOR LVDS33 122 P
IO_L58N_VREF_4	L8	IOR LVDS33 122 N
IO_L59P_M4DQ14_4	N2	IOR LVDS33 15 P
IO_L59N_M4DQ15_4	N1	IOR LVDS33 15 N
IO_L60P_M4DQ12_4	M3	IOR LVDS33 31 P
IO_L60N_M4DQ13_4	M1	IOR LVDS33 31 N
IO_L61P_M4UDQS_4	L2	IOR LVDS33 47 P
IO_L61N_M4UDQSN_4	L1	IOR LVDS33 47 N
IO_L62P_M4DQ10_4	K3	IOR LVDS33 14 P
IO_L62N_M4DQ11_4	K1	IOR LVDS33 14 N
IO_L63P_M4DQ8_4	J2	IOR LVDS33 30 P
IO_L63N_M4DQ9_4	J1	IOR LVDS33 30 N
IO_L64P_M4DQ0_4	H3	IOR LVDS33 46 P
IO_L64N_M4DQ1_4	H1	IOR LVDS33 46 N
IO_L65P_M4DQ2_4	G2	IOR LVDS33 13 P
IO_L65N_M4DQ3_4	F1	IOR LVDS33 13 N
IO_L66P_M4LDQS_4	F3	IOR LVDS33 29 P
IO_L66N_M4LDQSN_4	F1	IOR LVDS33 29 N
IO_L67P_M4DQ6_4	E2	IOR LVDS33 45 P
IO_L67N_M4DQ7_4	E1	IOR LVDS33 45 N
IO_L68P_M4DQ4_4	D3	IOR LVDS33 12 P
IO_L68N_M4DQ5_4	D1	IOR LVDS33 12 N
IO_L69P_M4UDM_4	J4	IOR LVDS33 93 P
IO_L69N_M4LDM_4	J3	IOR LVDS33 93 N
IO_L70P_M4RASN_4	K7	IOR LVDS33 60 P
IO_L70N_M4CASN_4	K6	IOR LVDS33 60 N
IO_L71P_M4A5_4	K5	IOR LVDS33 102 P
IO_L71N_M4A6_4	J5	IOR LVDS33 102 N
IO_L72P_M4A3_4	J7	IOR LVDS33 118 P
IO_L72N_M4ODT_4	H7	IOR LVDS33 118 N
IO_L73P_M4CLK_4	G4	IOR LVDS33 76 P
IO_L73N_M4CLKN_4	G3	IOR LVDS33 76 N
IO_L74P_M4A0_4	K10	IOR LVDS33 124 P
IO_L74N_M4A1_4	L9	IOR LVDS33 124 N
IO_L75P_M4BA0_4	H6	IOR LVDS33 108 P
IO_L75N_M4BA1_4	H5	IOR LVDS33 108 N
IO_L76P_M4A7_4	J10	IOR LVDS33 116 P
IO_L76N_M4A2_4	K9	IOR LVDS33 116 N
IO_L77P_M4WE_4	E4	IOR LVDS33 92 P
IO_L77N_M4BA2_4	E3	IOR LVDS33 92 N
IO_L78P_M4A10_4	G6	IOR LVDS33 59 P
IO_L78N_M4A4_4	G5	IOR LVDS33 59 N
IO_L79P_M4A8_4	C2	IOR LVDS33 28 P
IO_L79N_M4A9_4	C1	IOR LVDS33 28 N
IO_L80P_M4CKE_4	F5	IOR LVDS33 101 P
IO_L80N_M4A12_4	E5	IOR LVDS33 101 N
IO_L81P_M4RESET_4	B2	IOR LVDS33 44 P
IO_L81N_M4A11_4	B1	IOR LVDS33 44 N
IO_L82P_M4A13_4	H8	IOR LVDS33 134 P
IO_L82N_M4A14_4	G7	IOR LVDS33 134 N
IO_L83P_4	C4	IOR LVDS33 75 P
IO_L83N_VREF_4	C3	IOR LVDS33 75 N

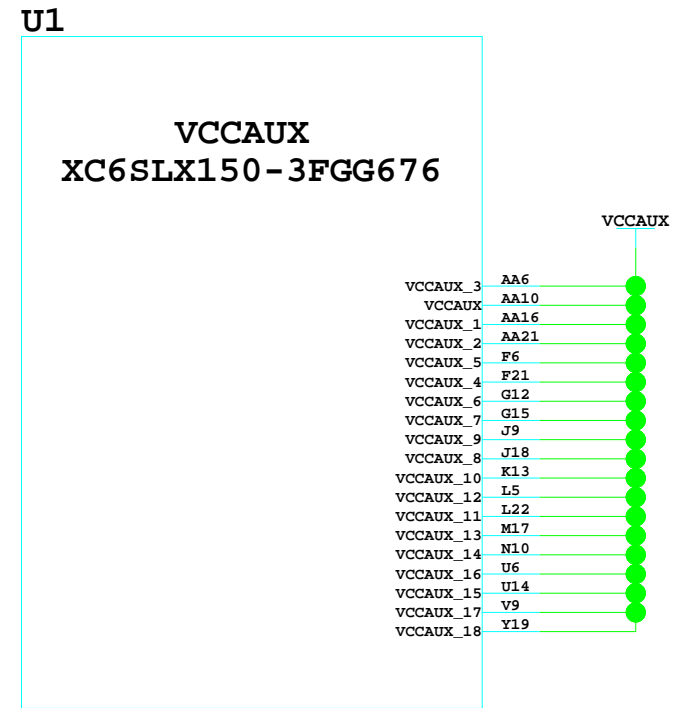
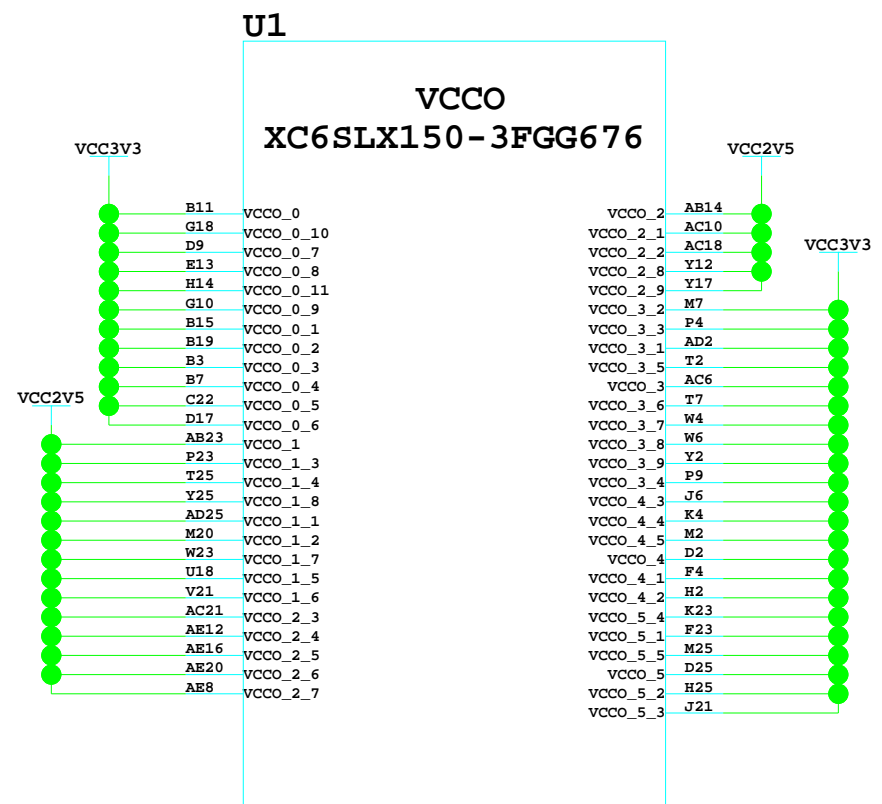
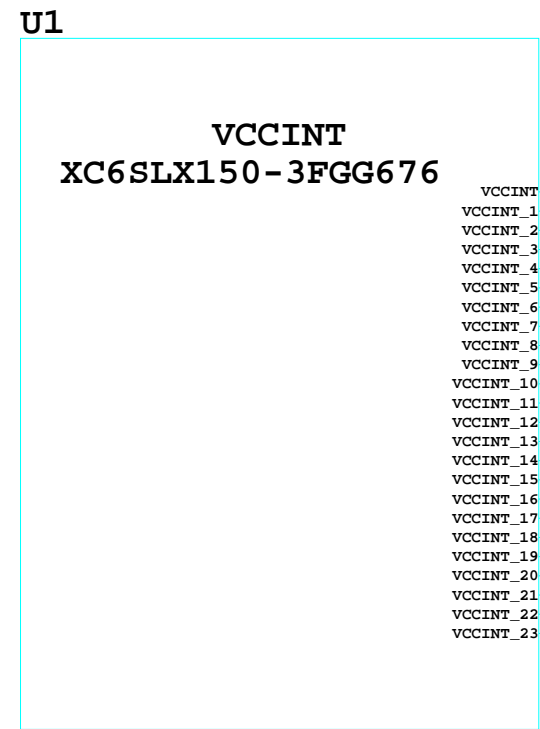
U1

BANK5
XC6SLX150-3FGG676

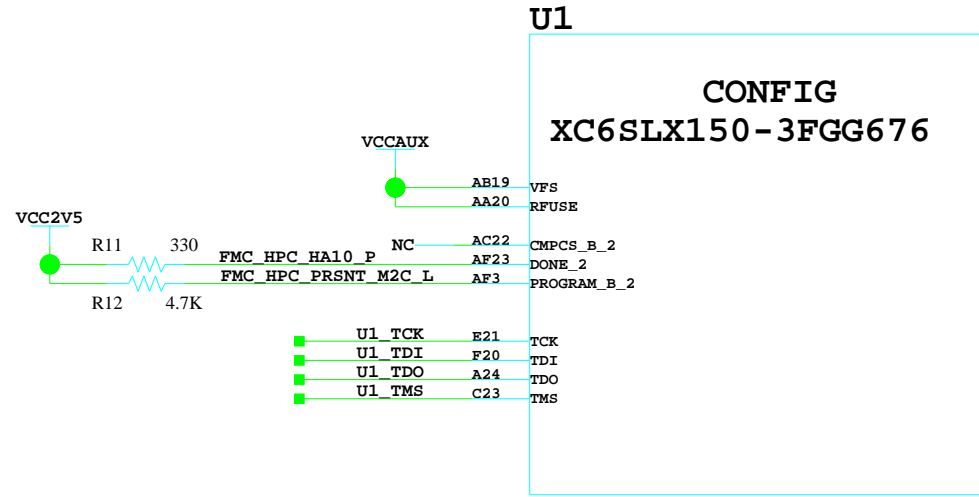
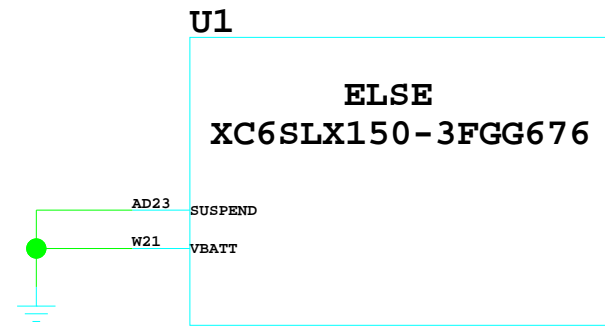
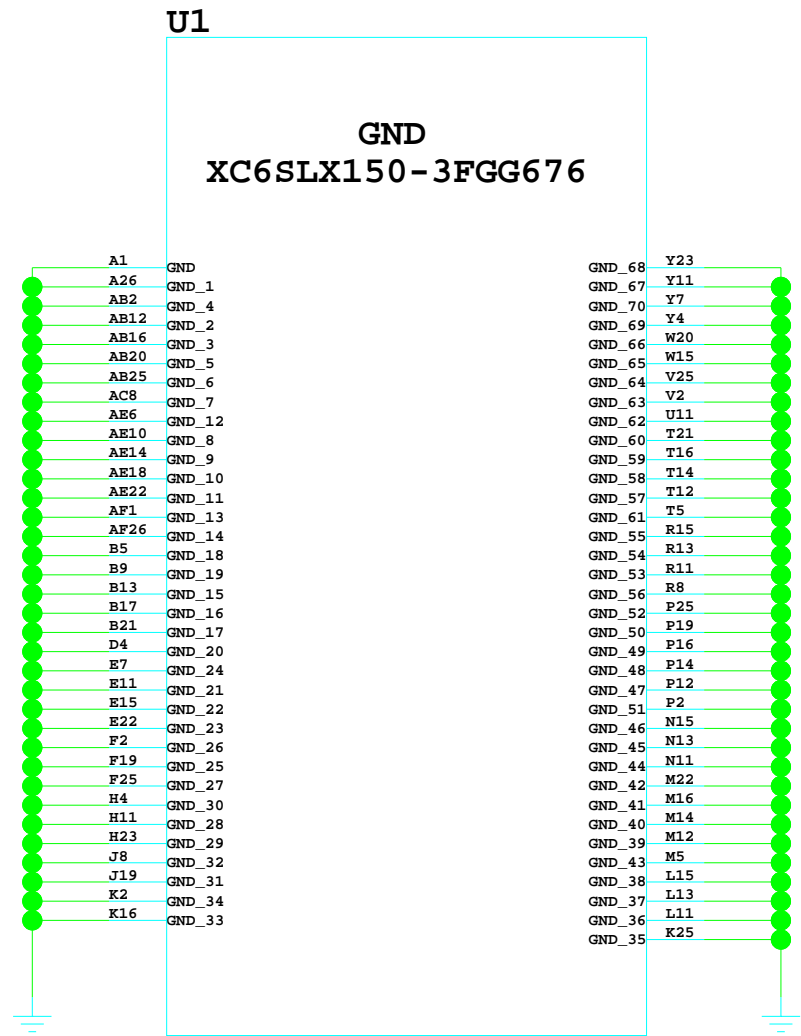
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IO_L1N_A24_VREF_5	A23	IOR LVDS33 37 N
IO_L2P_M5A13_5	G20	IOR LVDS33 53 P
IO_L2N_M5A14_5	G21	IOR LVDS33 53 N
IO_L3P_M5RESET_5	D23	IOR LVDS33 67 P
IO_L3N_M5A11_5	C24	IOR LVDS33 67 N
IO_L4P_M5CKE_5	F22	IOR LVDS33 84 P
IO_L4N_M5A12_5	D22	IOR LVDS33 84 N
IO_L5P_M5A8_5	H20	IOR LVDS33 105 P
IO_L5N_M5A9_5	H21	IOR LVDS33 105 N
IO_L6P_M5A10_5	H22	IOR LVDS33 50 P
IO_L6N_M5A4_5	G22	IOR LVDS33 50 N
IO_L7P_M5WE_5	E23	IOR LVDS33 83 P
IO_L7N_M5BA2_5	E24	IOR LVDS33 83 N
IO_L8P_M5A7_5	G23	IOR LVDS33 66 P
IO_L8N_M5A2_5	G24	IOR LVDS33 66 N
IO_L9P_M5BA0_5	H18	IOR LVDS33 98 P
IO_L9N_M5BA1_5	G19	IOR LVDS33 98 N
IO_L10P_M5A0_5	B24	IOR LVDS33 4 P
IO_L10N_M5A1_5	A25	IOR LVDS33 4 N
IO_L11P_M5CLK_5	C25	IOR LVDS33 36 P
IO_L11N_M5CLKN_5	C26	IOR LVDS33 36 N
IO_L12P_M5A3_5	B25	IOR LVDS33 20 P
IO_L12N_M5ODT_5	B26	IOR LVDS33 20 N
IO_L13P_M5A5_5	K20	IOR LVDS33 85 P
IO_L13N_M5A6_5	K21	IOR LVDS33 85 N
IO_L14P_M5RASN_5	K22	IOR LVDS33 49 P
IO_L14N_M5CASN_5	J22	IOR LVDS33 49 N
IO_L15P_M5UDM_5	J23	IOR LVDS33 82 P
IO_L15N_M5LDM_5	J24	IOR LVDS33 82 N
IO_L16P_M5DQ4_5	E25	IOR LVDS33 19 P
IO_L16N_M5DQ5_5	E26	IOR LVDS33 19 N
IO_L17P_M5DQ6_5	D24	IOR LVDS33 3 P
IO_L17N_M5DQ7_5	D26	IOR LVDS33 3 N
IO_L18P_M5LDQS_5	F24	IOR LVDS33 35 P
IO_L18N_M5LDQSN_5	F26	IOR LVDS33 35 N
IO_L19P_M5DQ2_5	H24	IOR LVDS33 18 P
IO_L19N_M5DQ3_5	H26	IOR LVDS33 18 N
IO_L20P_M5DQ0_5	G25	IOR LVDS33 2 P
IO_L20N_M5DQ1_5	G26	IOR LVDS33 2 N
IO_L21P_M5DQ8_5	K24	IOR LVDS33 1 P
IO_L21N_M5DQ9_5	K26	IOR LVDS33 1 N
IO_L22P_M5DQ10_5	J25	IOR LVDS33 34 P
IO_L22N_M5DQ11_5	J26	IOR LVDS33 34 N
IO_L23P_M5UDQS_5	M24	IOR LVDS33 33 P
IO_L23N_M5UDQSN_5	M26	IOR LVDS33 33 N
IO_L24P_M5DQ12_5	L25	IOR LVDS33 17 P
IO_L24N_M5DQ13_5	L26	IOR LVDS33 17 N
IO_L25P_M5DQ14_5	N25	IOR LVDS33 0 P
IO_L25N_M5DQ15_5	N26	IOR LVDS33 0 N
IO_L26N_VREF_5	L20	IOR LVDS33 51 P
IO_L27P_5	L21	IOR LVDS33 51 N
IO_L27N_5	H19	IOR LVDS33 147 P
	J20	IOR LVDS33 147 N

TITLE: Adapter
Bank4 and Bank5 of U1, VCCO: 3.3V

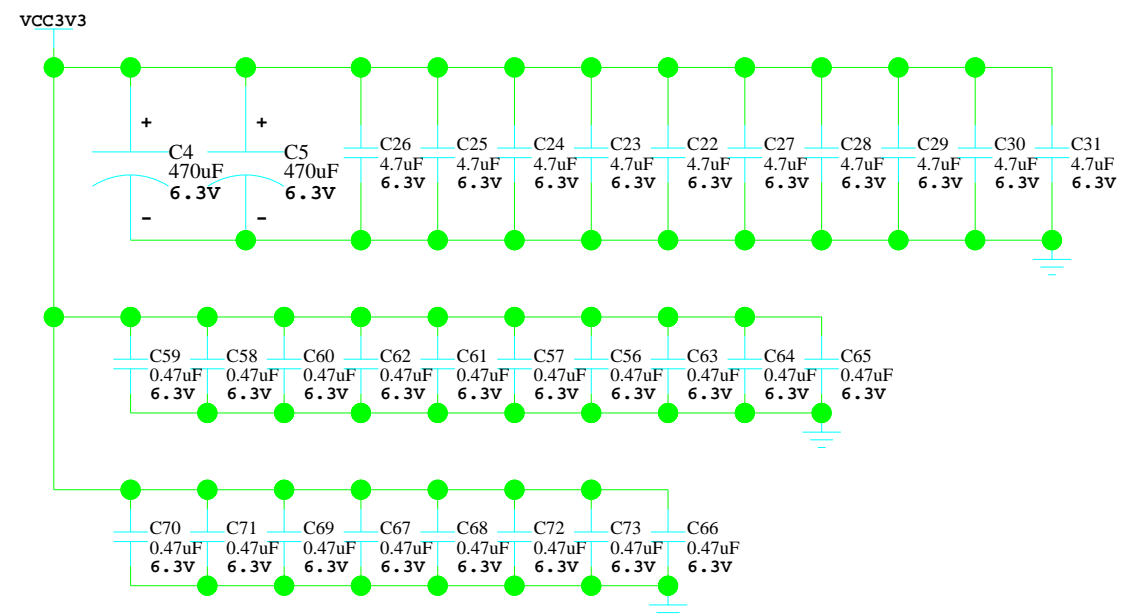
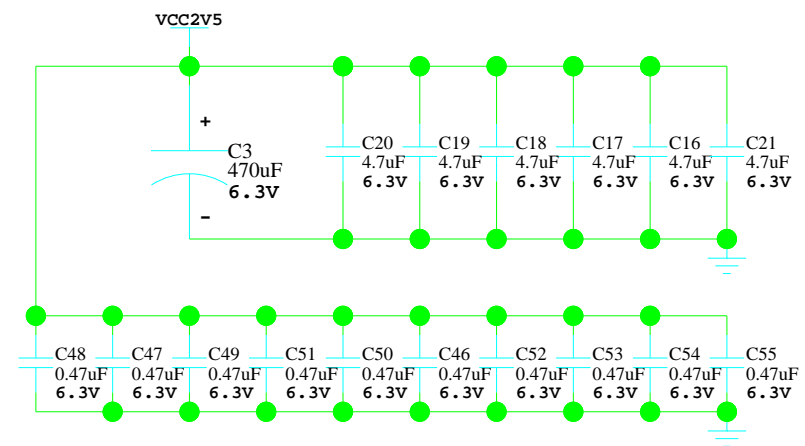
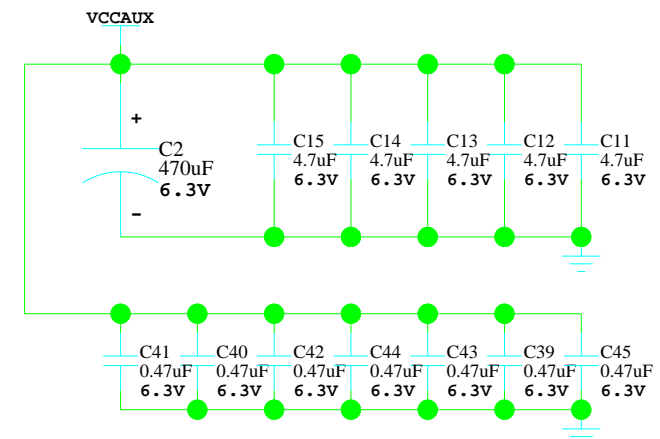
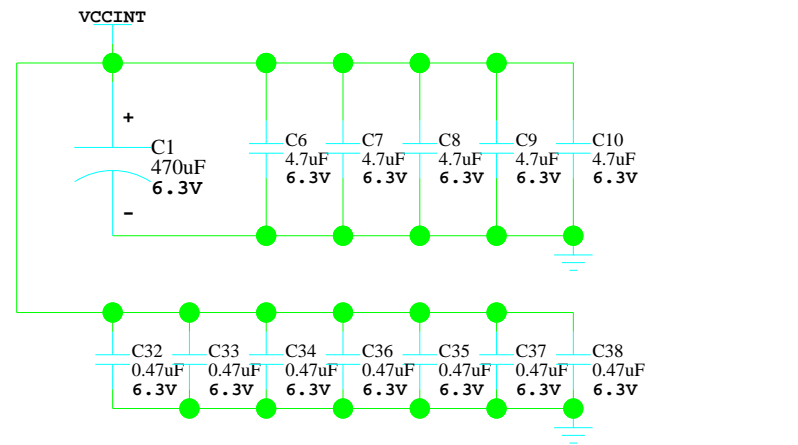
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Sheet Size:	B	REV:	1.0
Sheet	6 of 28	Drawn By	Shiming Yang



TITLE: Adapter	
Power supply inputs of U1	
Date:	30-03-2011_16:34
Sheet Size:	B
Sheet	7 of 28
DWG:	Adapter/Termination Board
REV:	1.0
Drawn By	Shiming Yang



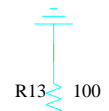
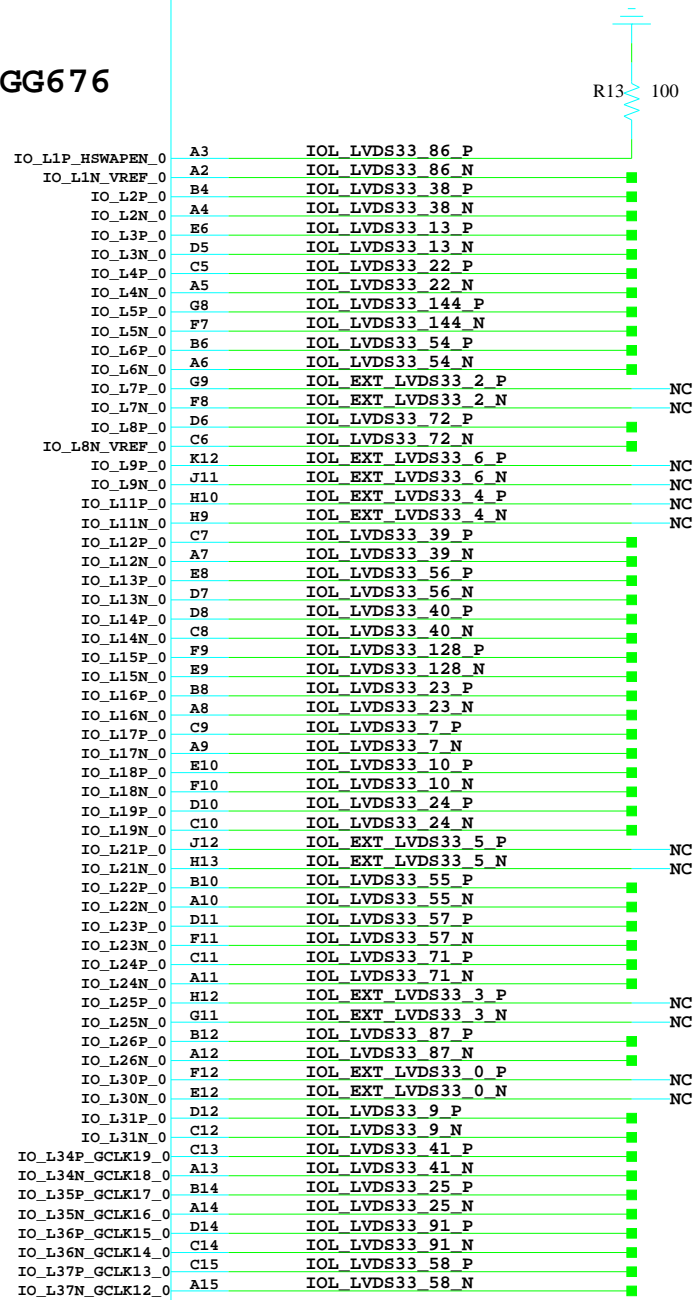
TITLE: Adapter	
Ground, Configuration inputs of U1	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 8 of 28	Drawn By Shiming Yang



TITLE: Adapter	
Decoupling capacitors of U1	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 9 of 28	Drawn By Shiming Yang

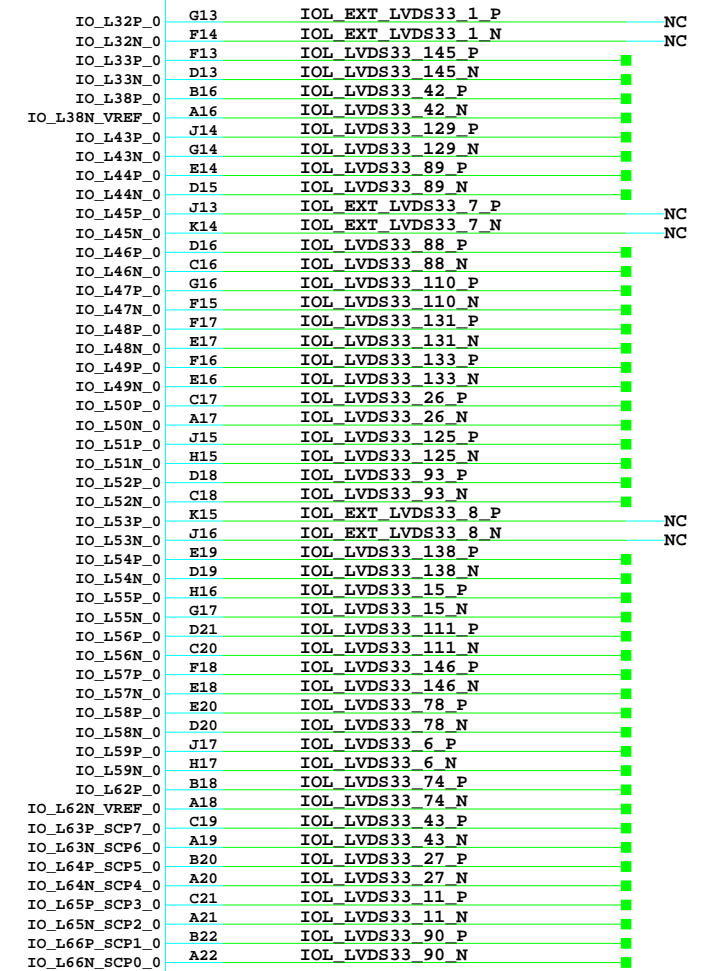
U2

BANK0
XC6SLX150-3FGG676



U2

BANK0
XC6SLX150-3FGG676

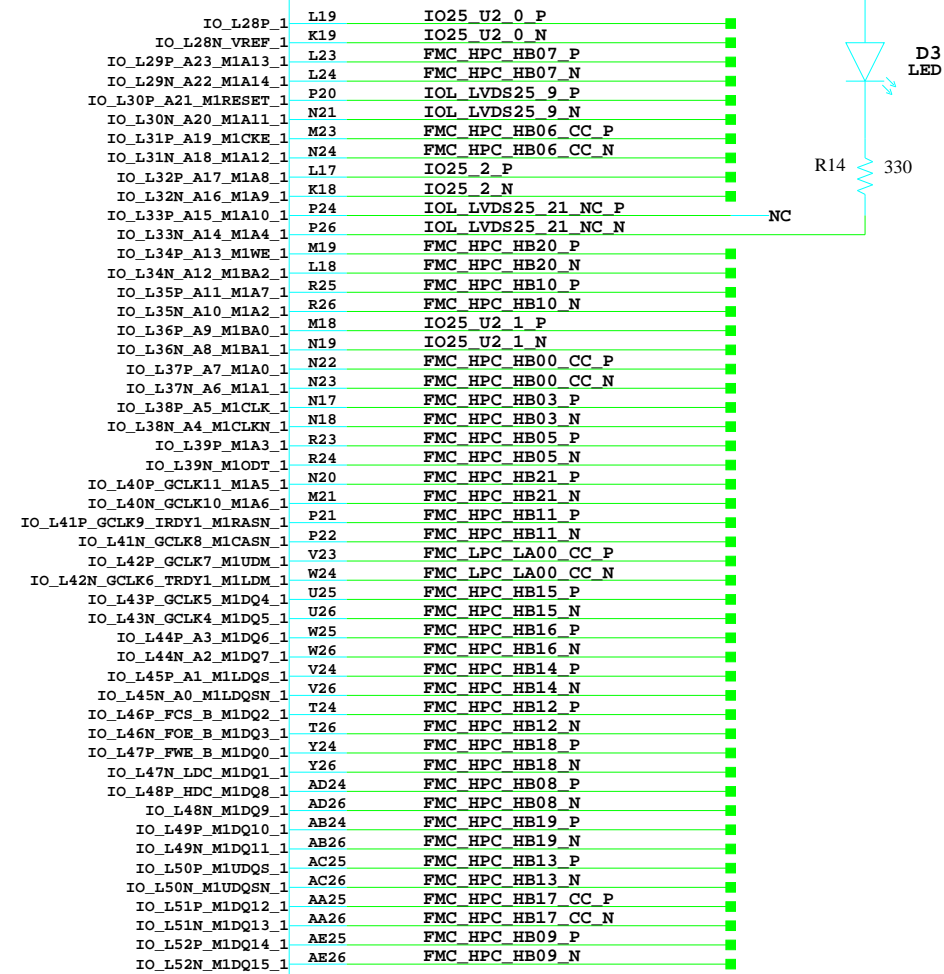


TITLE: Adapter
Bank0 of U2, VCC0: 3.3V

Date:	30-03-2011_16:34	DWG:	Adapter/Termination Board
Sheet Size:	B	REV:	1.0
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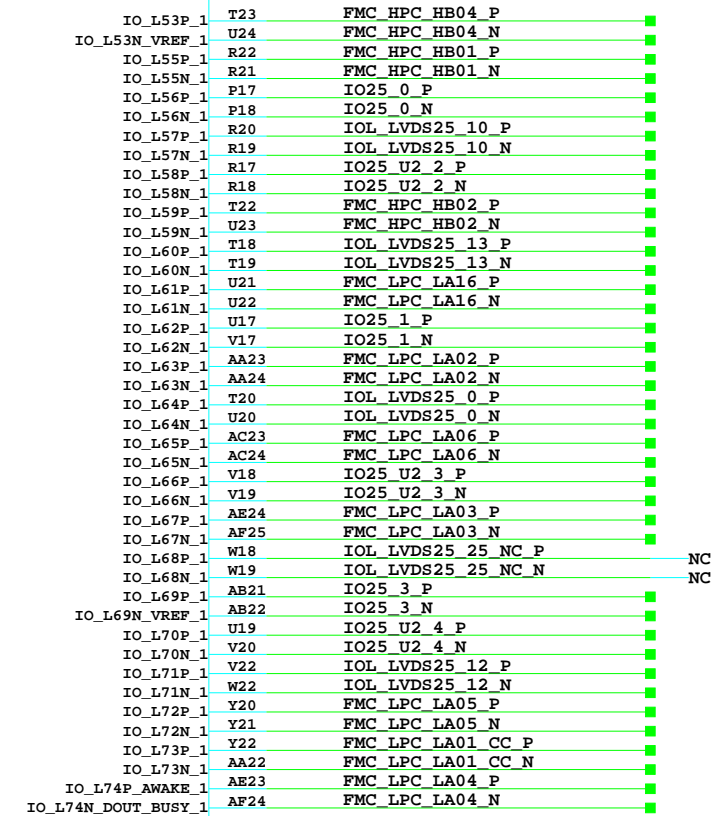
U2

BANK1
XC6SLX150-3FGG676



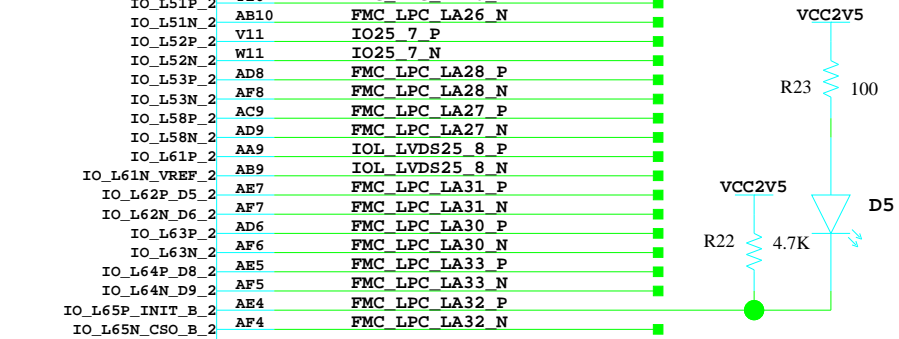
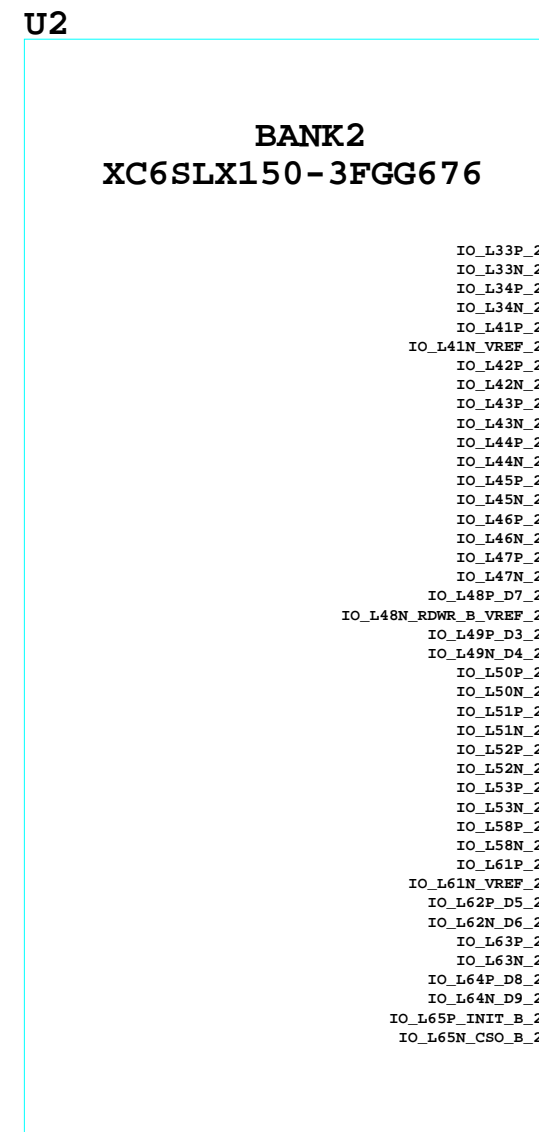
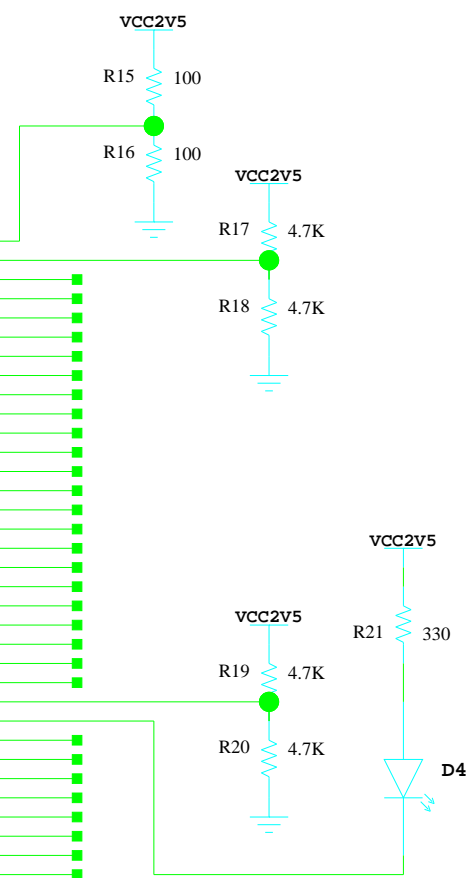
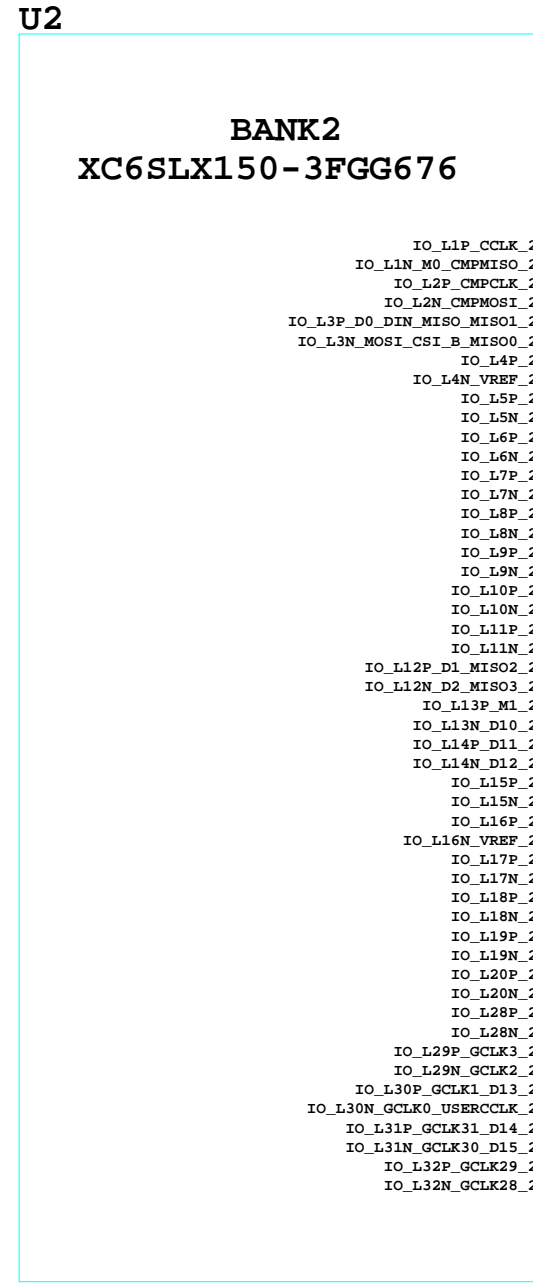
U2

BANK1
XC6SLX150-3FGG676



TITLE: Adapter
Bank1 of U2, VCCO: 2.5V

Date:	30-03-2011_16:34	DWG:	Adapter/Termination Board
Sheet Size:	B	REV:	1.0
Sheet	11 of 28	Drawn By	Shiming Yang



TITLE: Adapter	
Bank2 of U2, VCCO: 2.5V	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 12 of 28	Drawn By Shiming Yang

U2

BANK3
XC6SLX150-3FGG676

IO_L1P_3	AC7	IOL LVDS33 98 P
IO_L1N_VREF_3	AD7	IOL LVDS33 98 N
IO_L2P_3	AE3	IOL LVDS33 147 P
IO_L2N_3	AF2	IOL LVDS33 147 N
IO_L3P_3	AC4	IOL LVDS33 99 P
IO_L3N_3	AD4	IOL LVDS33 99 N
IO_L4P_3	AA8	IOL LVDS33 142 P
IO_L4N_3	AB8	IOL LVDS33 142 N
IO_L7P_3	AA7	IOL LVDS33 59 P
IO_L7N_3	Y6	IOL LVDS33 59 N
IO_L8P_3	AB7	IOL LVDS33 109 P
IO_L8N_3	AB6	IOL LVDS33 109 N
IO_L9P_3	Y9	IOL LVDS33 82 P
IO_L9N_3	Y8	IOL LVDS33 82 N
IO_L10P_3	AC5	IOL LVDS33 66 P
IO_L10N_3	AD5	IOL LVDS33 66 N
IO_L15P_3	W8	IOL LVDS33 139 P
IO_L15N_3	W7	IOL LVDS33 139 N
IO_L16P_3	AA5	IOL LVDS33 92 P
IO_L16N_3	AB5	IOL LVDS33 92 N
IO_L17P_3	V7	IOL LVDS33 73 P
IO_L17N_VREF_3	V6	IOL LVDS33 73 N
IO_L18P_3	AB4	IOL LVDS33 83 P
IO_L18N_3	AC3	IOL LVDS33 83 N
IO_L19P_3	W9	IOL LVDS33 100 P
IO_L19N_3	V8	IOL LVDS33 100 N
IO_L20P_3	AA4	IOL LVDS33 67 P
IO_L20N_3	AA3	IOL LVDS33 67 N
IO_L21P_3	W10	IOL LVDS33 140 P
IO_L21N_3	V10	IOL LVDS33 140 N
IO_L22P_3	W5	IOL LVDS33 122 P
IO_L22N_3	Y5	IOL LVDS33 122 N
IO_L23P_3	U8	IOL LVDS33 1 P
IO_L23N_3	U7	IOL LVDS33 1 N
IO_L24P_3	U5	IOL LVDS33 76 P
IO_L24N_3	V5	IOL LVDS33 76 N
IO_L25P_3	T10	IOL LVDS33 0 P
IO_L25N_3	U9	IOL LVDS33 0 N
IO_L27P_3	R10	IOL LVDS33 106 P
IO_L27N_3	T9	IOL LVDS33 106 N
IO_L28P_3	U4	IOL LVDS33 68 P
IO_L28N_3	U3	IOL LVDS33 68 N
IO_L29P_3	T8	IOL LVDS33 126 P
IO_L29N_3	T6	IOL LVDS33 126 N
IO_L30P_3	R5	IOL LVDS33 101 P
IO_L30N_3	T4	IOL LVDS33 101 N
IO_L31P_3	R7	IOL LVDS33 3 P
IO_L31N_VREF_3	R6	IOL LVDS33 3 N
IO_L32P_M3DQ14_3	AB3	IOL LVDS33 117 P
IO_L32N_M3DQ15_3	AB1	IOL LVDS33 117 N

U2

BANK3
XC6SLX150-3FGG676

IO_L33P_M3DQ12_3	AD3	IOL LVDS33 132 P
IO_L33N_M3DQ13_3	AD1	IOL LVDS33 132 N
IO_L34P_M3UDQS_3	AC2	IOL LVDS33 116 P
IO_L34N_M3UDQSN_3	AC1	IOL LVDS33 116 N
IO_L35P_M3DQ10_3	AE2	IOL LVDS33 115 P
IO_L35N_M3DQ11_3	AE1	IOL LVDS33 115 N
IO_L36P_M3DQ8_3	AA2	IOL LVDS33 134 P
IO_L36N_M3DQ9_3	AA1	IOL LVDS33 134 N
IO_L37P_M3DQ0_3	Y3	IOL LVDS33 118 P
IO_L37N_M3DQ1_3	Y1	IOL LVDS33 118 N
IO_L38P_M3DQ2_3	W2	IOL LVDS33 135 P
IO_L38N_M3DQ3_3	W1	IOL LVDS33 135 N
IO_L39P_M3LDQS_3	V3	IOL LVDS33 119 P
IO_L39N_M3LDQSN_3	V1	IOL LVDS33 119 N
IO_L40P_M3DQ6_3	U2	IOL LVDS33 70 P
IO_L40N_M3DQ7_3	U1	IOL LVDS33 70 N
IO_L41P_GCLK27_M3DQ4_3	T3	IOL LVDS33 49 P
IO_L41N_GCLK26_M3DQ5_3	T1	IOL LVDS33 49 N
IO_L42P_GCLK25_TRDY2_M3UDM_3	V4	IOL LVDS33 84 P
IO_L42N_GCLK24_M3LDM_3	W3	IOL LVDS33 84 N
IO_L43P_GCLK23_M3RASN_3	N8	IOL LVDS33 4 P
IO_L43N_GCLK22_IRDY2_M3CASN_3	P8	IOL LVDS33 4 N
IO_L44P_GCLK21_M3A5_3	R2	IOL LVDS33 33 P
IO_L44N_GCLK20_M3A6_3	R1	IOL LVDS33 33 N
IO_L45P_M3A3_3	P7	IOL LVDS33 60 P
IO_L45N_M3ODT_3	P6	IOL LVDS33 60 N
IO_L46P_M3CLK_3	R4	IOL LVDS33 85 P
IO_L46N_M3CLKN_3	R3	IOL LVDS33 85 N
IO_L47P_M3A0_3	N7	IOL LVDS33 107 P
IO_L47N_M3A1_3	N6	IOL LVDS33 107 N
IO_L48P_M3BA0_3	P3	IOL LVDS33 17 P
IO_L48N_M3BA1_3	P1	IOL LVDS33 17 N
IO_L49P_M3A7_3	P10	IOL LVDS33 2 P
IO_L49N_M3A2_3	R9	IOL LVDS33 2 N
IO_L50P_M3WE_3	P5	IOL LVDS33 123 P
IO_L50N_M3BA2_3	N5	IOL LVDS33 123 N
IO_L51P_M3A10_3	M10	IOL LVDS33 141 P
IO_L51N_M3A4_3	N9	IOL LVDS33 141 N
IO_L52P_M3A8_3	N4	IOL LVDS33 69 P
IO_L52N_M3A9_3	N3	IOL LVDS33 69 N
IO_L53P_M3CKE_3	M9	IOL LVDS33 44 P
IO_L53N_M3A12_3	M8	IOL LVDS33 44 N
IO_L54P_M3RESET_3	L4	IOL LVDS33 137 P
IO_L54N_M3A11_3	L3	IOL LVDS33 137 N
IO_L55P_M3A13_3	M6	IOL LVDS33 102 P
IO_L55N_M3A14_3	M4	IOL LVDS33 102 N
IO_L57P_3	L7	IOL LVDS33 124 P
IO_L57N_VREF_3	L6	IOL LVDS33 124 N

TITLE: Adapter
Bank3 of U2, VCCO: 3.3V

Date:	30-03-2011_16:34	DWG:	Adapter/Termination Board
Sheet Size:	B	REV:	1.0
Sheet	13 of 28	Drawn By	Shiming Yang

U2

BANK4
XC6SLX150-3FGG676

IO_L58P_4	K8	IOL LVDS33 5 P
IO_L58N_VREF_4	L8	IOL LVDS33 5 N
IO_L59P_M4DQ14_4	N2	IOL LVDS33 50 P
IO_L59N_M4DQ15_4	N1	IOL LVDS33 50 N
IO_L60P_M4DQ12_4	M3	IOL LVDS33 34 P
IO_L60N_M4DQ13_4	M1	IOL LVDS33 34 N
IO_L61P_M4UDQS_4	L2	IOL LVDS33 18 P
IO_L61N_M4UDQSN_4	L1	IOL LVDS33 18 N
IO_L62P_M4DQ10_4	K3	IOL LVDS33 51 P
IO_L62N_M4DQ11_4	K1	IOL LVDS33 51 N
IO_L63P_M4DQ8_4	J2	IOL LVDS33 35 P
IO_L63N_M4DQ9_4	J1	IOL LVDS33 35 N
IO_L64P_M4DQ0_4	H3	IOL LVDS33 19 P
IO_L64N_M4DQ1_4	H1	IOL LVDS33 19 N
IO_L65P_M4DQ2_4	G2	IOL LVDS33 52 P
IO_L65N_M4DQ3_4	G1	IOL LVDS33 52 N
IO_L66P_M4LDQS_4	F3	IOL LVDS33 36 P
IO_L66N_M4LDQSN_4	F1	IOL LVDS33 36 N
IO_L67P_M4DQ6_4	E2	IOL LVDS33 20 P
IO_L67N_M4DQ7_4	E1	IOL LVDS33 20 N
IO_L68P_M4DQ4_4	D3	IOL LVDS33 53 P
IO_L68N_M4DQ5_4	D1	IOL LVDS33 53 N
IO_L69P_M4UDM_4	J4	IOL LVDS33 136 P
IO_L69N_M4LDM_4	J3	IOL LVDS33 136 N
IO_L70P_M4RASN_4	K7	IOL LVDS33 103 P
IO_L70N_M4CASN_4	K6	IOL LVDS33 103 N
IO_L71P_M4A5_4	K5	IOL LVDS33 77 P
IO_L71N_M4A6_4	J5	IOL LVDS33 77 N
IO_L72P_M4A3_4	J7	IOL LVDS33 12 P
IO_L72N_M4ODT_4	H7	IOL LVDS33 12 N
IO_L73P_M4CLK_4	G4	IOL LVDS33 121 P
IO_L73N_M4CLKN_4	G3	IOL LVDS33 121 N
IO_L74P_M4A0_4	K10	IOL LVDS33 108 P
IO_L74N_M4A1_4	L9	IOL LVDS33 108 N
IO_L75P_M4BA0_4	H6	IOL LVDS33 75 P
IO_L75N_M4BA1_4	H5	IOL LVDS33 75 N
IO_L76P_M4A7_4	J10	IOL LVDS33 112 P
IO_L76N_M4A2_4	K9	IOL LVDS33 112 N
IO_L77P_M4WE_4	E4	IOL LVDS33 120 P
IO_L77N_M4BA2_4	E3	IOL LVDS33 120 N
IO_L78P_M4A10_4	G6	IOL LVDS33 104 P
IO_L78N_M4A4_4	G5	IOL LVDS33 104 N
IO_L79P_M4A8_4	C2	IOL LVDS33 37 P
IO_L79N_M4A9_4	C1	IOL LVDS33 37 N
IO_L80P_M4CKE_4	F5	IOL LVDS33 127 P
IO_L80N_M4A12_4	E5	IOL LVDS33 127 N
IO_L81P_M4RESET_4	B2	IOL LVDS33 21 P
IO_L81N_M4A11_4	B1	IOL LVDS33 21 N
IO_L82P_M4A13_4	H8	IOL LVDS33 143 P
IO_L82N_M4A14_4	G7	IOL LVDS33 143 N
IO_L83P_4	C4	IOL LVDS33 105 P
IO_L83N_VREF_4	C3	IOL LVDS33 105 N

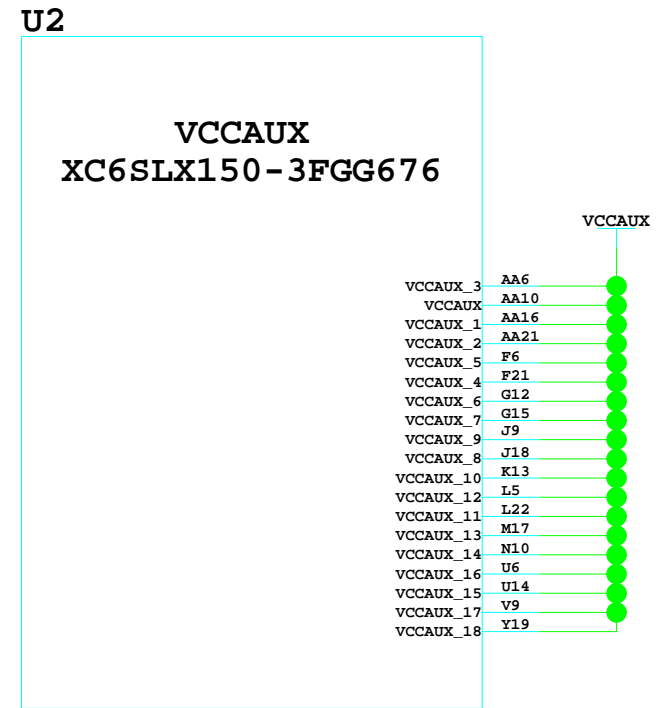
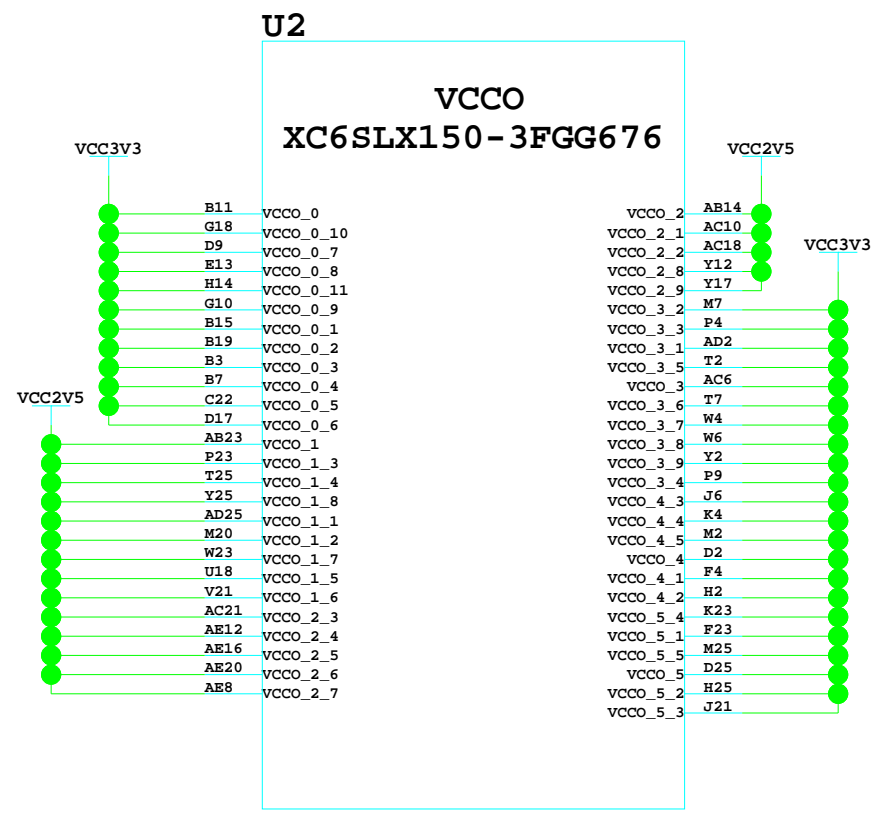
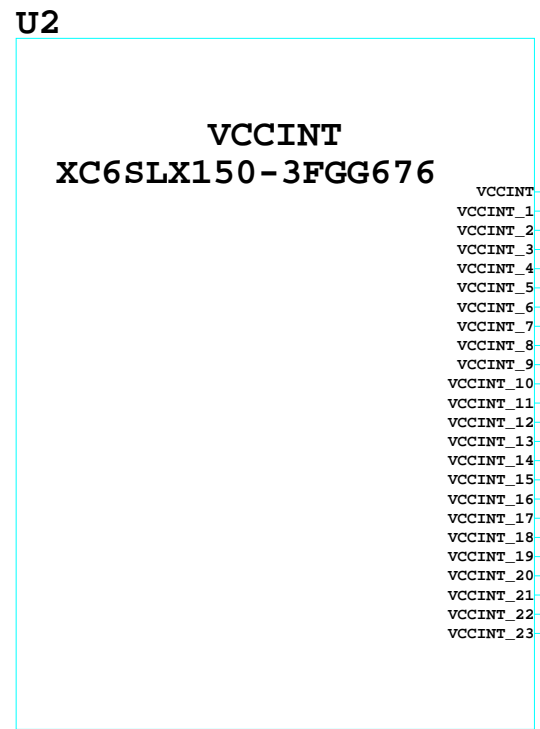
U2

BANK5
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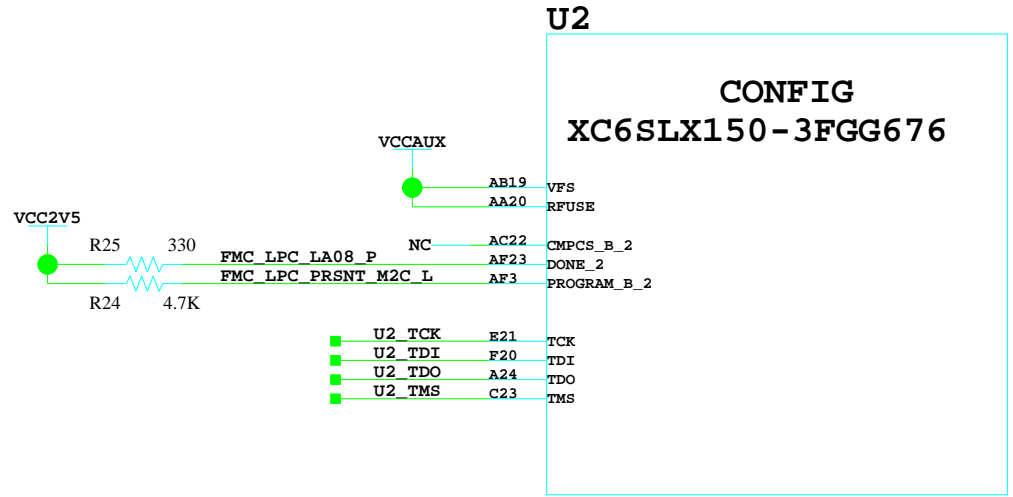
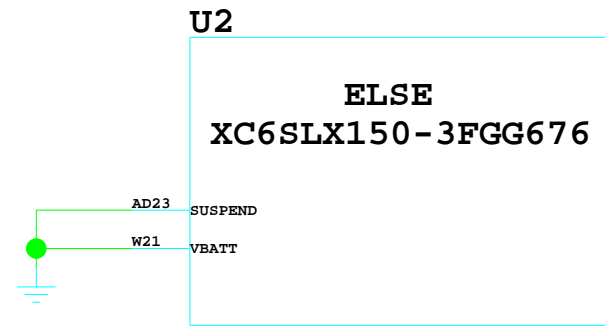
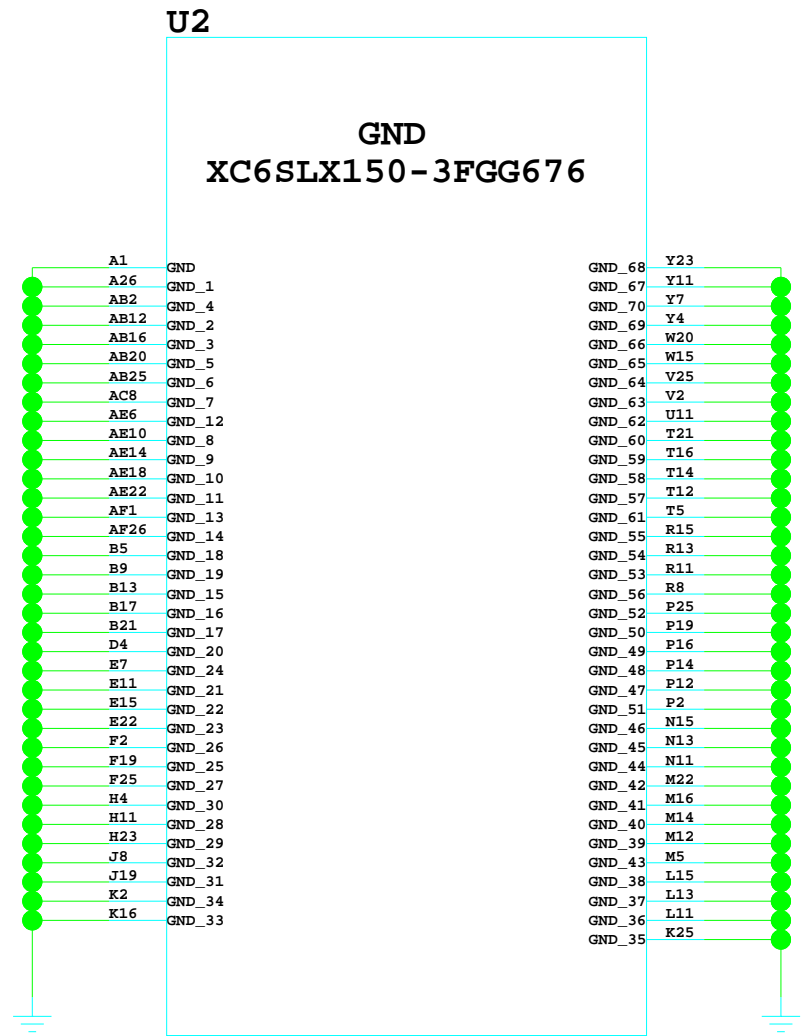
IO_L1P_A25_5	B23	IOL LVDS33 28 P
IO_L1N_A24_VREF_5	A23	IOL LVDS33 28 N
IO_L2P_M5A13_5	G20	IOL LVDS33 94 P
IO_L2N_M5A14_5	G21	IOL LVDS33 94 N
IO_L3P_M5RESET_5	D23	IOL LVDS33 96 P
IO_L3N_M5A11_5	C24	IOL LVDS33 96 N
IO_L4P_M5CKE_5	F22	IOL LVDS33 79 P
IO_L4N_M5A12_5	D22	IOL LVDS33 79 N
IO_L5P_M5A8_5	H20	IOL LVDS33 8 P
IO_L5N_M5A9_5	H21	IOL LVDS33 8 N
IO_L6P_M5A10_5	H22	IOL LVDS33 113 P
IO_L6N_M5A4_5	G22	IOL LVDS33 113 N
IO_L7P_M5WE_5	E23	IOL LVDS33 80 P
IO_L7N_M5BA2_5	E24	IOL LVDS33 80 N
IO_L8P_M5A7_5	G23	IOL LVDS33 97 P
IO_L8N_M5A2_5	G24	IOL LVDS33 97 N
IO_L9P_M5BA0_5	H18	IOL LVDS33 130 P
IO_L9N_M5BA1_5	G19	IOL LVDS33 130 N
IO_L10P_M5A0_5	B24	IOL LVDS33 61 P
IO_L10N_M5A1_5	A25	IOL LVDS33 61 N
IO_L11P_M5CLK_5	C25	IOL LVDS33 29 P
IO_L11N_M5CLKN_5	C26	IOL LVDS33 29 N
IO_L12P_M5A3_5	B25	IOL LVDS33 45 P
IO_L12N_M5ODT_5	B26	IOL LVDS33 45 N
IO_L13P_M5A5_5	K20	IOL LVDS33 95 P
IO_L13N_M5A6_5	K21	IOL LVDS33 95 N
IO_L14P_M5RASN_5	K22	IOL LVDS33 114 P
IO_L14N_M5CASN_5	J22	IOL LVDS33 114 N
IO_L15P_M5UDM_5	J23	IOL LVDS33 81 P
IO_L15N_M5LDM_5	J24	IOL LVDS33 81 N
IO_L16P_M5DQ4_5	E25	IOL LVDS33 46 P
IO_L16N_M5DQ5_5	E26	IOL LVDS33 46 N
IO_L17P_M5DQ6_5	D24	IOL LVDS33 62 P
IO_L17N_M5DQ7_5	D26	IOL LVDS33 62 N
IO_L18P_M5LDQS_5	F24	IOL LVDS33 30 P
IO_L18N_M5LDQSN_5	F26	IOL LVDS33 30 N
IO_L19P_M5DQ2_5	H24	IOL LVDS33 47 P
IO_L19N_M5DQ3_5	H26	IOL LVDS33 47 N
IO_L20P_M5DQ0_5	G25	IOL LVDS33 14 P
IO_L20N_M5DQ1_5	G26	IOL LVDS33 14 N
IO_L21P_M5DQ8_5	K24	IOL LVDS33 64 P
IO_L21N_M5DQ9_5	K26	IOL LVDS33 64 N
IO_L22P_M5DQ10_5	J25	IOL LVDS33 31 P
IO_L22N_M5DQ11_5	J26	IOL LVDS33 31 N
IO_L23P_M5UDQS_5	M24	IOL LVDS33 32 P
IO_L23N_M5UDQSN_5	M26	IOL LVDS33 32 N
IO_L24P_M5DQ12_5	L25	IOL LVDS33 48 P
IO_L24N_M5DQ13_5	L26	IOL LVDS33 48 N
IO_L25P_M5DQ14_5	N25	IOL LVDS33 65 P
IO_L25N_M5DQ15_5	N26	IOL LVDS33 65 N
IO_L26N_VREF_5	L20	IOL LVDS33 63 P
IO_L27P_5	H19	IOL LVDS33 16 P
IO_L27N_5	J20	IOL LVDS33 16 N

TITLE: Adapter
Bank4 and Bank5 of U2, VCCO: 3.3V

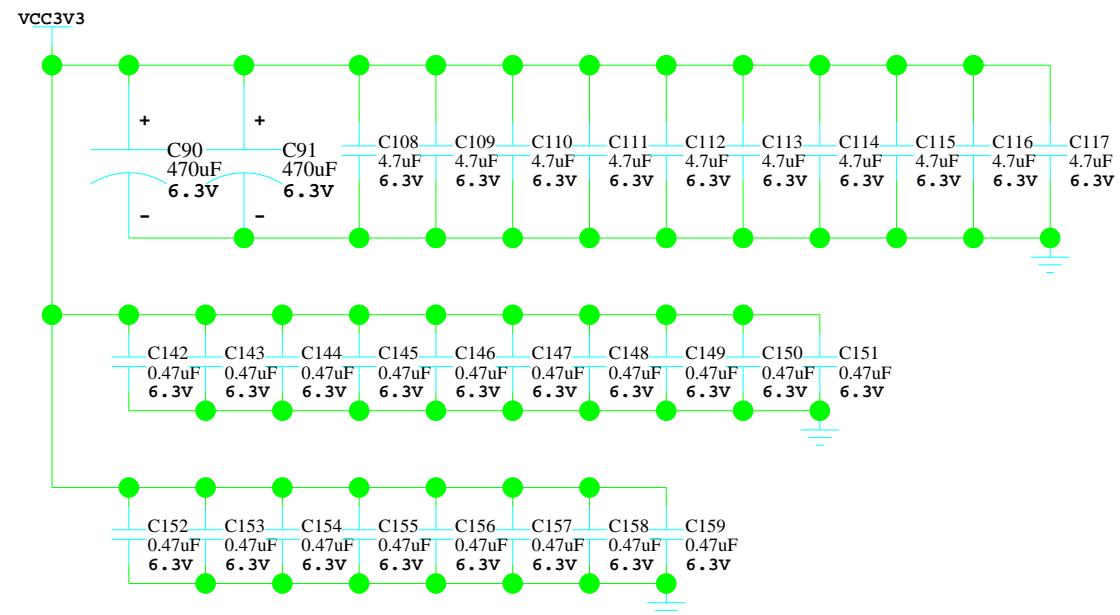
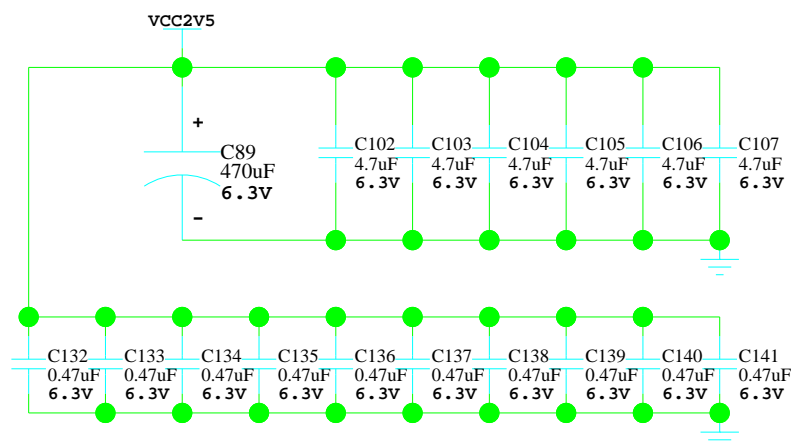
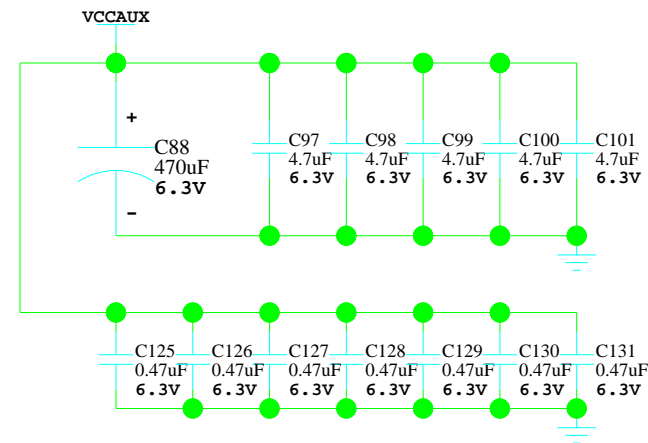
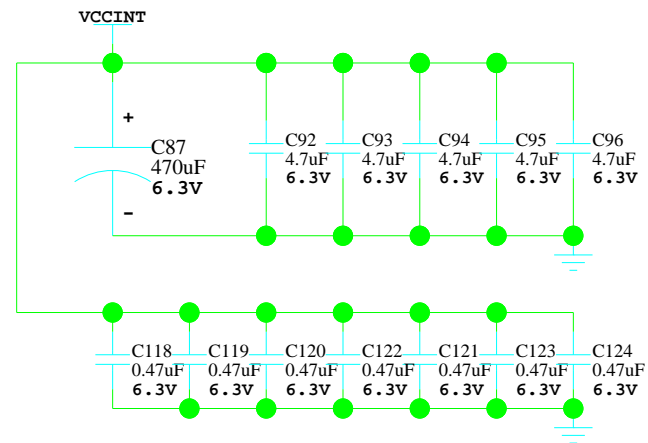
Date:	30-03-2011_16:34	DWG:	Adapter/Termination Board
Sheet Size:	B	REV:	1.0
Sheet	14 of 28	Drawn By	Shiming Yang



TITLE: Adapter		Power supply inputs of U2	
Date:	30-03-2011_16:34	DWG:	Adapter/Termination Board
Sheet Size:	B	REV:	1.0
Sheet	15 of 28	Drawn By	Shiming Yang

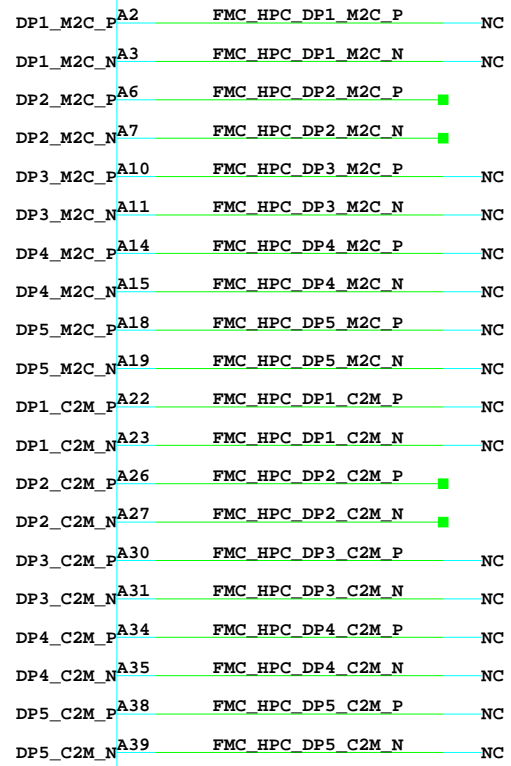


TITLE: Adapter	
Ground, Configuration inputs of U2	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 16 of 28	Drawn By Shiming Yang



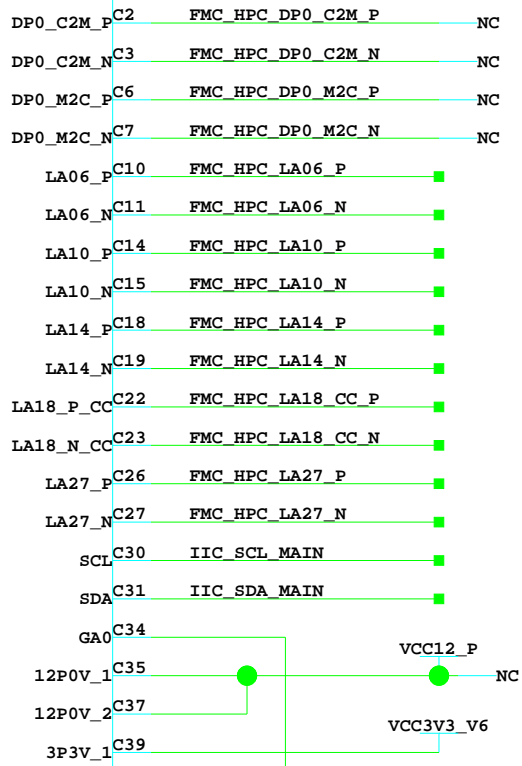
TITLE: Adapter	
Decoupling capacitors of U2	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 17 of 28	Drawn By Shiming Yang

J64



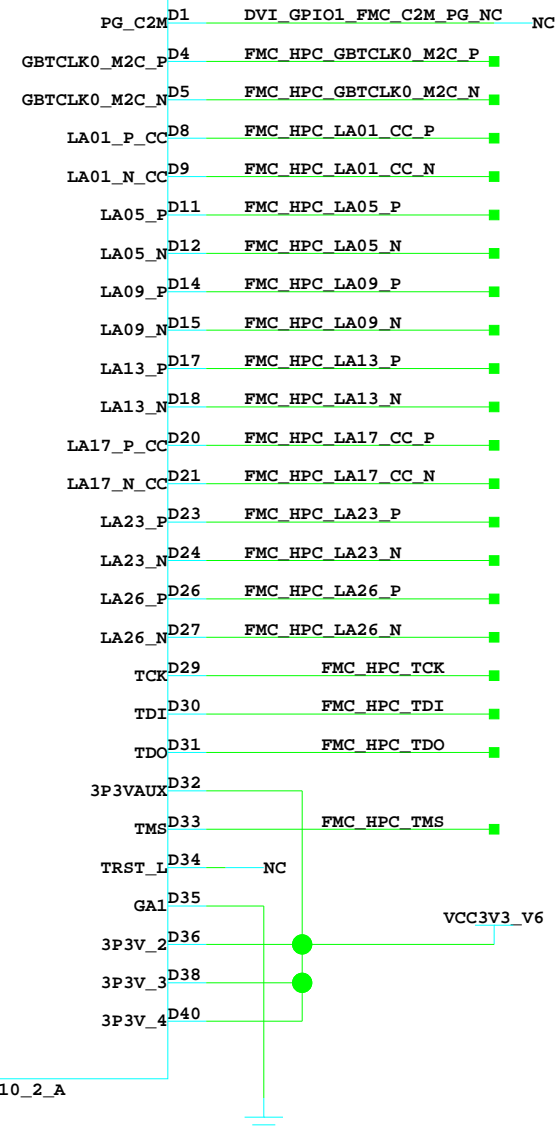
SEAM_40_035_s_10_2_A

J64



SEAM_40_035_s_10_2_A

J64



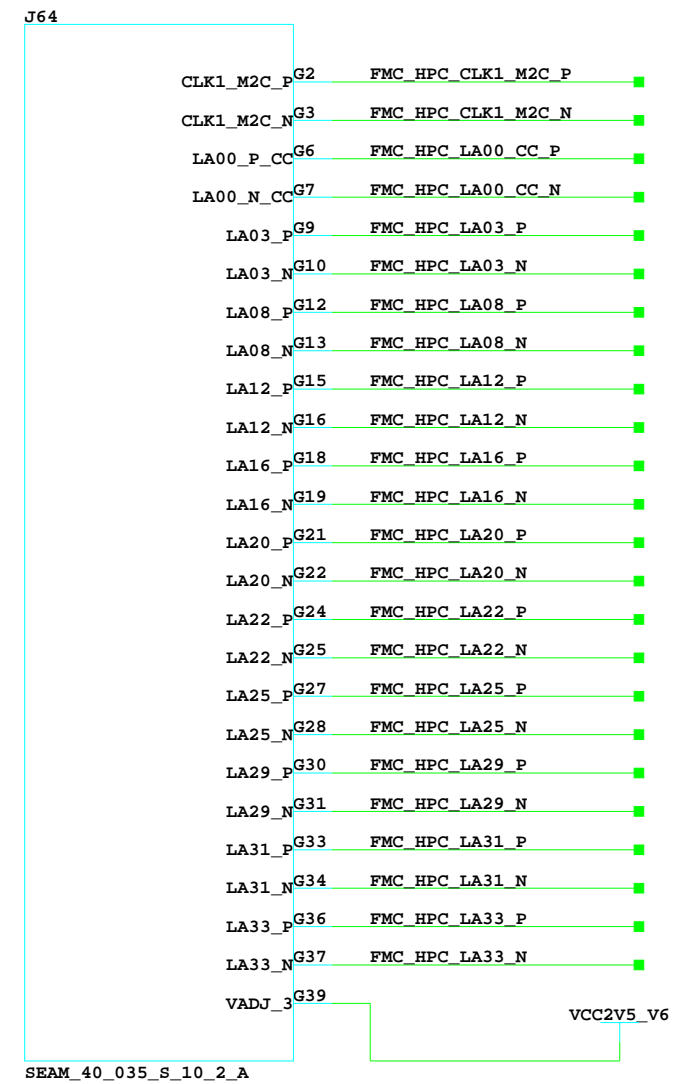
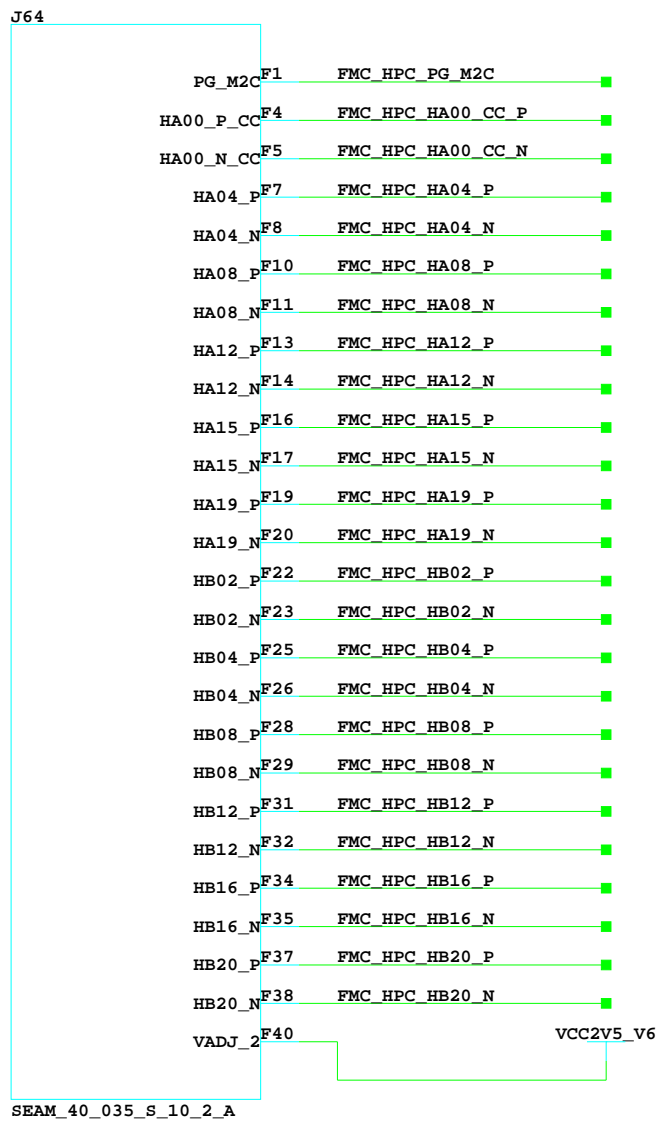
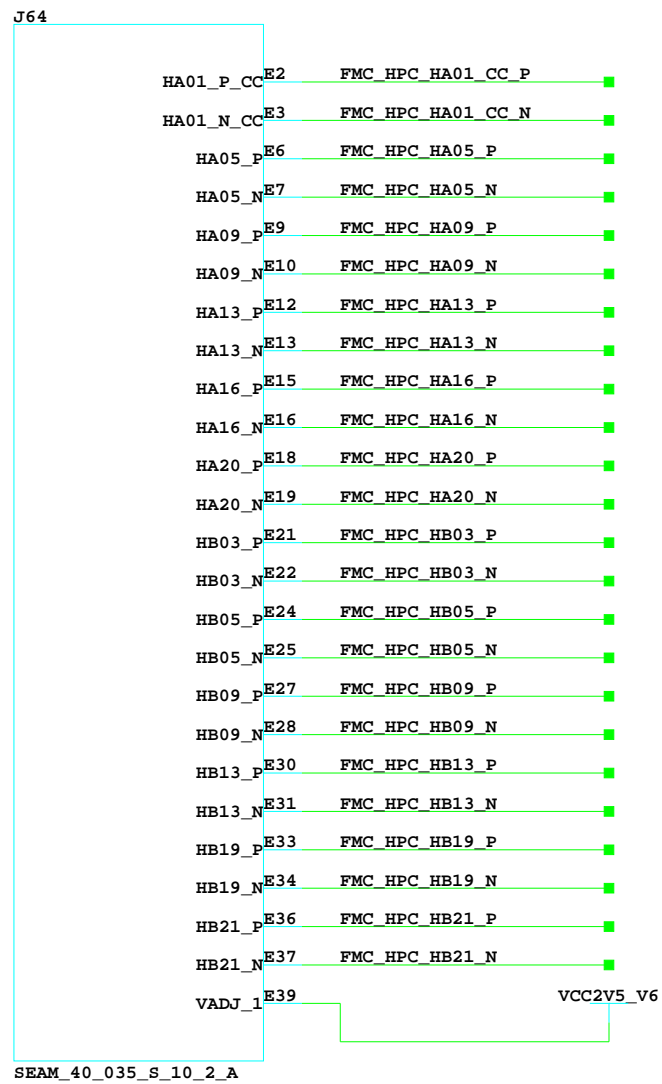
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J64

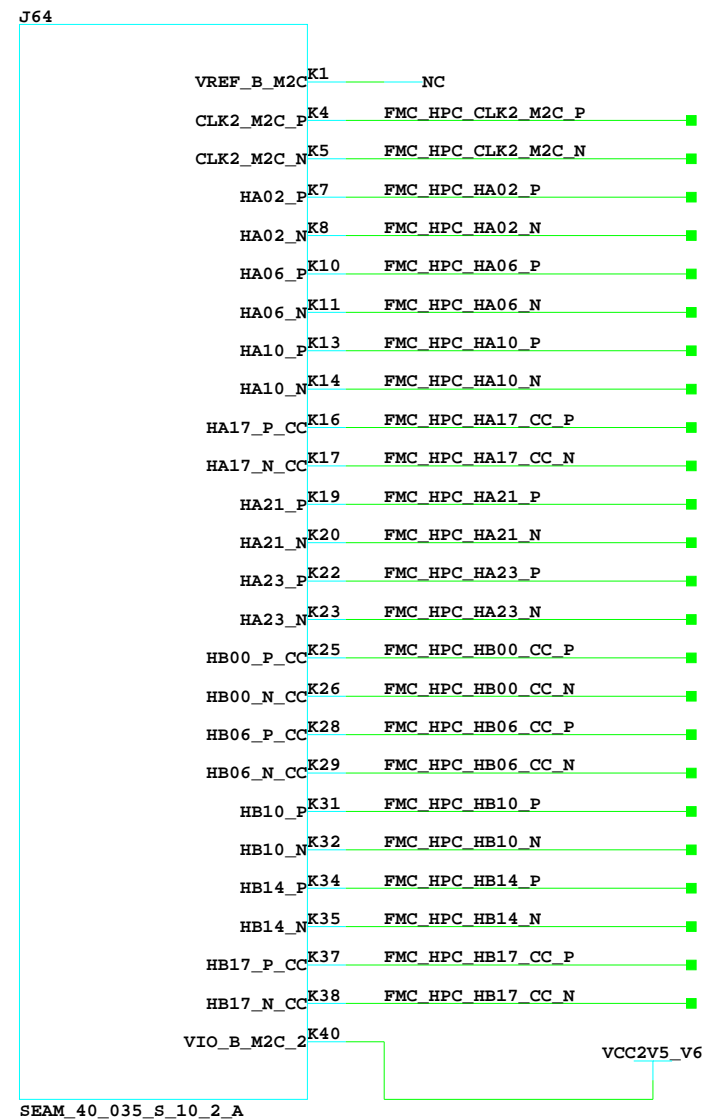
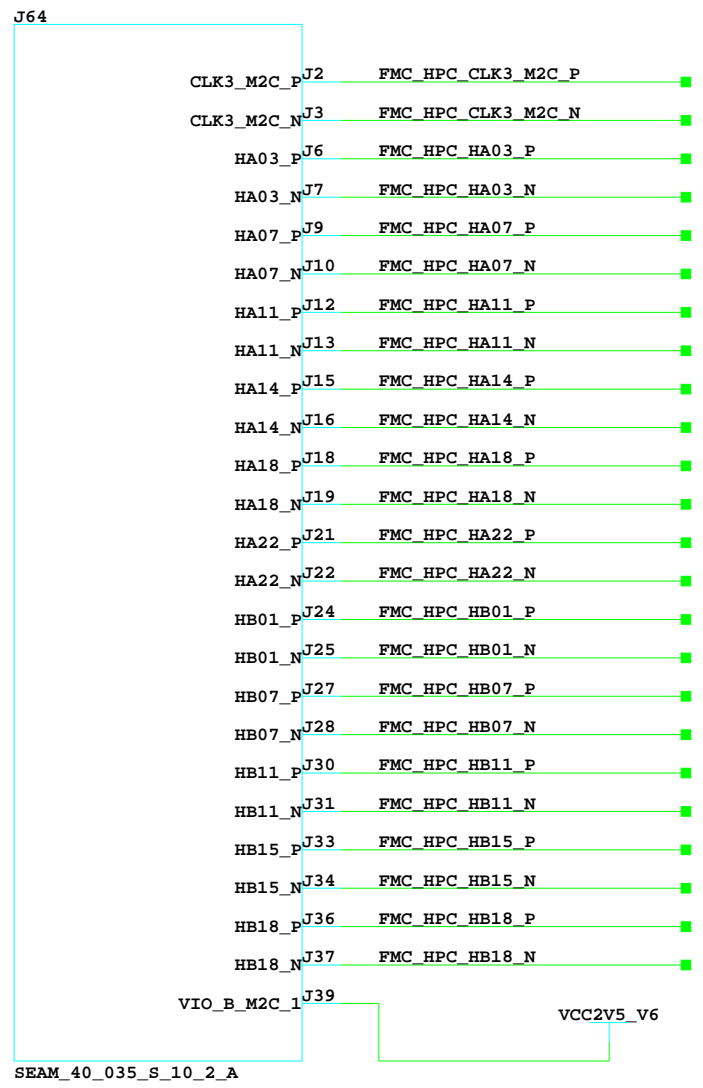
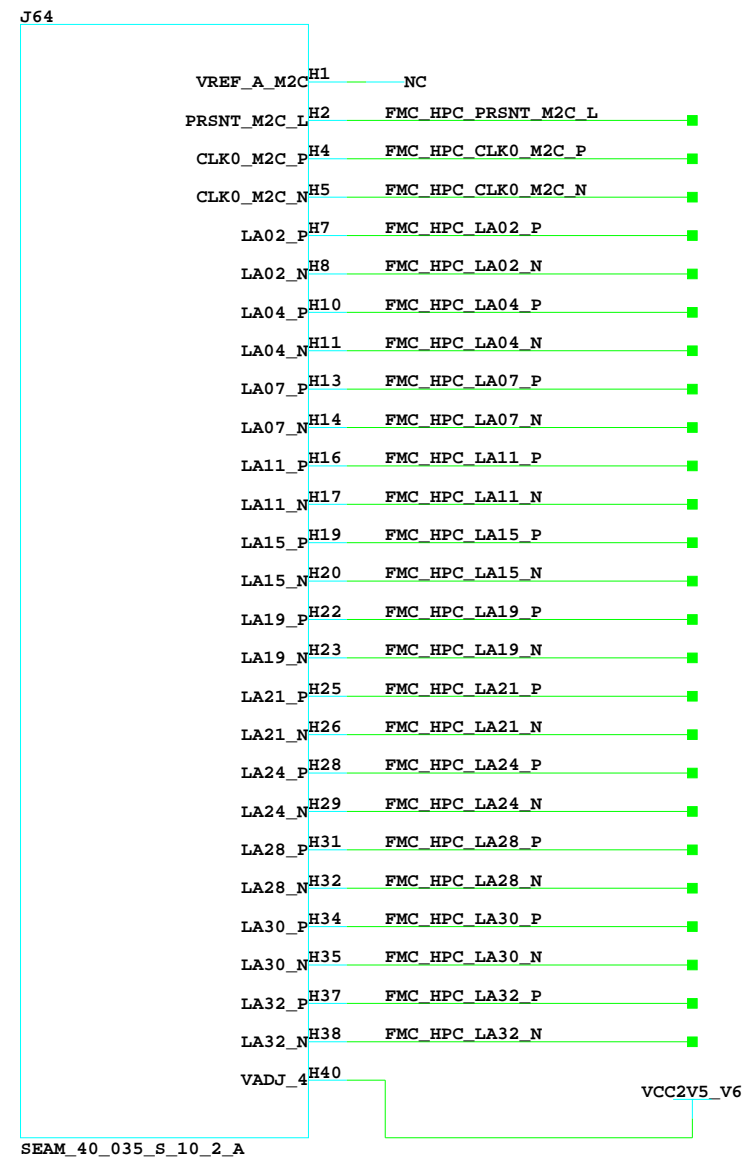


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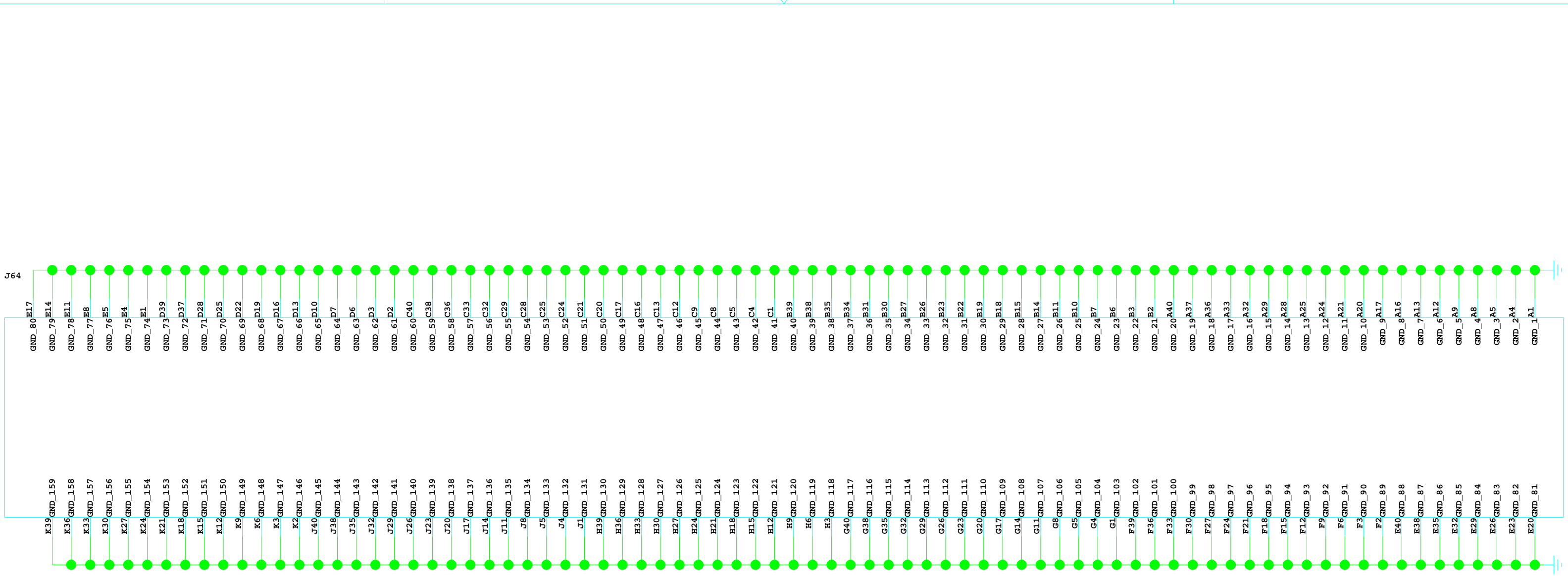
TITLE: Adapter FMC_HPC_Header Rows A,B,C,D	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 18 of 28	Drawn By Shiming Yang



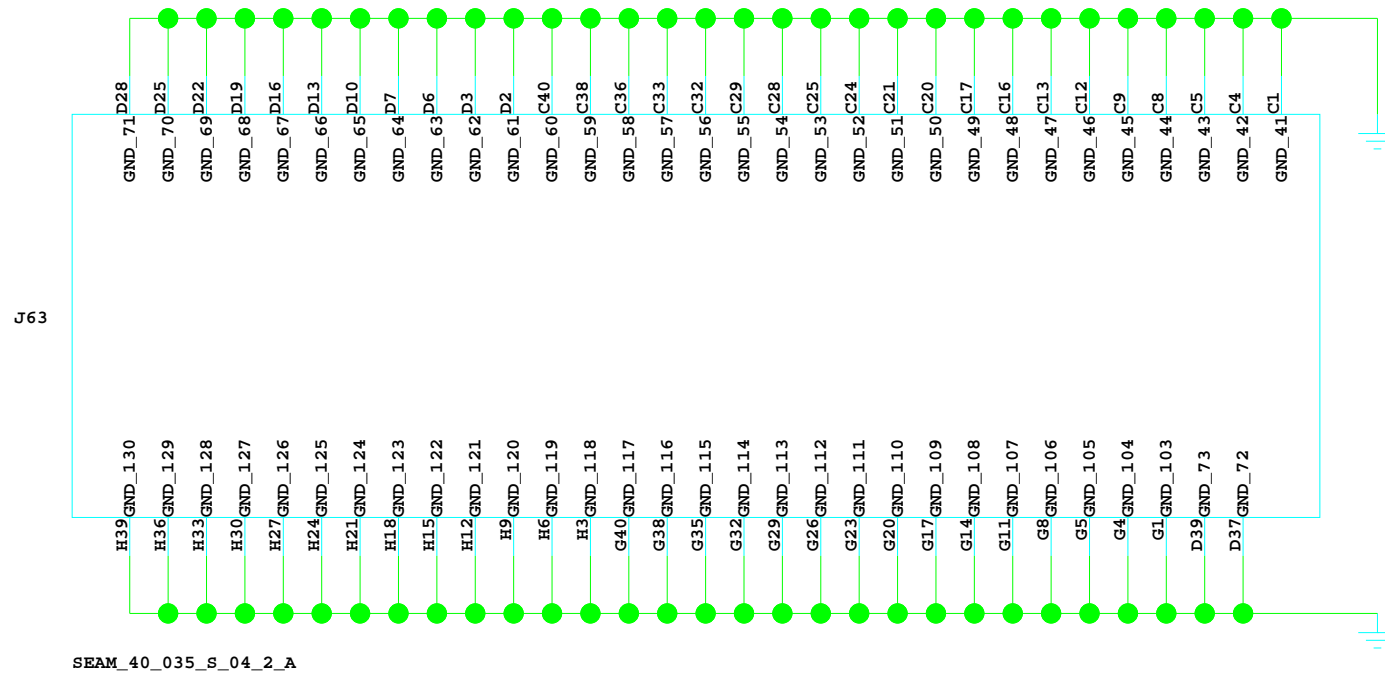
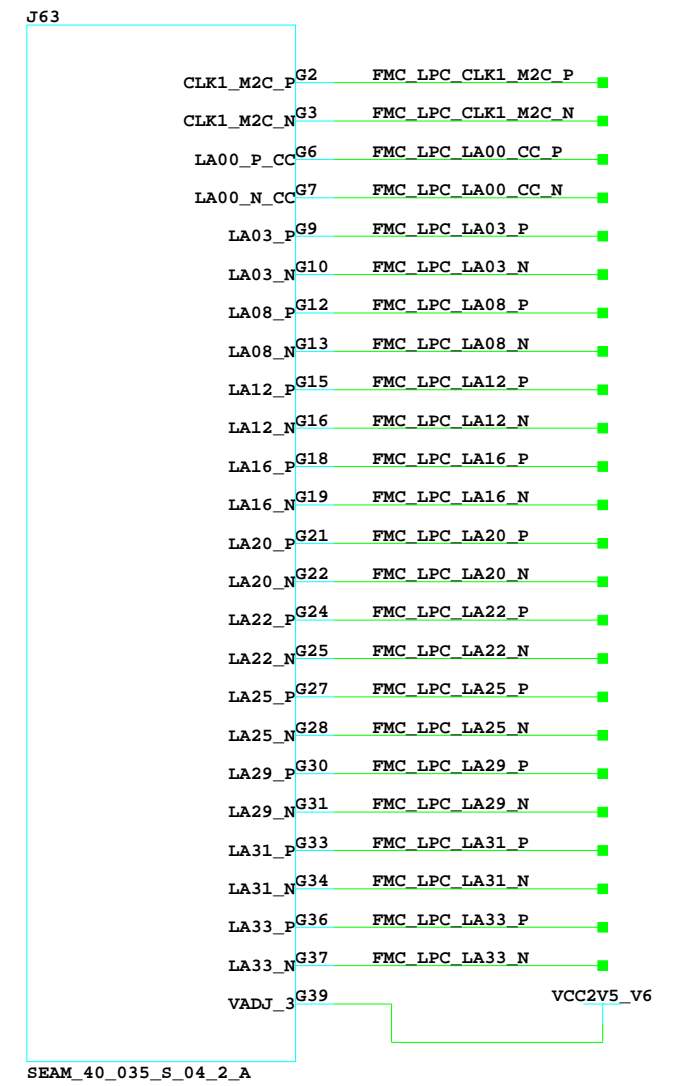
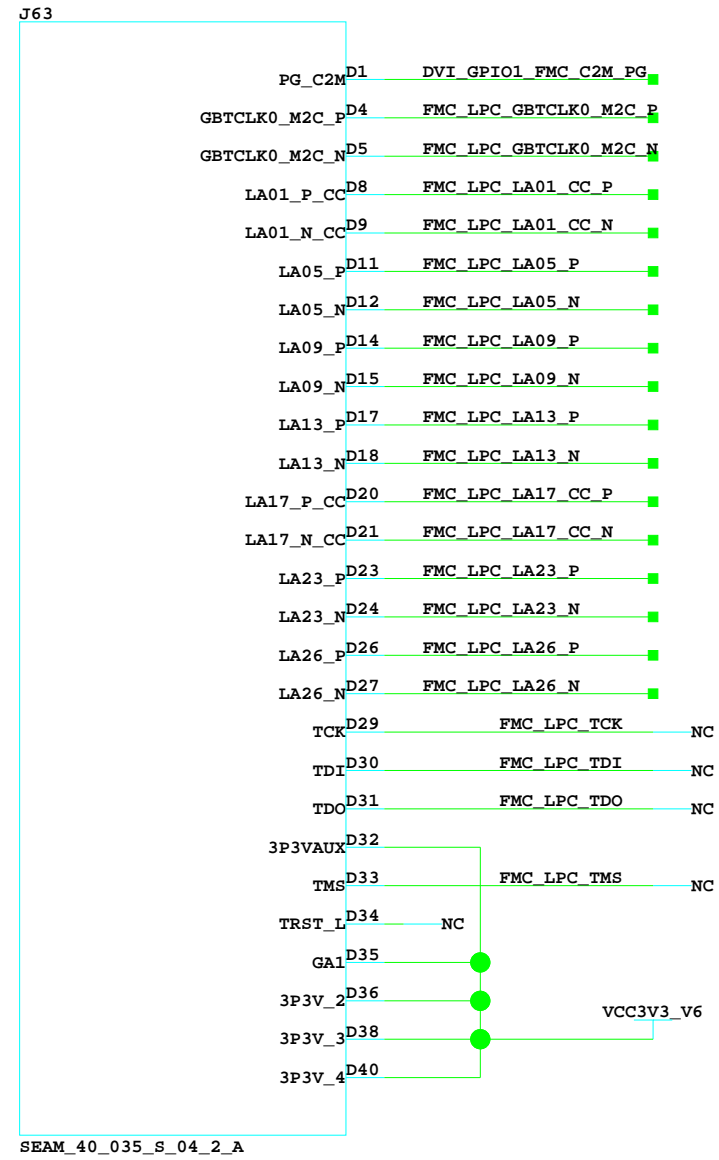
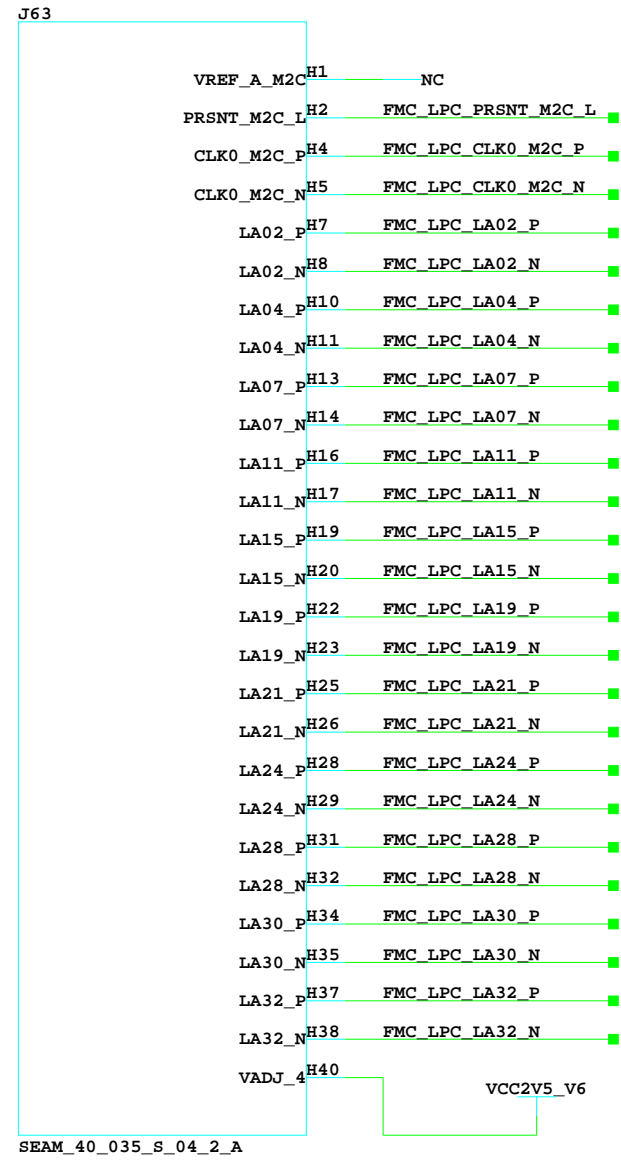
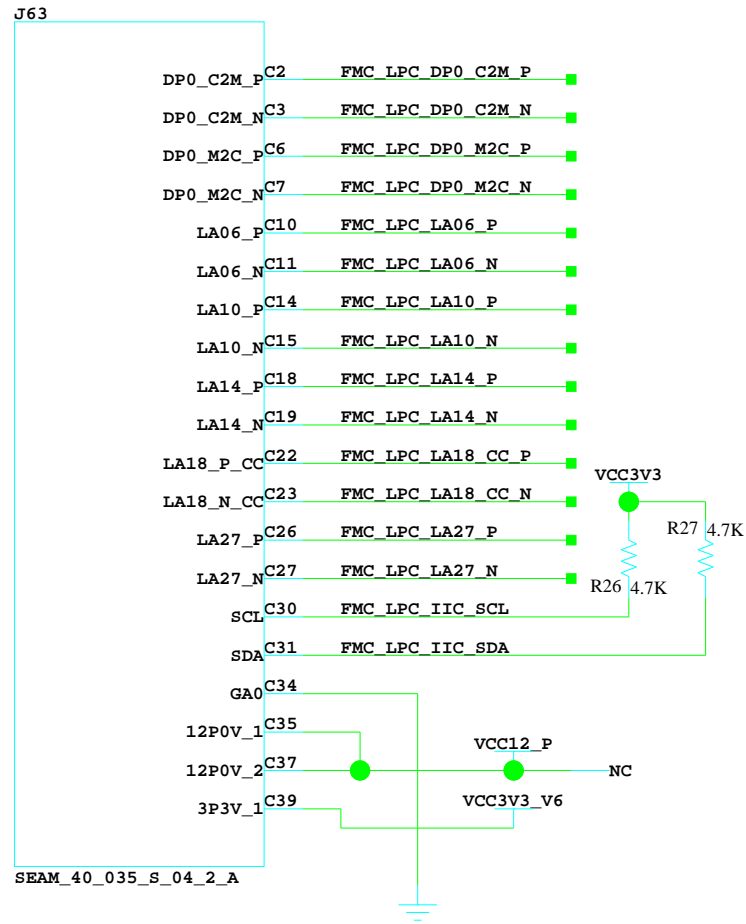
TITLE: Adapter FMC_HPC_Header Rows E,F,G	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 19 of 28	Drawn By Shiming Yang



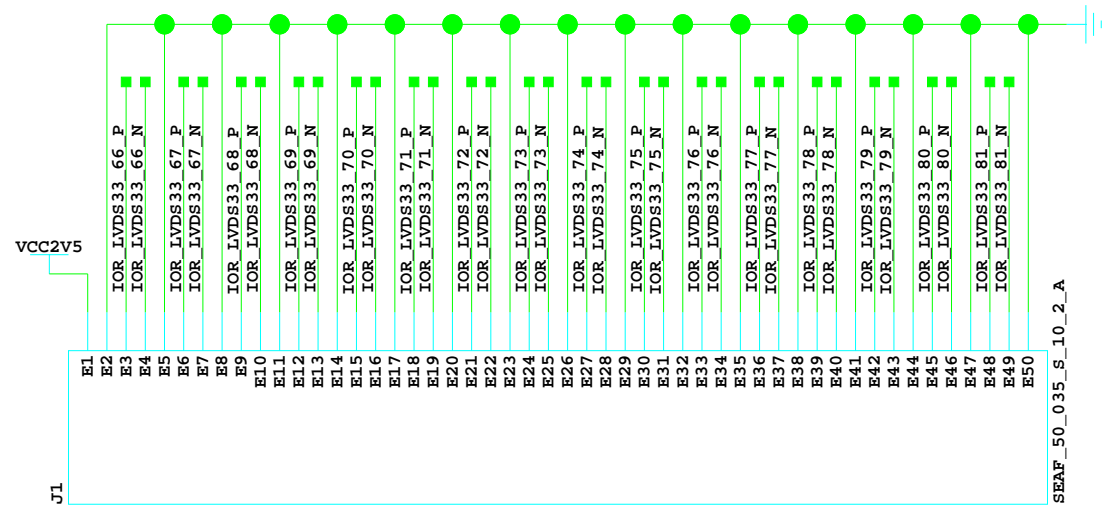
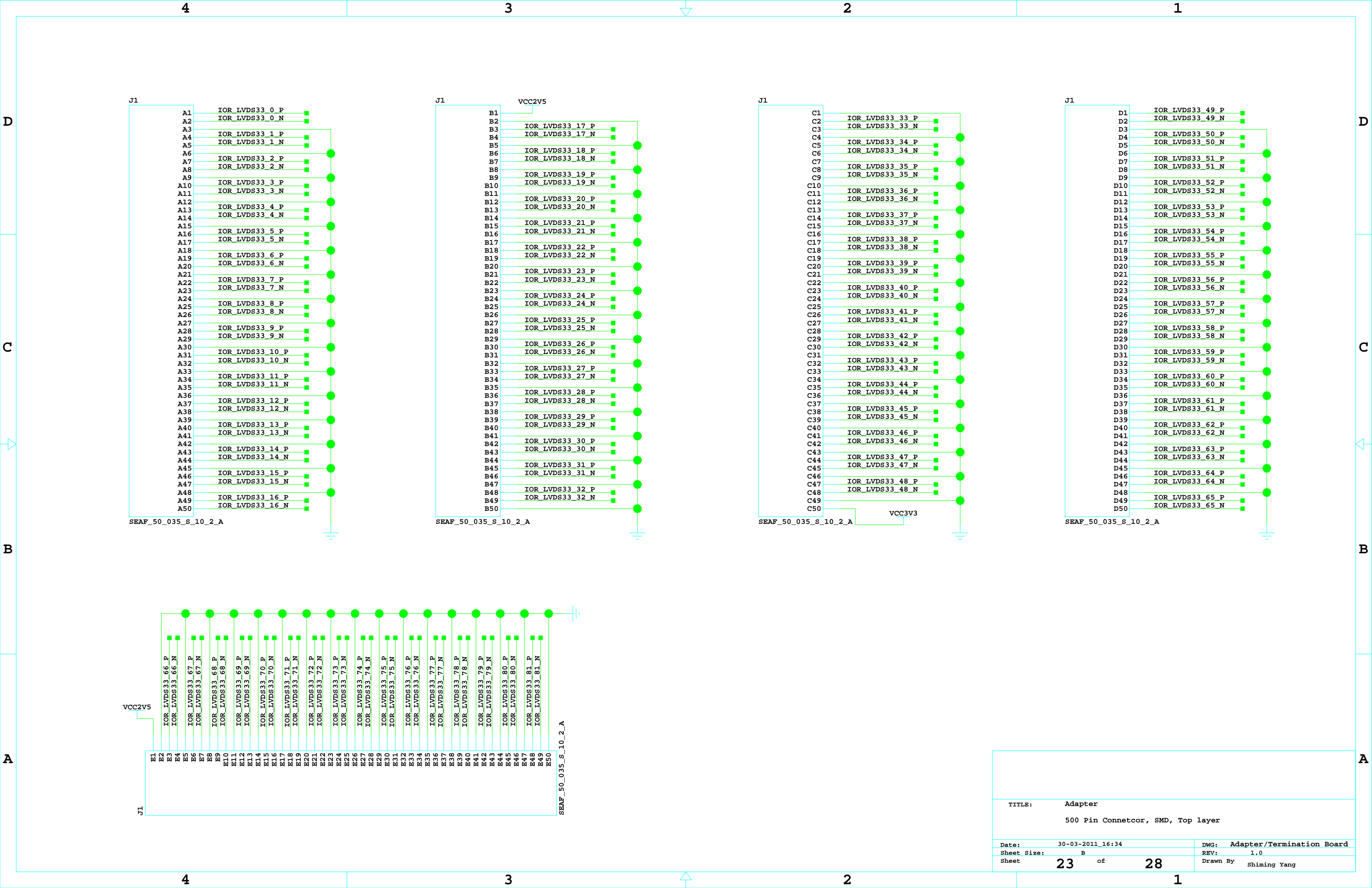
TITLE: Adapter FMC_HPC_Header Rows H,J,K	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 20 of 28	Drawn By Shiming Yang



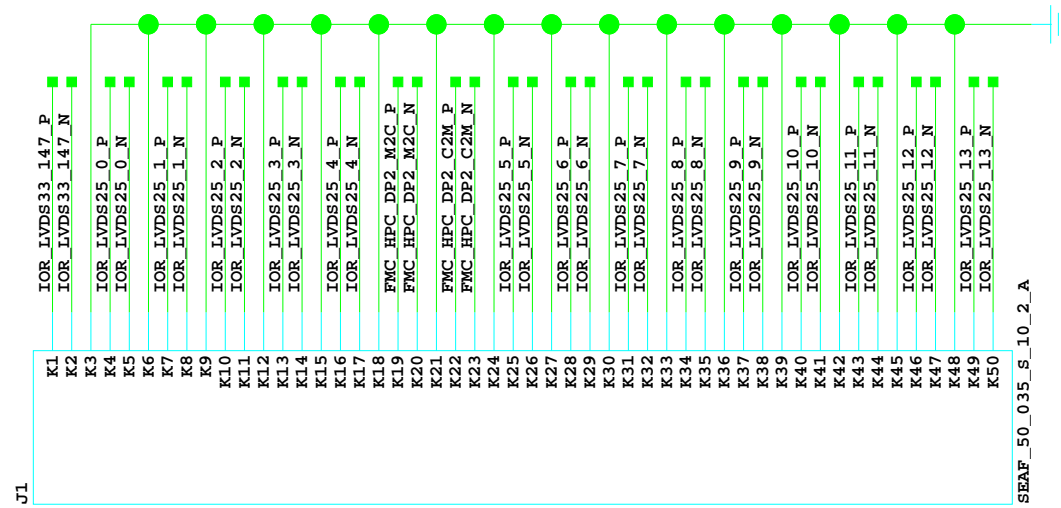
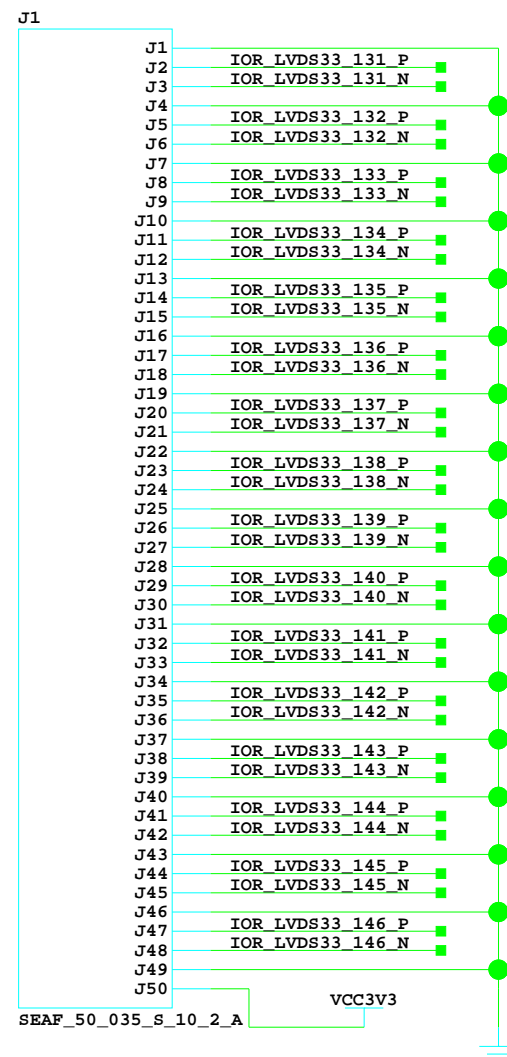
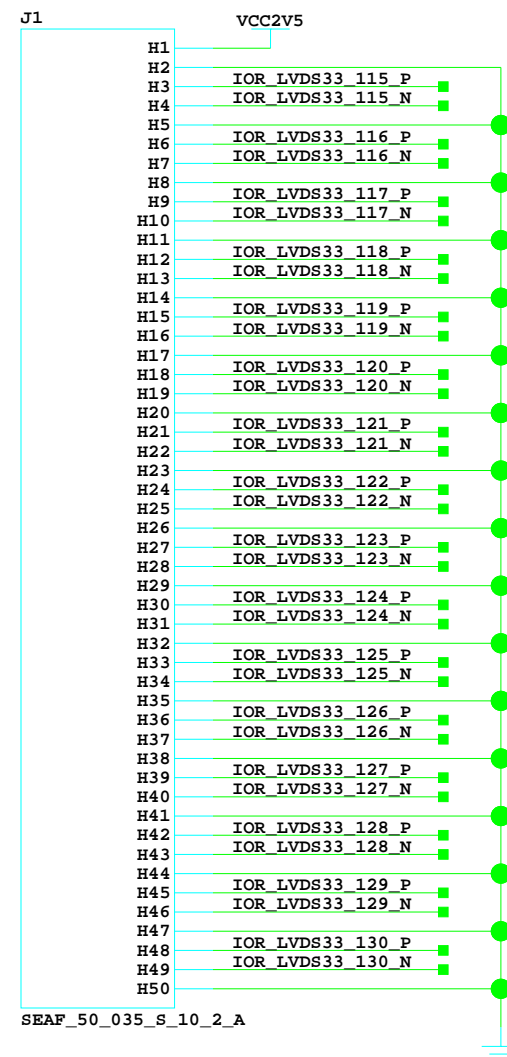
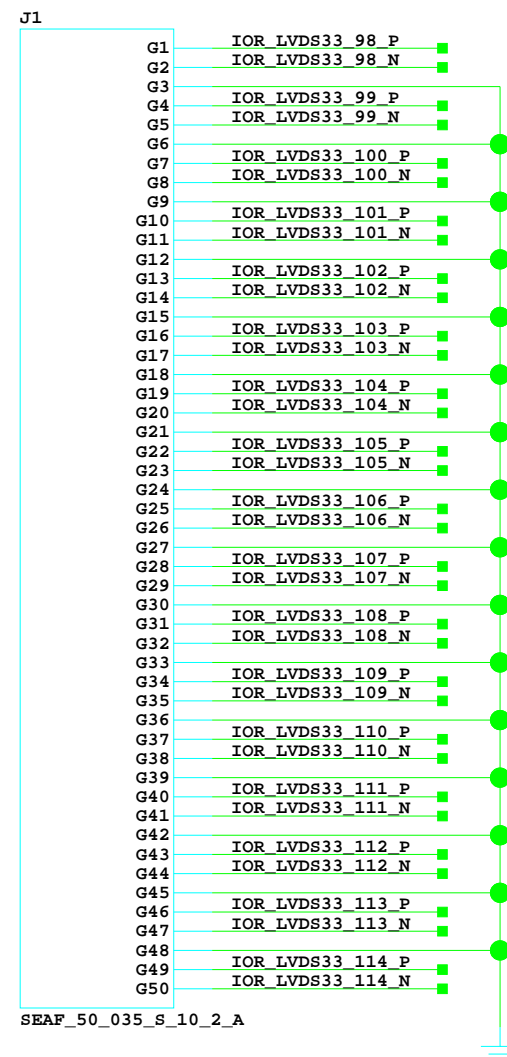
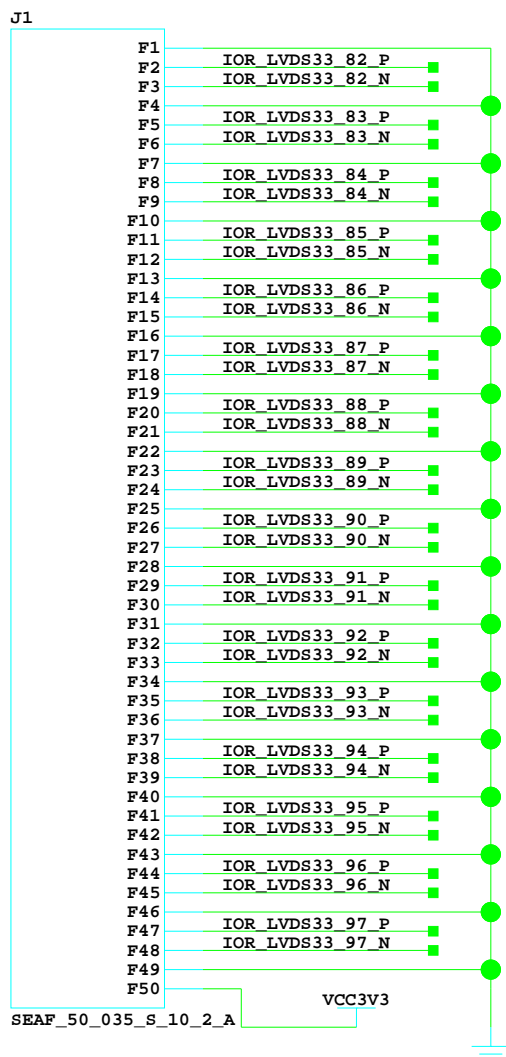
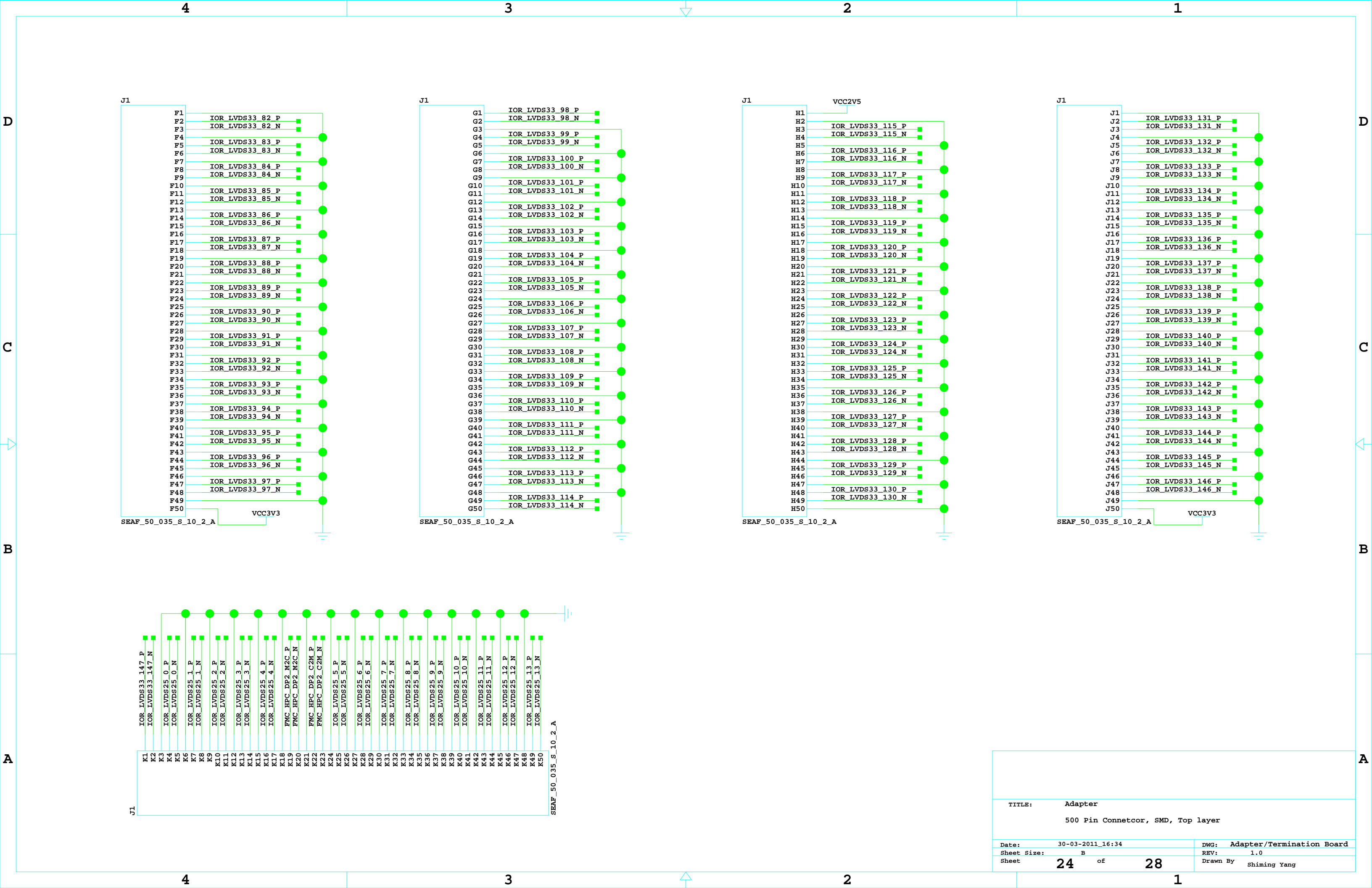
TITLE: Adapter FMC_HPC_Header GND Connections	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 21 of 28	Drawn By Shiming Yang



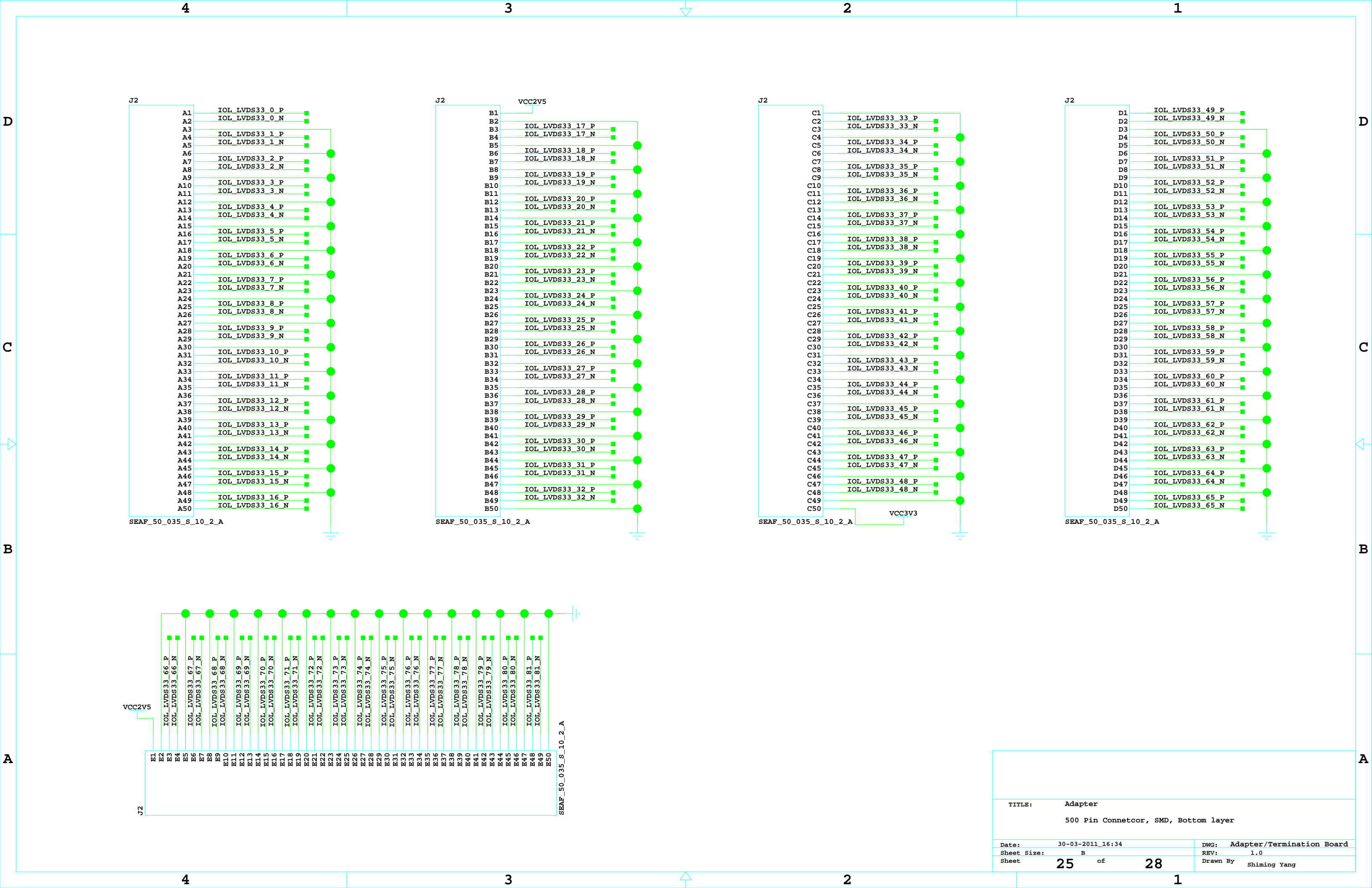
TITLE: Adapter FMC_LPC_Connector Rows C,D,G,H	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 22 of 28	Drawn By Shiming Yang



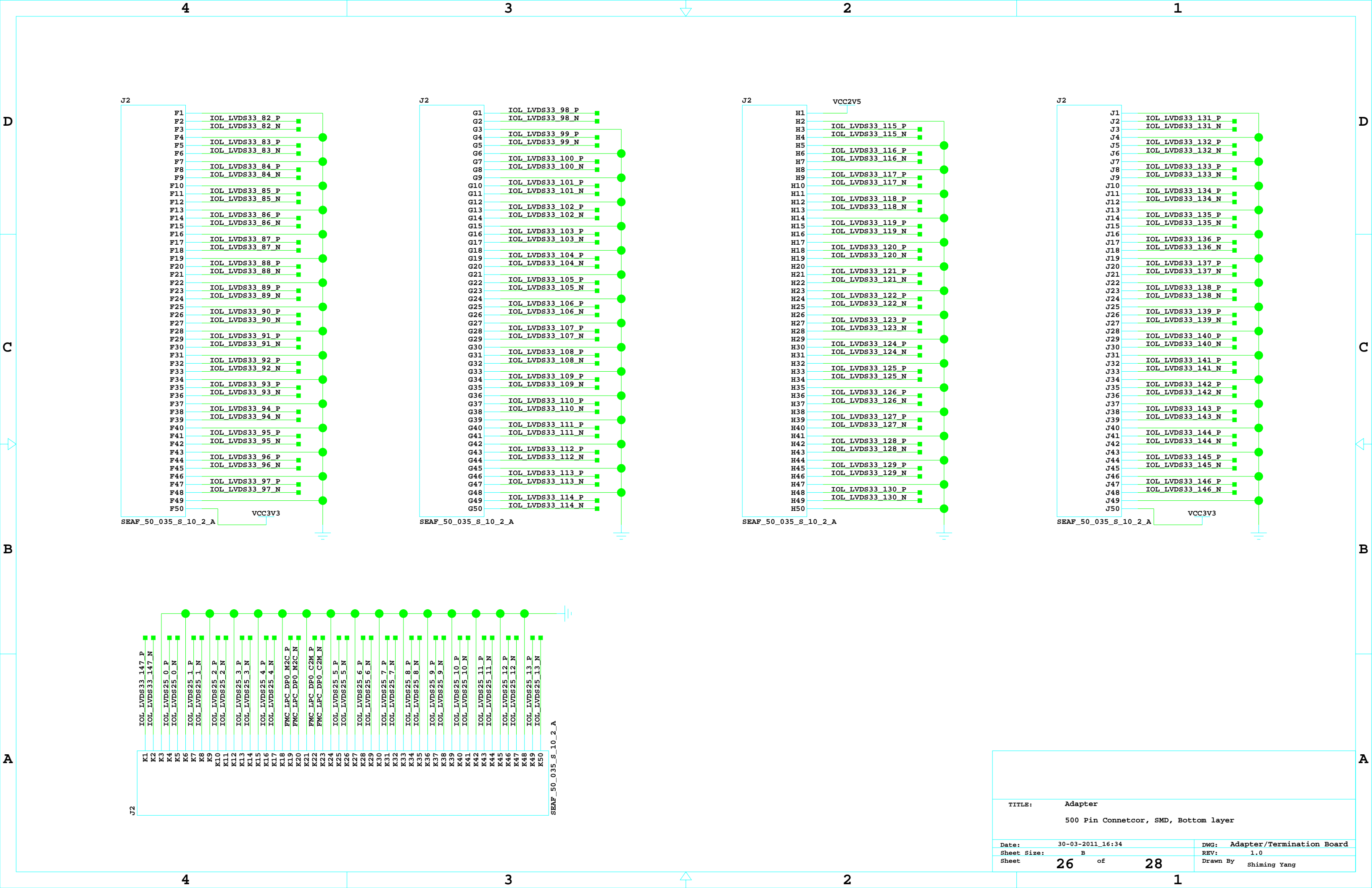
TITLE: Adapter	
500 Pin Connetcor, SMD, Top layer	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 23 of 28	Drawn By Shiming Yang



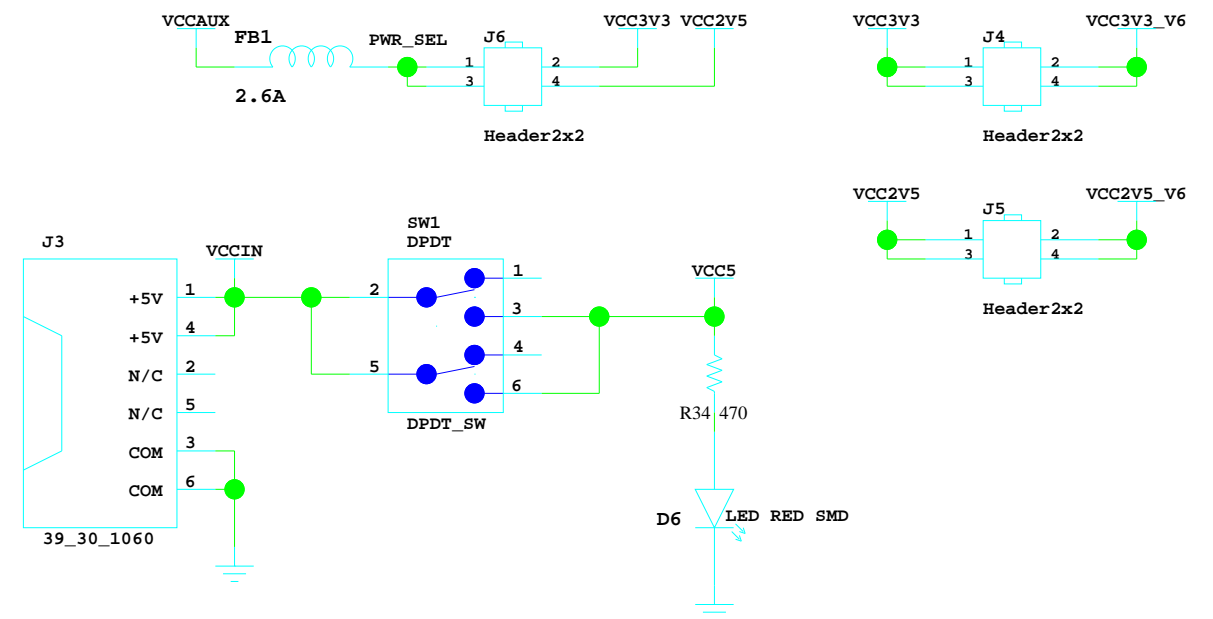
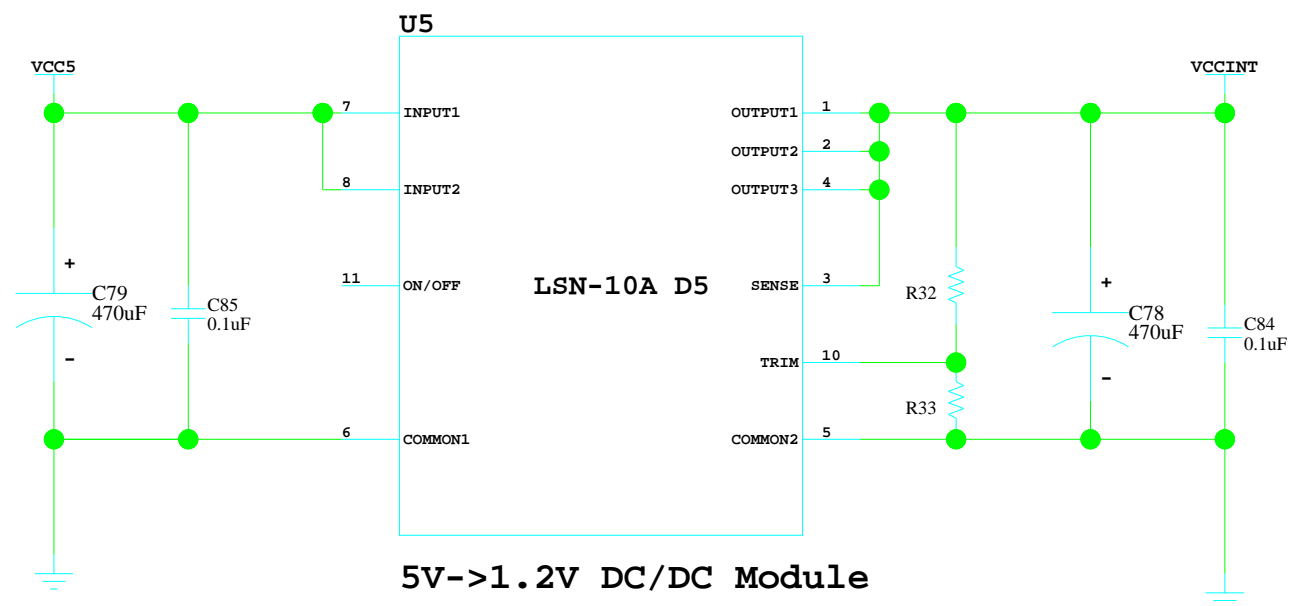
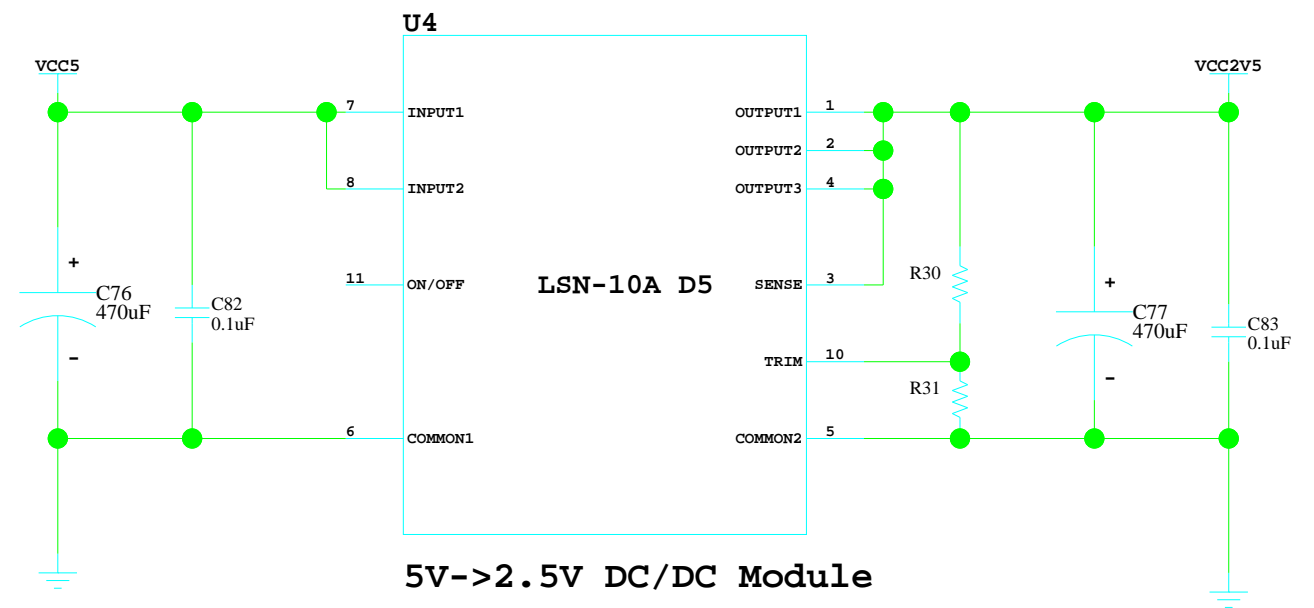
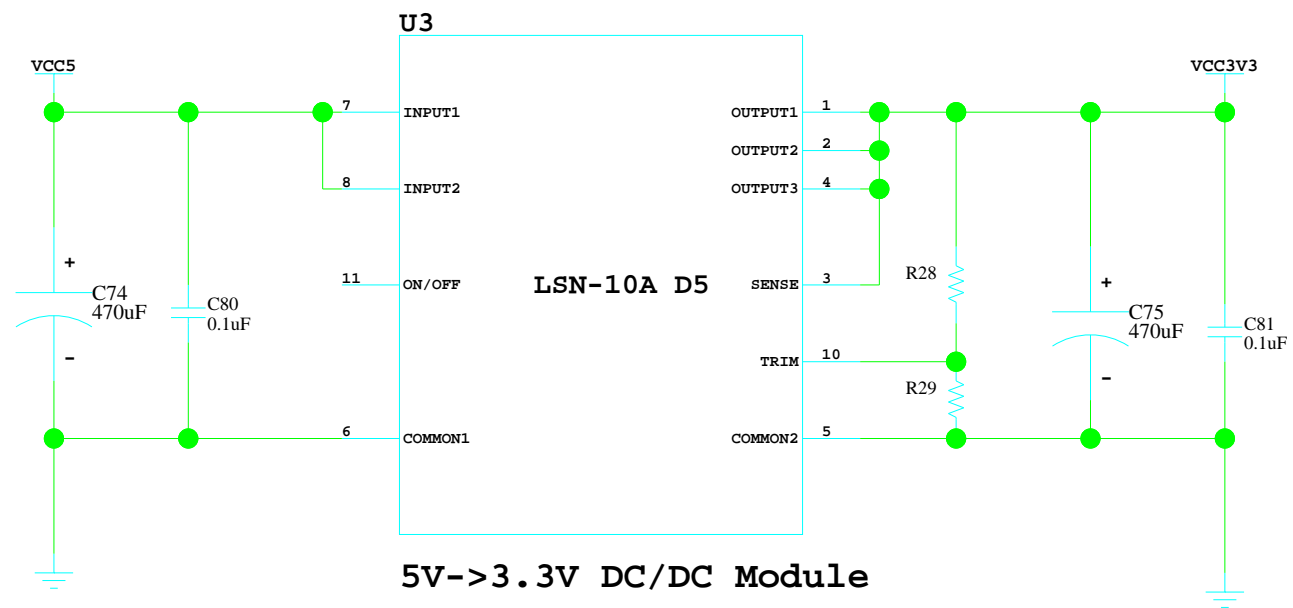
TITLE: Adapter	
500 Pin Connetcor, SMD, Top layer	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 24 of 28	Drawn By Shiming Yang



TITLE: Adapter	
500 Pin Connetcor, SMD, Bottom layer	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 25 of 28	Drawn By Shiming Yang

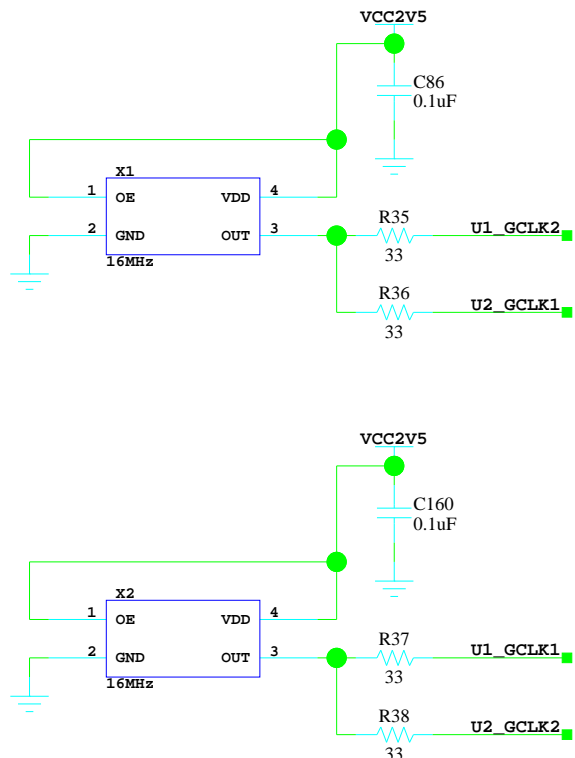


TITLE: Adapter	
500 Pin Connetcor, SMD, Bottom layer	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 26 of 28	Drawn By Shiming Yang

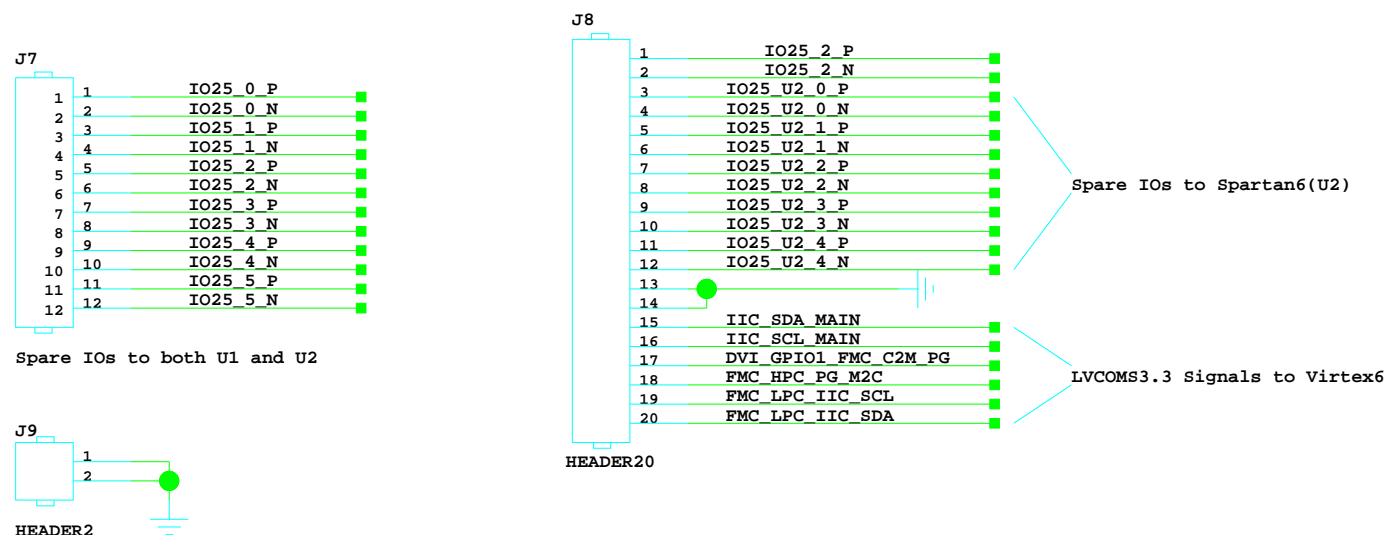


TITLE: Adapter	
Power supply modules, Connectors, Jumpers	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 27 of 28	Drawn By Shiming Yang

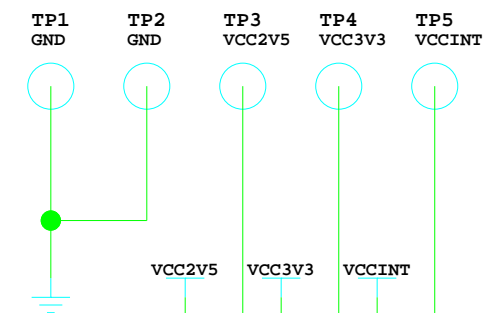
CLOCK INPUT



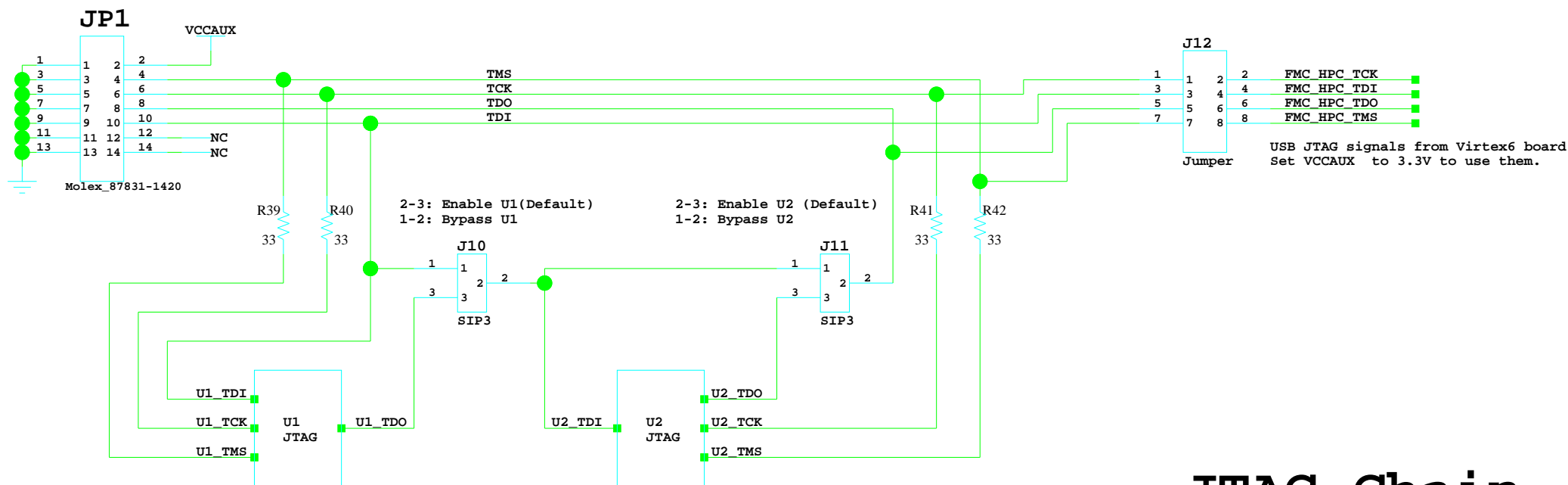
Spare Signals



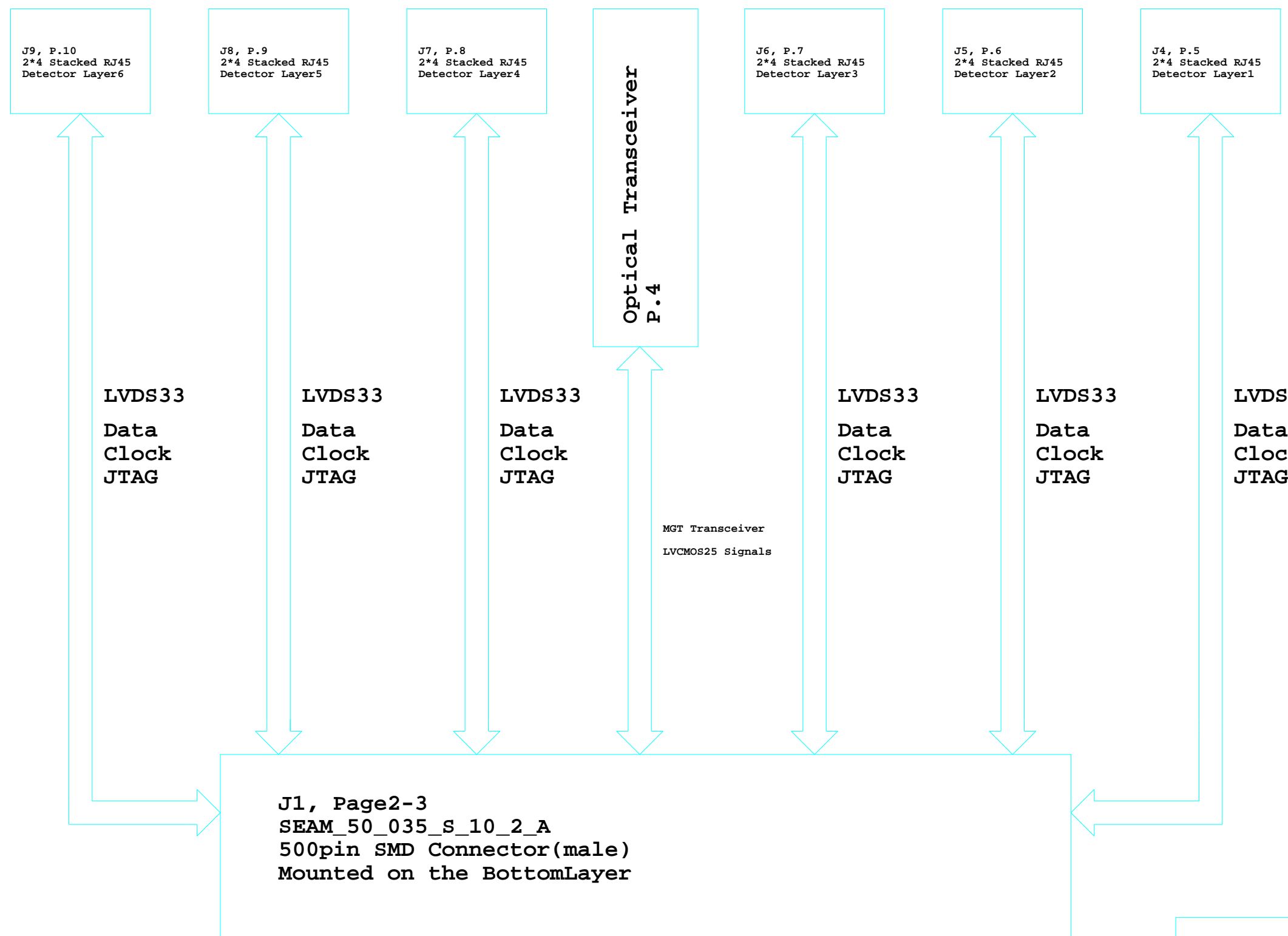
Test Points



JTAG Chain



TITLE: Adapter	
Miscellaneous circuits	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 28 of 28	Drawn By Shiming Yang



J9, P.10
2*4 Stacked RJ45
Detector Layer6

J8, P.9
2*4 Stacked RJ45
Detector Layer5

J7, P.8
2*4 Stacked RJ45
Detector Layer4

Optical Transceiver
P.4

J6, P.7
2*4 Stacked RJ45
Detector Layer3

J5, P.6
2*4 Stacked RJ45
Detector Layer2

J4, P.5
2*4 Stacked RJ45
Detector Layer1

LVDS33
Data
Clock
JTAG

LVDS33
Data
Clock
JTAG

LVDS33
Data
Clock
JTAG

MGT Transceiver
LVCOS25 Signals

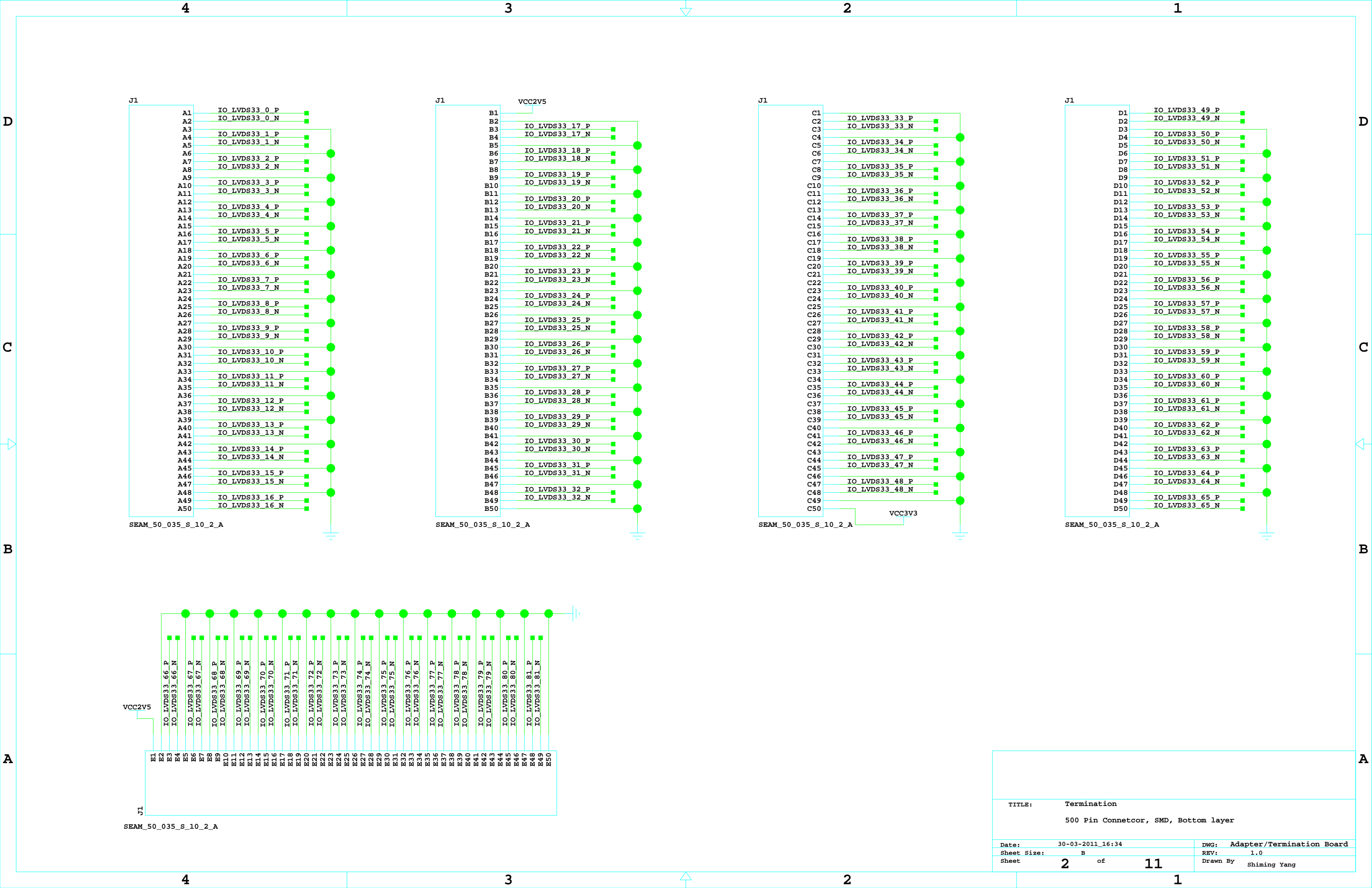
LVDS33
Data
Clock
JTAG

LVDS33
Data
Clock
JTAG

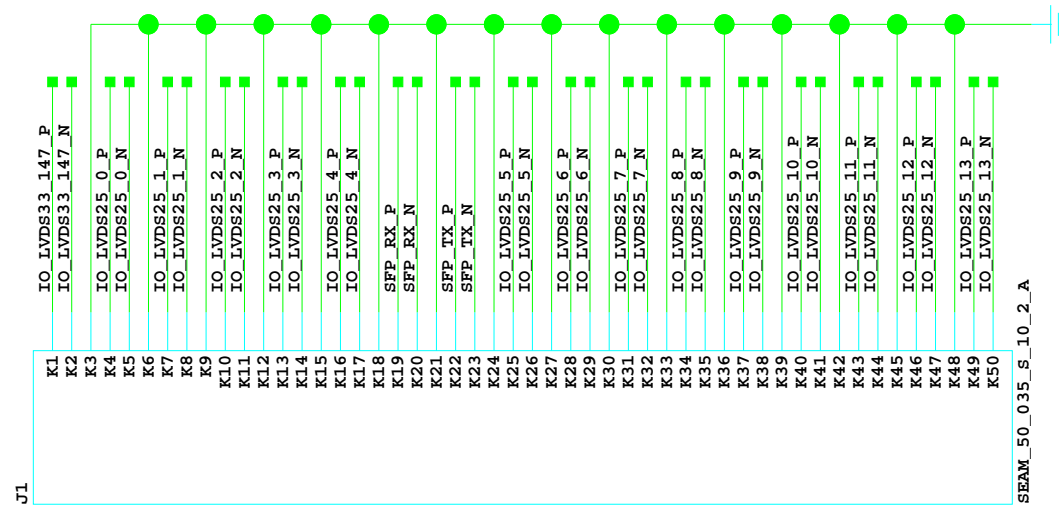
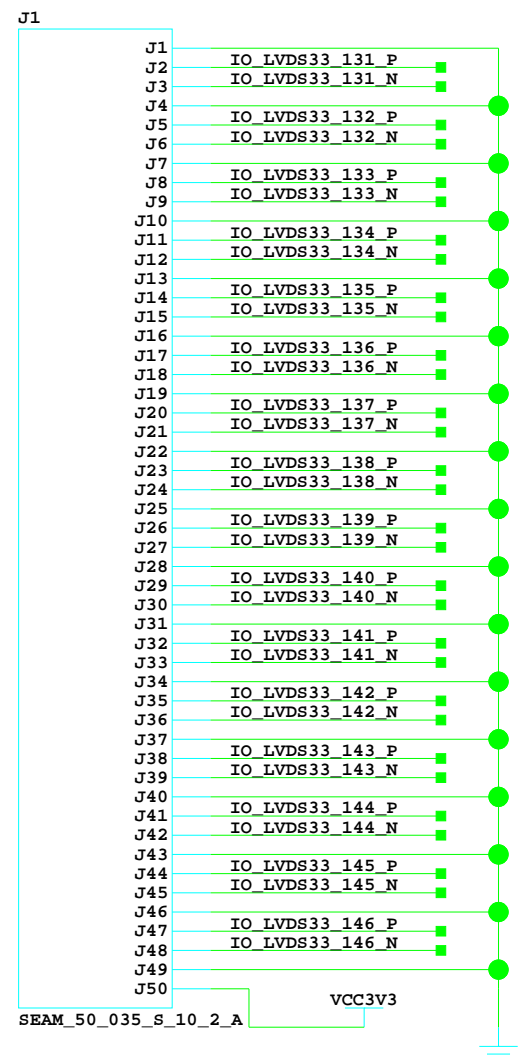
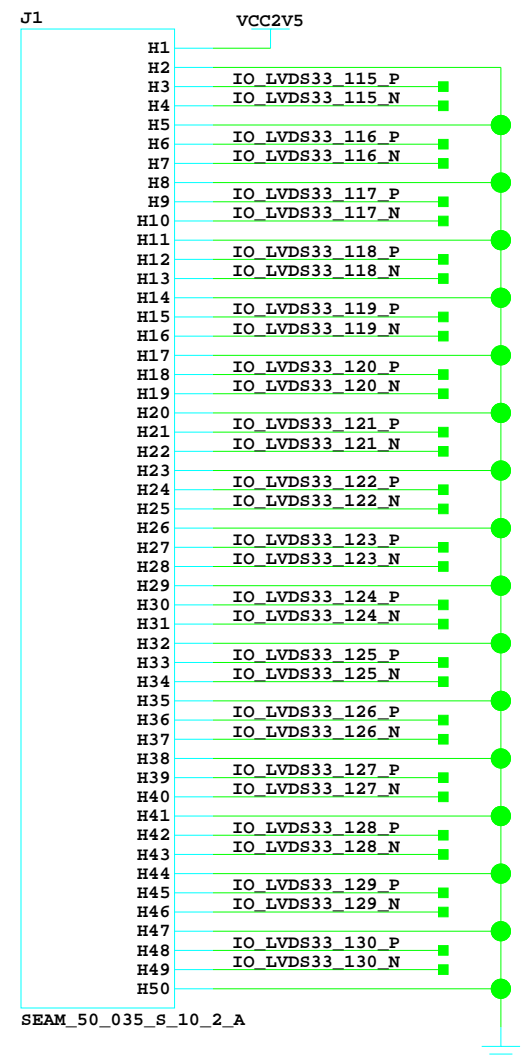
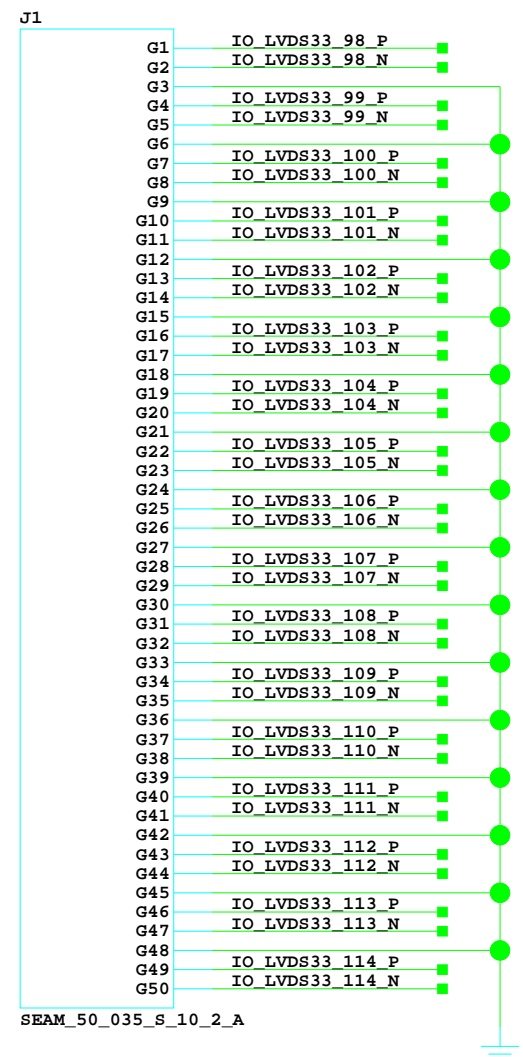
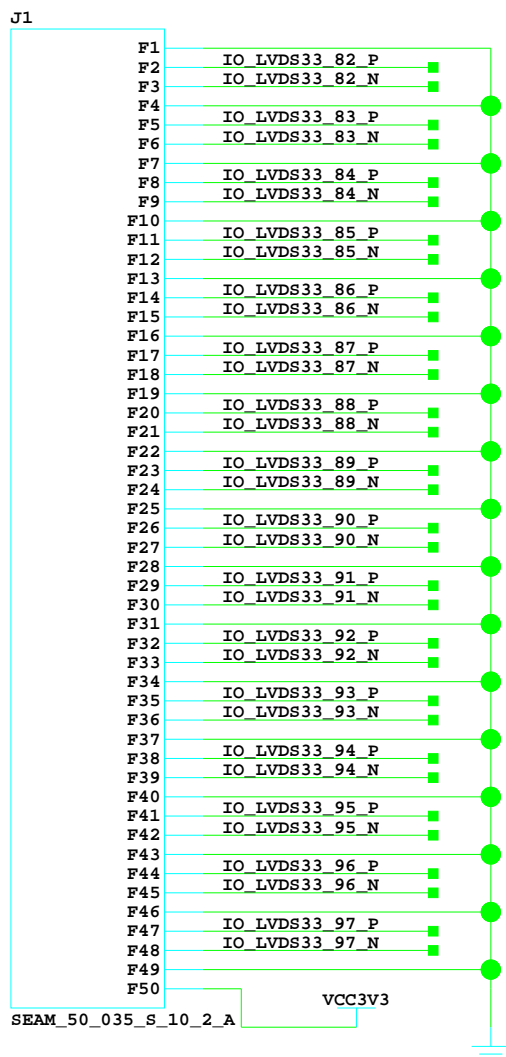
LVDS33
Data
Clock
JTAG

J1, Page2-3
SEAM_50_035_s_10_2_A
500pin SMD Connector(male)
Mounted on the BottomLayer

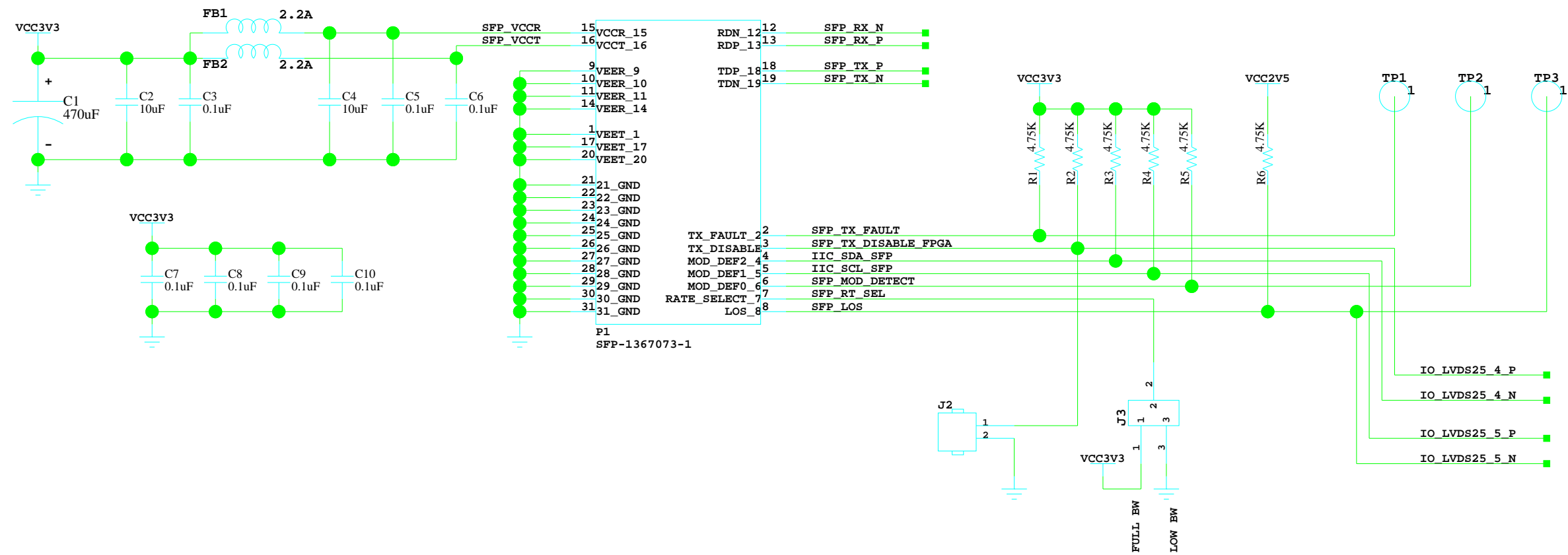
TITLE: Termination System Diagram	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 1 of 11	Drawn By Shiming Yang



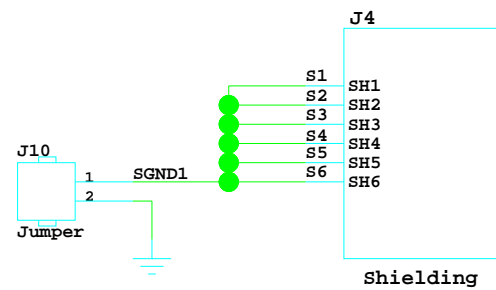
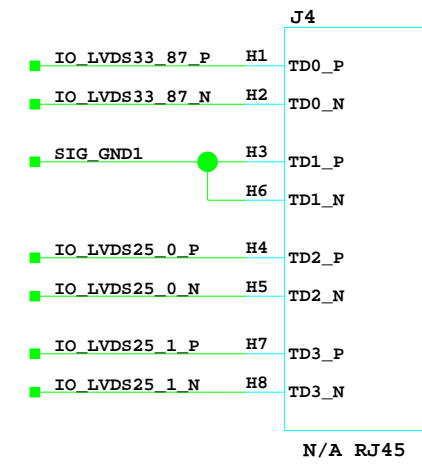
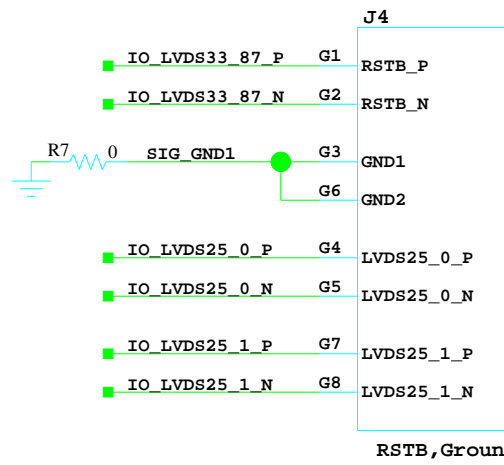
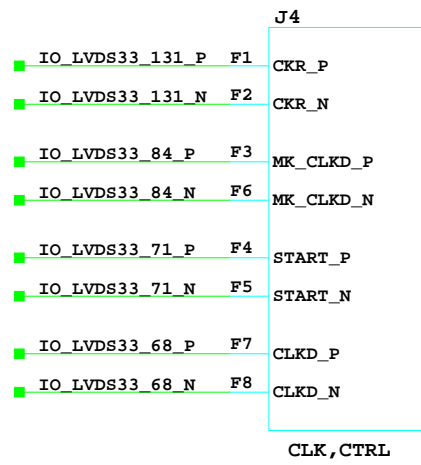
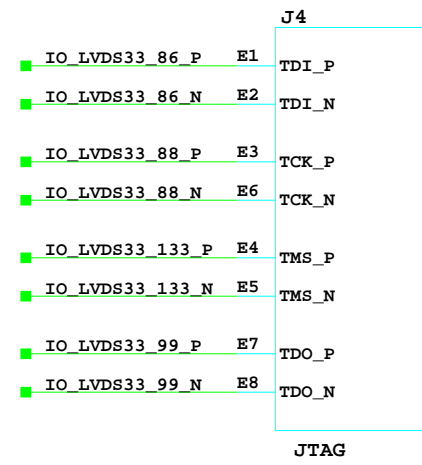
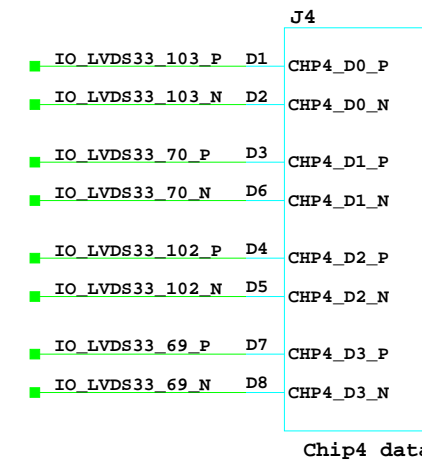
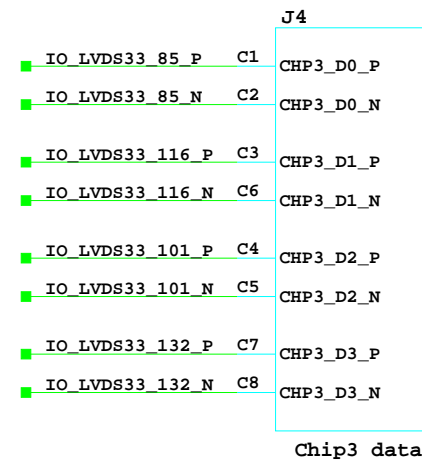
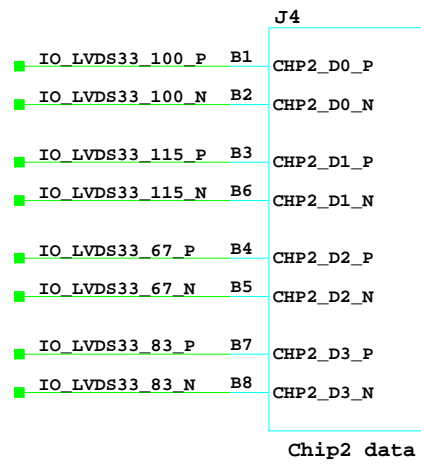
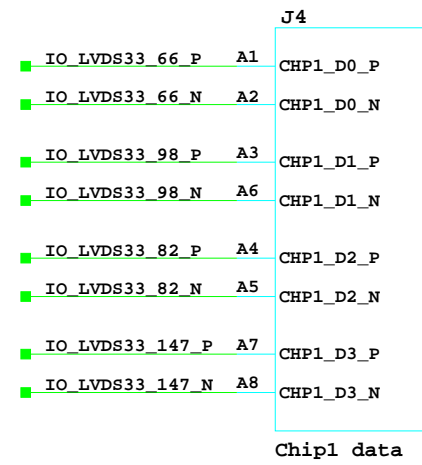
TITLE: Termination	
500 Pin Conneccor, SMD, Bottom layer	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 2 of 11	Drawn By Shiming Yang



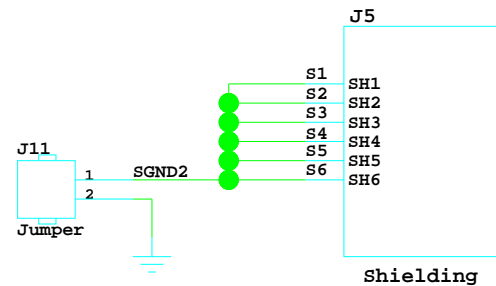
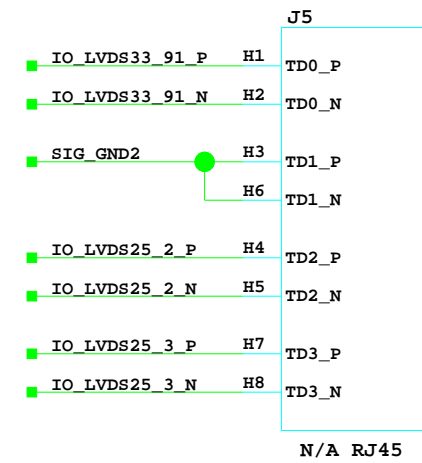
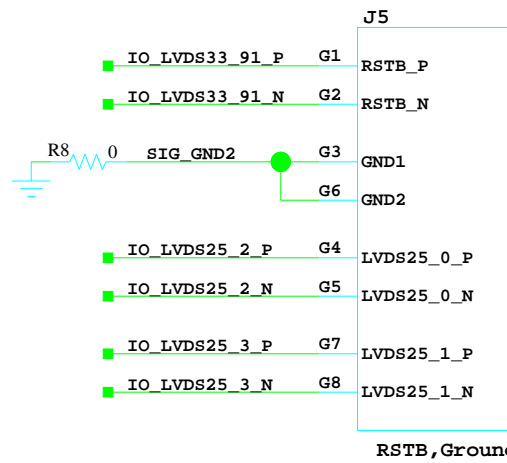
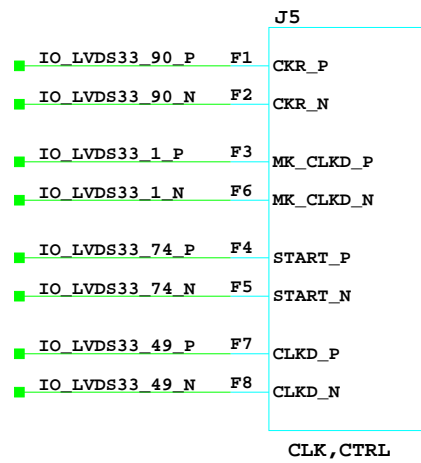
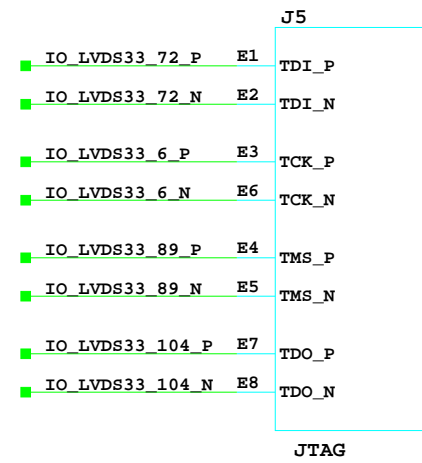
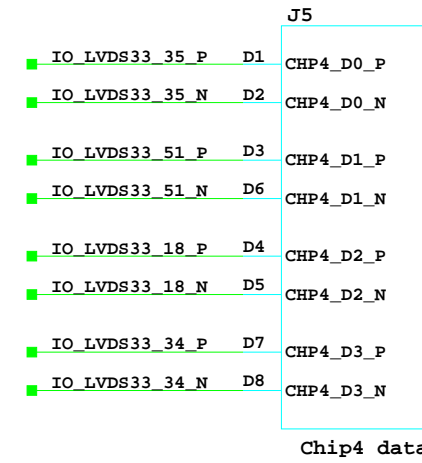
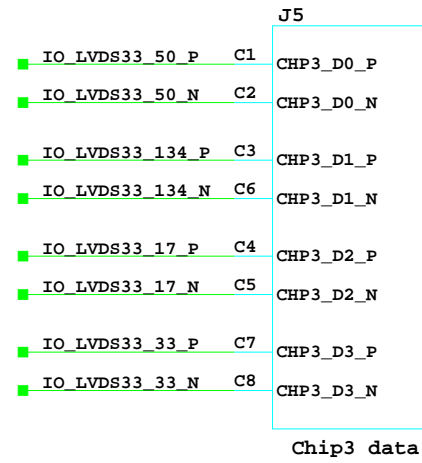
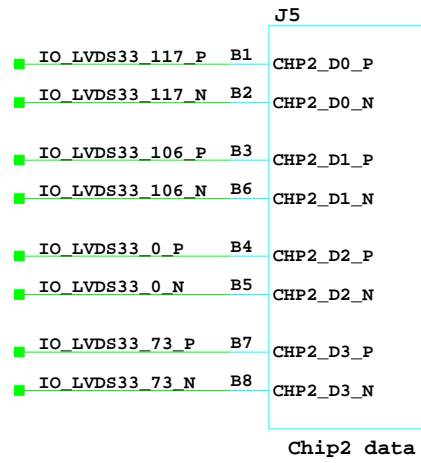
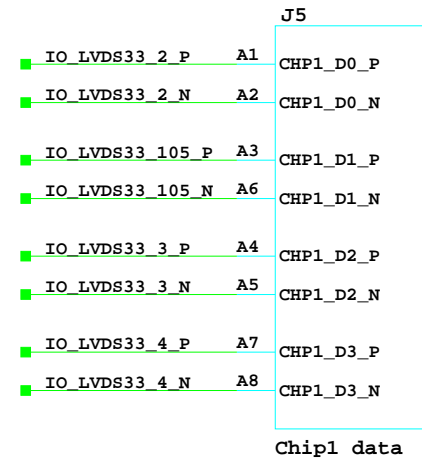
TITLE: Termination	
500 Pin Connetcor, SMD, Top layer	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 3 of 11	Drawn By Shiming Yang



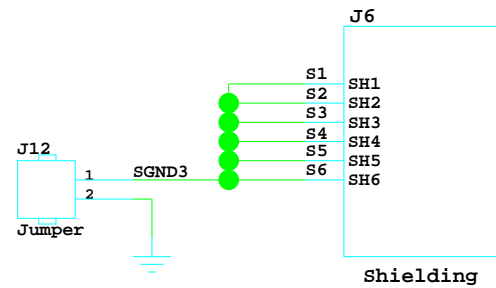
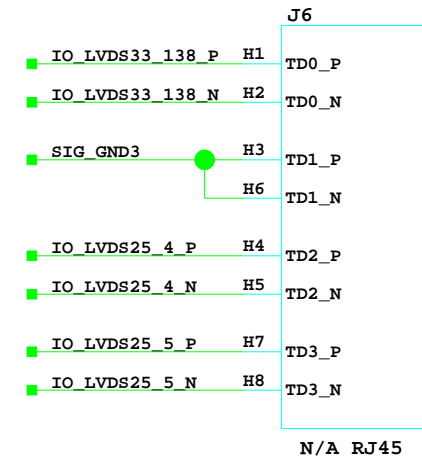
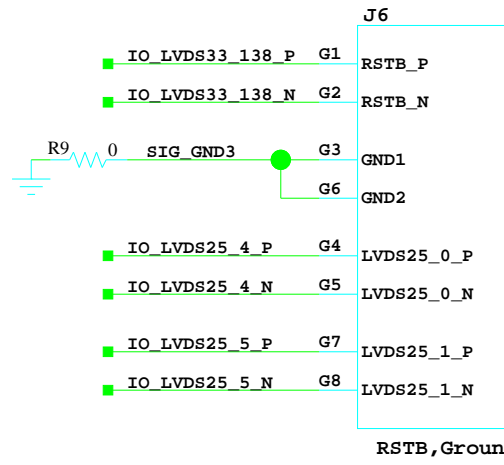
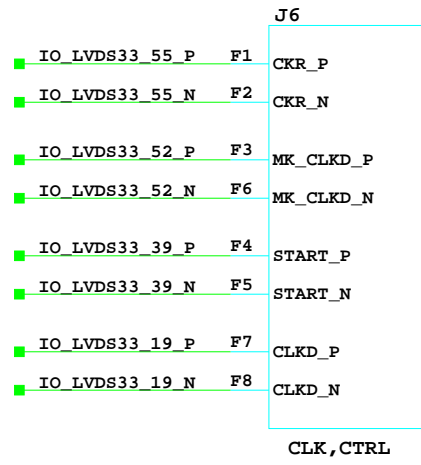
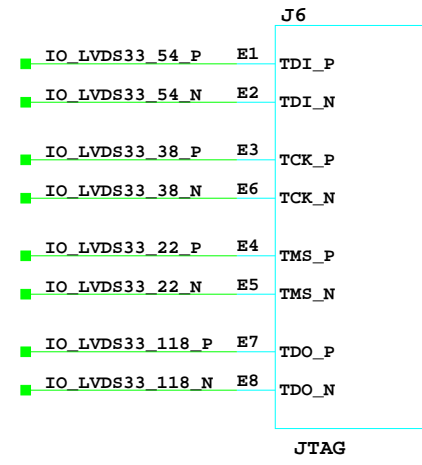
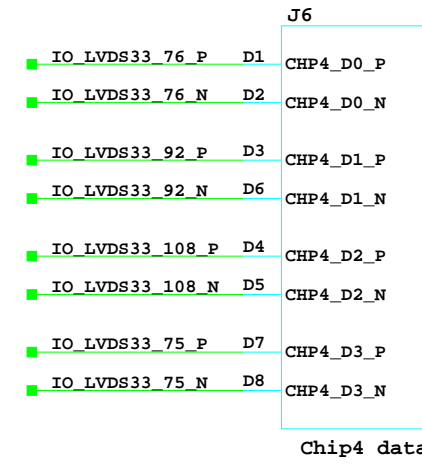
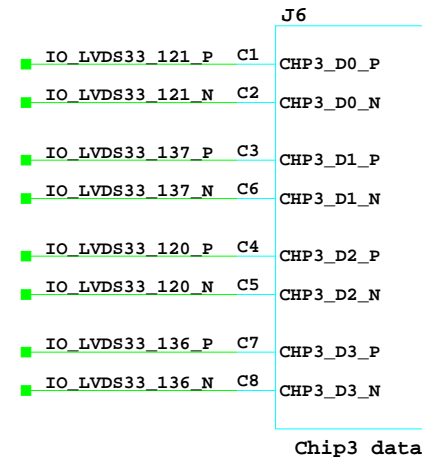
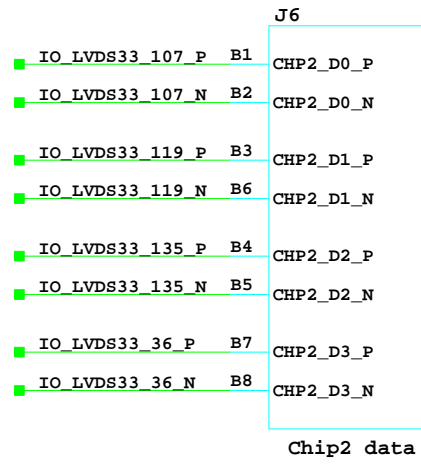
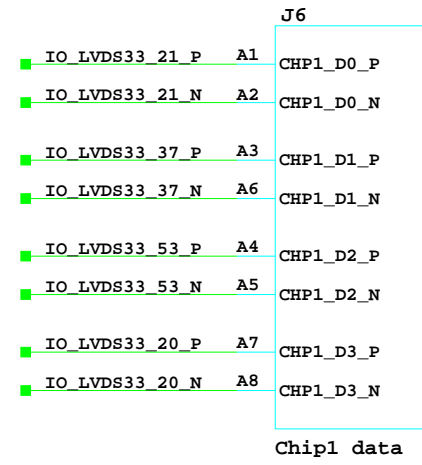
TITLE: Termination SFP Cage	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 4 of 11	Drawn By Shiming Yang



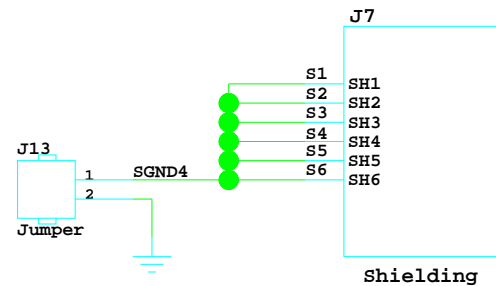
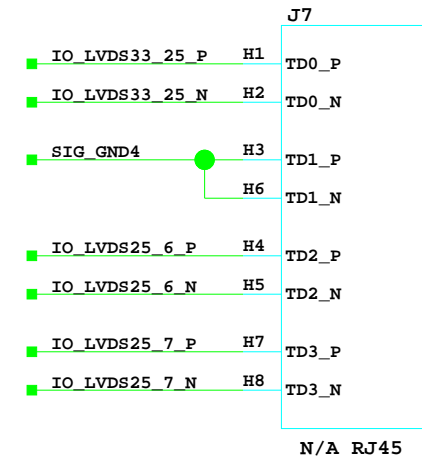
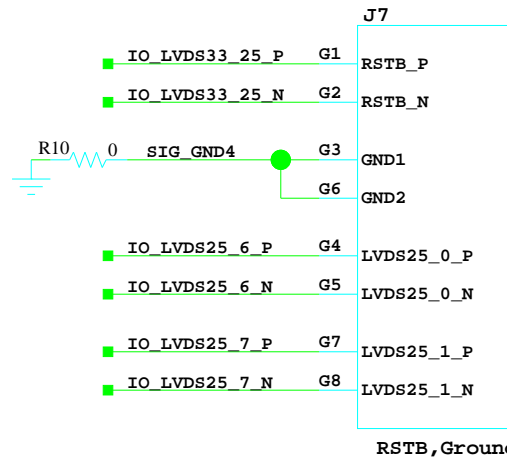
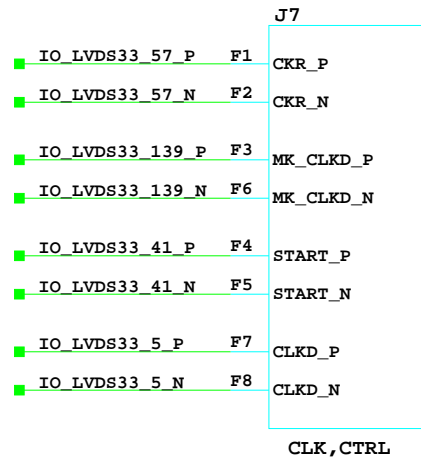
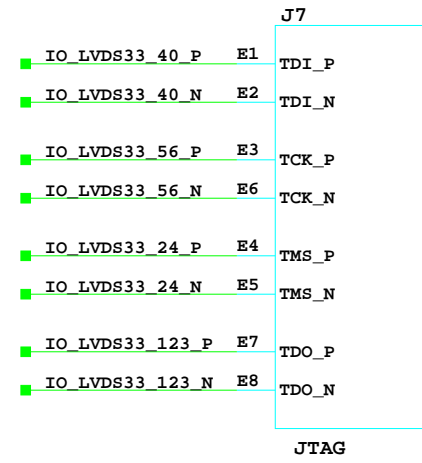
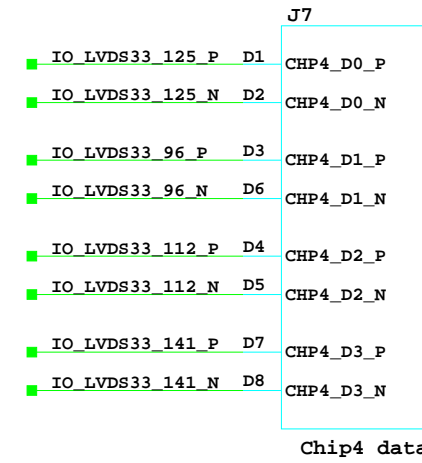
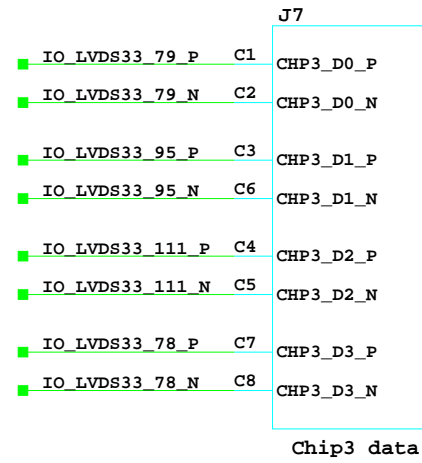
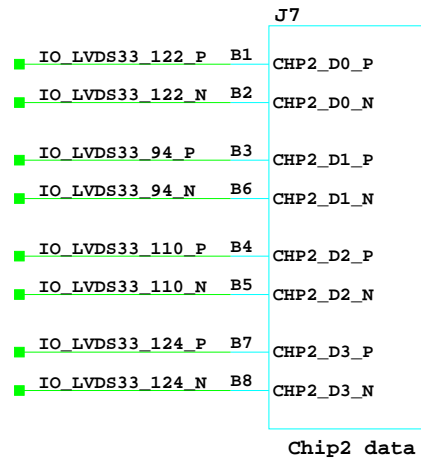
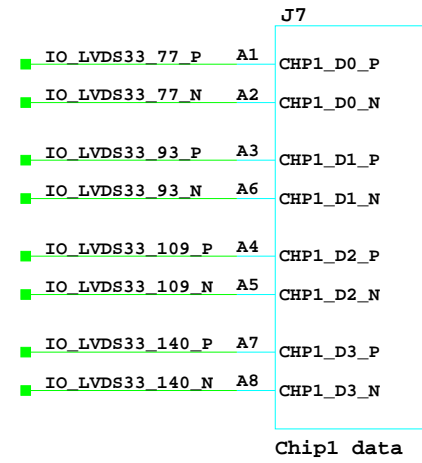
TITLE: Termination	
RJ45 connections for Layer1	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 5 of 11	Drawn By Shiming Yang



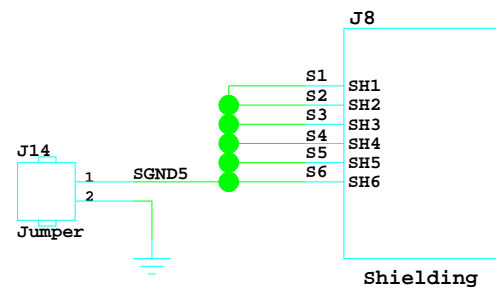
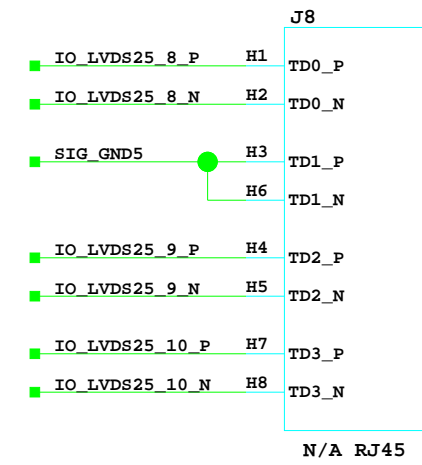
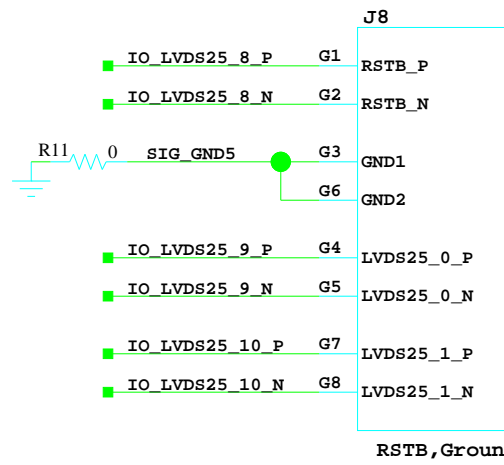
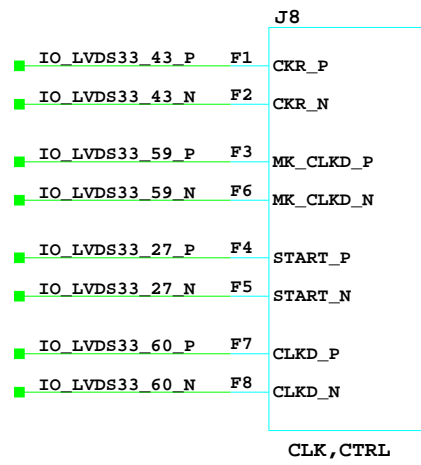
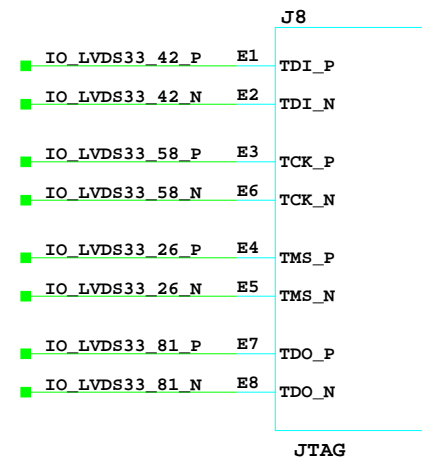
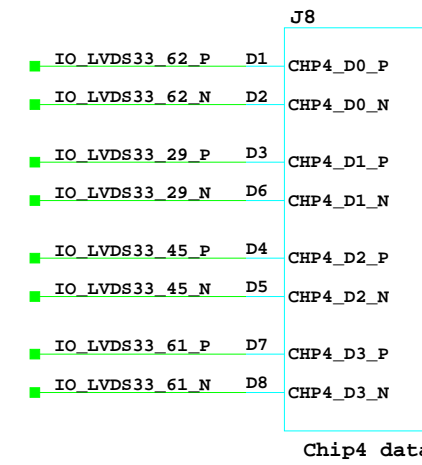
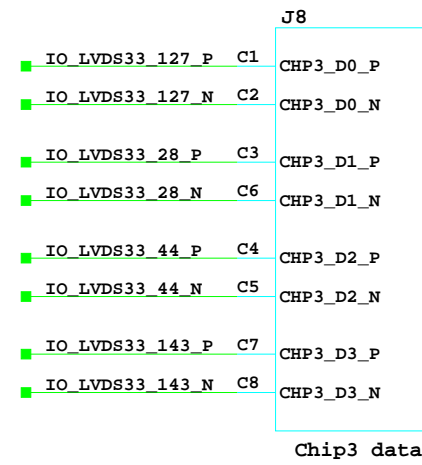
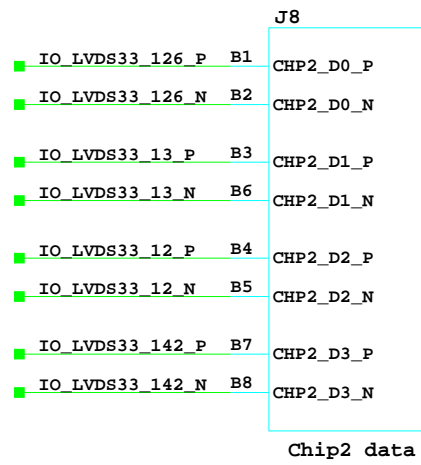
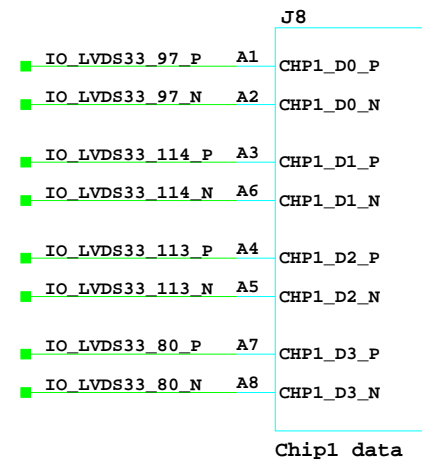
TITLE: Termination	
RJ45 connections for Layer2	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 6 of 11	Drawn By Shiming Yang



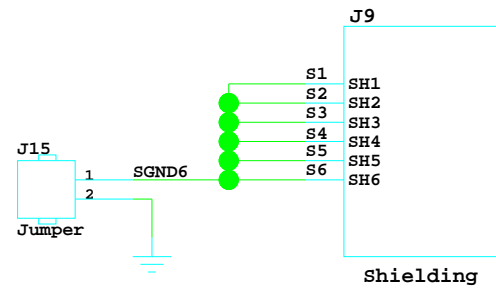
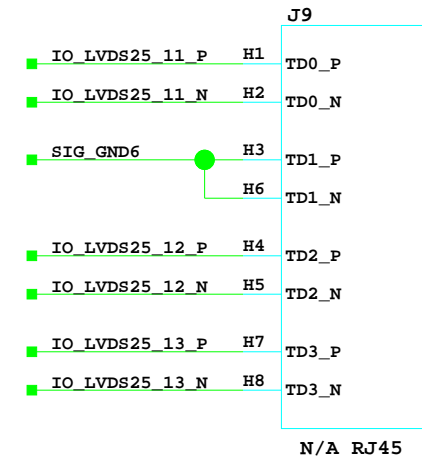
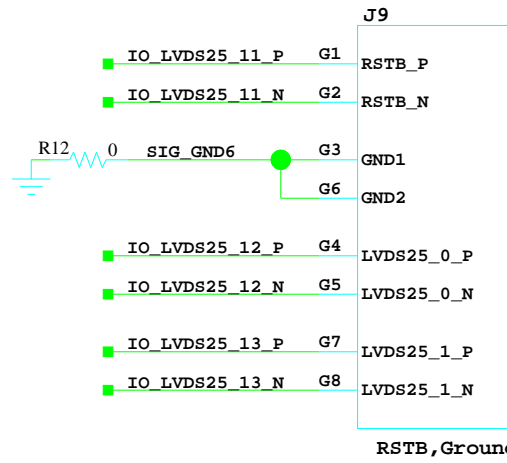
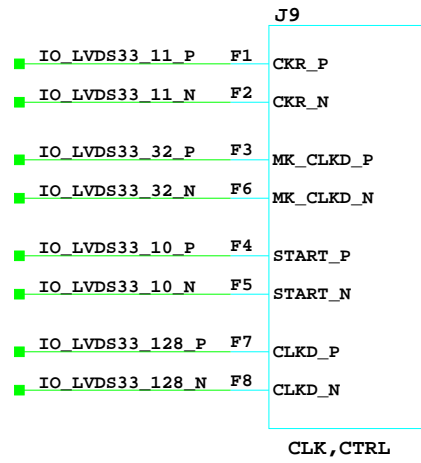
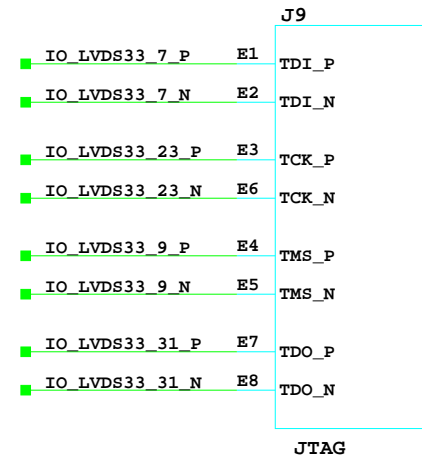
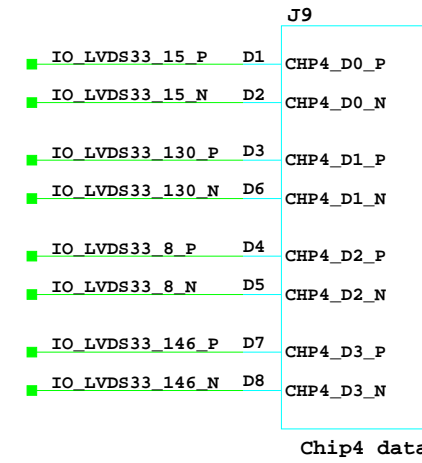
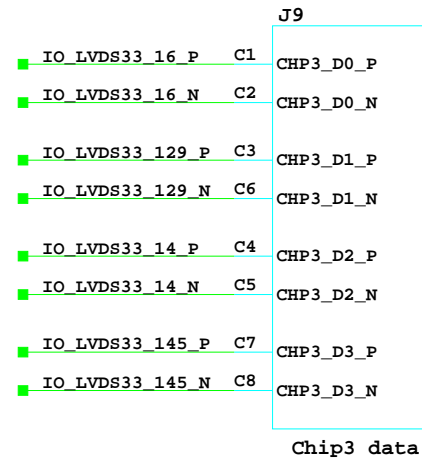
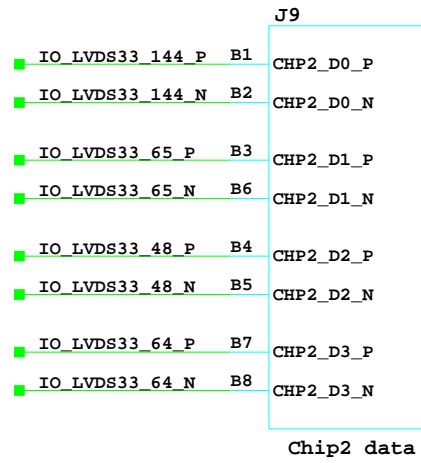
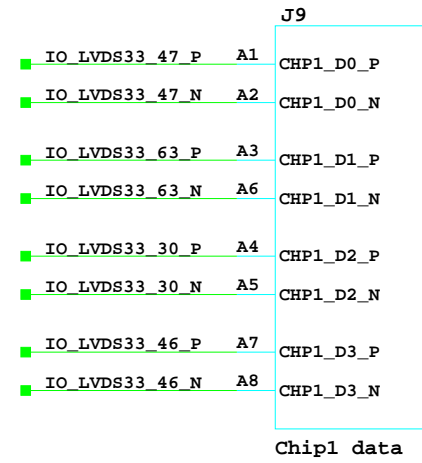
TITLE: Termination	
RJ45 connections for Layer3	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 7 of 11	Drawn By Shiming Yang



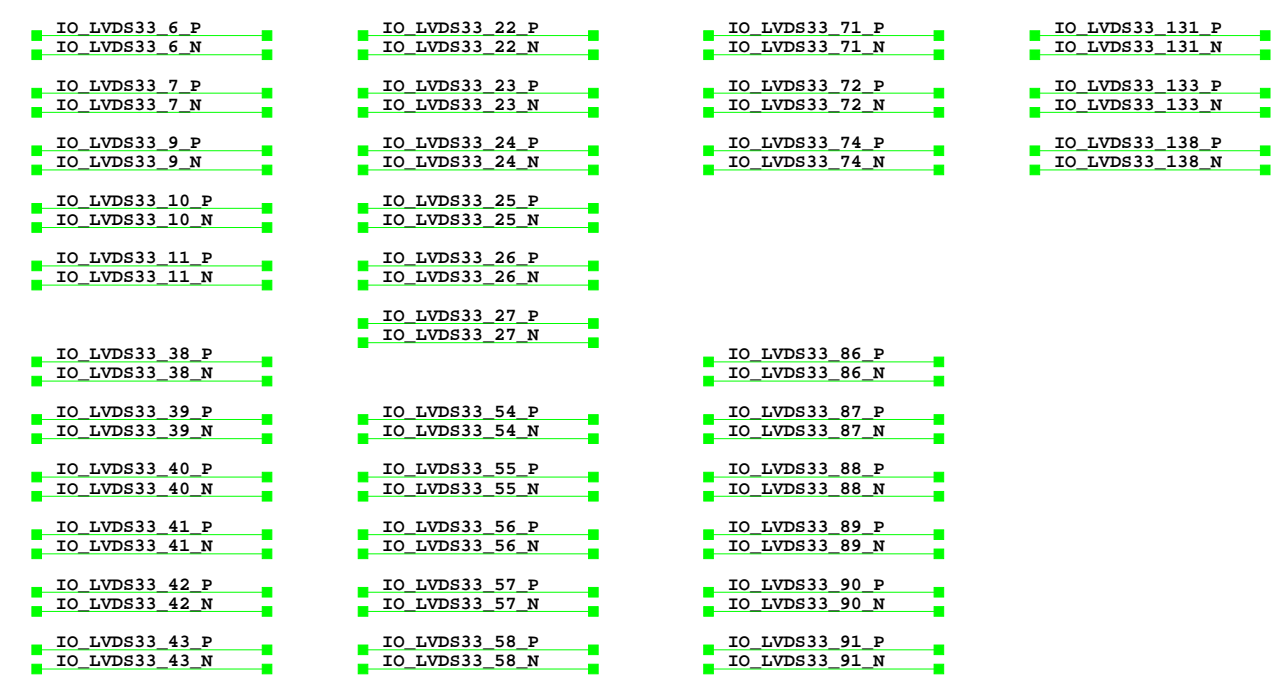
TITLE: Termination	
RJ45 connections for Layer4	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 8 of 11	Drawn By Shiming Yang



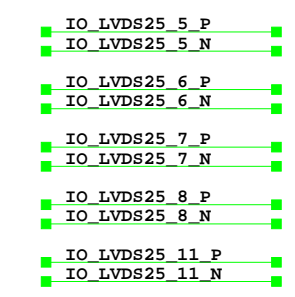
TITLE: Termination	
RJ45 connections for Layer5	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 9 of 11	Drawn By Shiming Yang



TITLE: Termination	
RJ45 connections for Layer6	
Date: 30-03-2011_16:34	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 10 of 11	Drawn By Shiming Yang



LVDS33 Signals



LVDS25 Signals

LVDS Signals on the Termination board that can be used both as input and output.
They are used as:

- TDI | TCK | TMS
- CKR | START | RSTB

TITLE: Termination	
Output LVDS Signals	
Date: 30-03-2011_16:35	DWG: Adapter/Termination Board
Sheet Size: B	REV: 1.0
Sheet 11 of 11	Drawn By Shiming Yang